



45V NPN MEDIUM-POWER TRANSISTOR IN PowerDI3333-8

Features

- BVcEo > 45V
- Small Form Factor Thermally Efficient Package Enables Higher Density End Products
- Ic = 3A High Continuous Current
- High Gain hFE > 400 @ 1A
- Low Saturation Voltage V_{CE(sat)} < 300mV @ 1A
- Rated to +175°C Ideal for High-Temperature Environment
- Wettable Flank for Improved Optical Inspection
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The DXTN07045DFGQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Package: PowerDI[®]3333-8
- Package Material: Molded Plastic. "Green" Molding Compound UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Solderable per MIL-STD-202, Method 208 (23)
- Weight: 0.03 grams (Approximate)

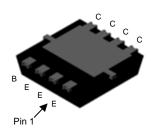
Applications

- Load switches
- Linear regulators
- MOSFET or IGBT gate driving
- Battery charging

PowerDI3333-8/SWP (Type UX)

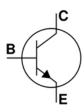


Top View



Bottom View

Equivalent Circuit



Device Symbol

Ordering Information (Note 4)

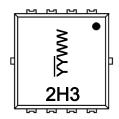
Orderable Part Number	Package	Marking	Reel Size (inches)	Tape Width (mm)	Packing	
Orderable Part Nulliber			Reel Size (Iliches)		Qty.	Carrier
DXTN07045DFGQ-7	PowerDI3333-8/SWP (Type UX)	2H3	7	12	2000	Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information

PowerDI3333-8/SWP (Type UX)



2H3 = Product Type Marking Code

YYWW = Date Code Marking

YY = Last Two Digits of Year (ex: 25 = 2025)

WW = Week Code (01 to 53)



Absolute Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	Vсво	45	V
Collector-Emitter Voltage	VCEO	45	V
Emitter-Base Voltage	VEBO	7	V
Continuous Collector Current	Ic	3	Α
Peak Pulse Current	Ісм	6	Α

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
	(Note 5)		1	W
Power Dissipation	(Note 6)	PD	2.3	W
	(Note 7)		3.4	W
	(Note 5)		140	°C/W
Thermal Resistance, Junction to Ambient	(Note 6)	Reja	65	°C/W
	(Note 7)		44	°C/W
Thermal Resistance, Junction to Leads (Note 8)		Rejl	8.5	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +175	°C	

ESD Ratings (Note 9)

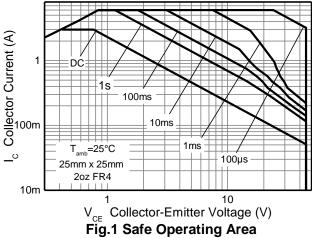
Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge—Human Body Model	ESD HBM	4000	V	3A
Electrostatic Discharge—Machine Model	ESD MM	400	V	С

Notes:

- 5. For a device mounted with the collector tab on MRP FR4-PCB; device is measured under still air conditions whilst operating in a steady state.
- 6. Same as Note 5, except the device is mounted on 25mm × 25mm 2oz copper. 7. Same as Note 5, except the device is mounted on 50mm × 50mm 2oz copper.
- 8. Thermal resistance from junction to solder-point (at the collector tab).
- 9. Refer to JEDEC specification JESD22-A114 and JESD22-A115.



Thermal Characteristics and Derating Information



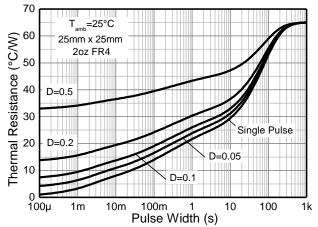


Fig.2 Transient Thermal Impedance

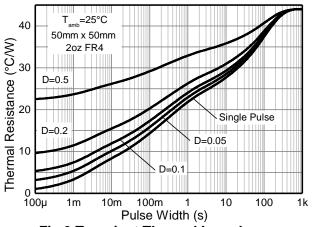


Fig.3 Transient Thermal Impedance

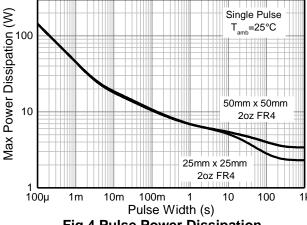


Fig.4 Pulse Power Dissipation

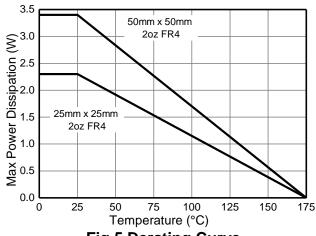


Fig.5 Derating Curve



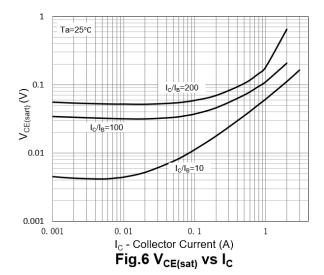
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

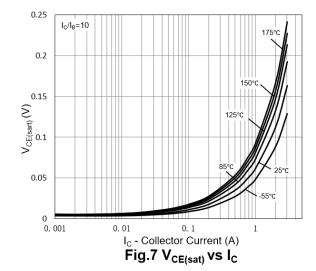
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	BV _{CBO}	50	150	_	V	I _C = 100μA
Collector-Emitter Breakdown Voltage (Note 10)	BV_{CEO}	45	59	_	V	I _C = 10mA
Emitter-Base Breakdown Voltage	BV_{EBO}	7	8	_	V	$I_E = 100\mu A$
Collector Dage Cutoff Current	1	_	1	20	nA	V _{CB} = 45V
Collector-Base Cutoff Current	I _{CBO}	_	0.02	10	μΑ	V _{CB} = 45V, T _A = +125°C
Emitter Cutoff Current	I _{EBO}	_	1	20	nA	$V_{EB} = 6V$
		500	760	_	_	I _C = 0.1A, V _{CE} = 2V
DC Comment Cain (Nata 40)	L-	400	700	_	_	I _C = 1A, V _{CE} = 2V
DC Current Gain (Note 10)	h _{FE}	150	490	_	_	$I_C = 2A$, $V_{CE} = 2V$
		50	220	_	_	$I_C = 3A$, $V_{CE} = 2V$
Collector Emitter Seturation Voltage (Note 10)	W	_	59	100	mV	$I_C = 0.1A, I_B = 0.5mA$
Collector-Emitter Saturation Voltage (Note 10)	$V_{CE(sat)}$	_	180	300	mV	$I_C = 1A$, $I_B = 5mA$
Base-Emitter Saturation Voltage (Note 10)	$V_{BE(sat)}$	_	0.625	1	V	$I_{C} = 1A, I_{B} = 10mA$
Base-Emitter Turn-On Voltage (Note 10)	V _{BE(on)}	_	0.560	0.9	V	I _C = 1A, V _{CE} = 2V
Input Capacitance	C _{ibo}	_	200	_	pF	V _{EB} = 0.5V, f = 1MHz
Output Capacitance	C _{obo}	_	16	_	pF	V _{CB} = 10V, f = 1MHz
Current Gain-Bandwidth Product	f⊤	150	_	_	MHz	$V_{CE} = 5V, I_{C} = 50mA, f = 50MHz$
Turn-On Time	t _{on}	_	33	_	ns	$V_{CC} = 10V, I_{C} = 500mA$
Turn-Off Time	t _{off}	_	1300	_	ns	$I_{B1} = -I_{B2} = 50 \text{mA}$

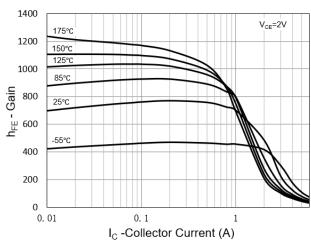
Note: 10. Measured under pulsed conditions. Pulse width \leq 300 μ s. Duty cycle \leq 2%.

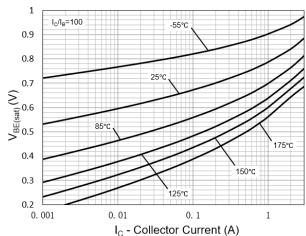


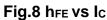
$\hline \textbf{Typical} \ \underline{\textbf{Electrical Characteristics}} \ (@T_A = +25^{\circ}C, \text{ unless otherwise specified.}) \\$



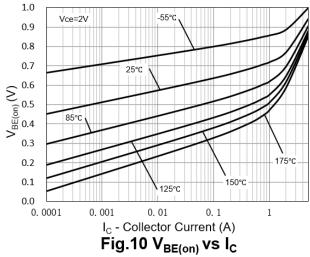








 I_{C} - Collector Current (A) Fig.9 $V_{BE(sat)}$ vs I_{C}

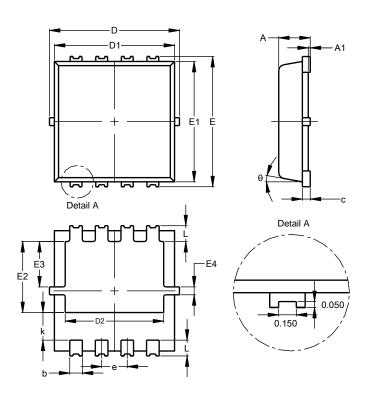




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI3333-8/SWP (Type UX)

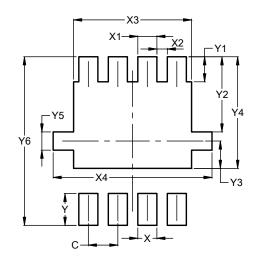


PowerDI3333-8/SWP					
(Type UX)					
Dim	Min	Max	Тур		
Α	0.75	0.85	0.80		
A1	0.00	0.05			
b	0.25	0.40	0.32		
С	0.10	0.25	0.15		
D	3.20	3.40	3.30		
D1	2.95	3.15	3.05		
D2	2.30	2.70	2.50		
E	3.20	3.40	3.30		
E1	2.95	3.15	3.05		
E2	1.60	2.00	1.80		
E3	0.95	1.35	1.15		
E4	0.10	0.30	0.20		
е	_	_	0.65		
k	0.50	0.90	0.70		
L	0.30	0.50	0.40		
θ	0°	12°	10°		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI3333-8/SWP (Type UX)



Dimensions	Value (in mm)
C	0.650
Х	0.420
X1	0.420
X2	0.230
Х3	2.600
X4	3.500
Υ	0.700
Y1	0.550
Y2	1.650
Y3	0.600
Y4	2.450
Y5	0.400
Y6	3.700

Note: 11. Side wall tin plated package for wettable flanks in AOI.



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