

# P-Channel 20 V (D-S) MOSFET

PRODUCT SUMMARY						
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$ (Max.)	I <sub>D</sub> (A)	Q <sub>g</sub> (Typ.)			
- 20	$0.006$ at $V_{GS} = -4.5 \text{ V}$	- 50 <sup>a</sup>	00 0			
20	0.007 at V <sub>GS</sub> = - 2.5 V	- 37 <sup>a</sup>	93 nC			

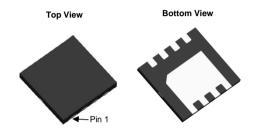
#### **FEATURES**

- TrenchFET® Power MOSFET
- Thermally Enhanced DFN3X3 Package
  - Small Footprint Area
  - Low On-Resistance



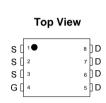


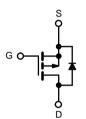
#### DFN 3x3 EP



#### **APPLICATIONS**

 Load Switch, PA Switch, and Battery Switch for Portable Devices





P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25 °C, unless otherwise noted)						
Parameter		Symbol	Limit	Unit		
Drain-Source Voltage		V <sub>DS</sub>	- 20	V		
Gate-Source Voltage		$V_{GS}$	± 12	V		
	T <sub>C</sub> = 25 °C		- 50 <sup>a</sup>			
Continuous Drain Current (T <sub>.I</sub> = 150 °C)	T <sub>C</sub> = 70 °C	la	- 39 <sup>a</sup>			
Continuous Diain Current (1) = 130 C)	T <sub>A</sub> = 25 °C	I <sub>D</sub>	- 31 <sup>b, c</sup>			
	T <sub>A</sub> = 70 °C		- 25 <sup>b, c</sup>	A		
Pulsed Drain Current (t = 300 μs)	I <sub>DM</sub>	- 200				
Continuous Source-Drain Diode Current	T <sub>C</sub> = 25 °C	I <sub>S</sub>	- 50 <sup>a</sup>			
Continuous Godice-Diam Diode Current	T <sub>A</sub> = 25 °C	'S	- 29 <sup>b, c</sup>			
	T <sub>C</sub> = 25 °C		89			
Maximum Power Dissipation	T <sub>C</sub> = 70 °C	P <sub>D</sub>	33	W		
Maximum Fower Dissipation	T <sub>A</sub> = 25 °C	' D	6.5 <sup>b, c</sup>			
	T <sub>A</sub> = 70 °C		4.2 <sup>b, c</sup>	7		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C		
Soldering Recommendations (Peak Temperatur	· ·	260				

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>b, f</sup>	t ≤ 5 s	$R_{thJA}$	18	26	°C/W
Maximum Junction-to-Case (Drain)	Steady State	$R_{thJC}$	1.3	1.5	C/VV

#### Notes:

- a. Package limited.
- b. Surface mounted on 1" x 1" FR4 board.
- c. t = 5 s
- d. See solder profile The DFN3X3 is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.
- e. Rework conditions: manual soldering with a soldering iron is not recommended for leadless components.
- f. Maximum under steady state conditions is 80 °C/W.

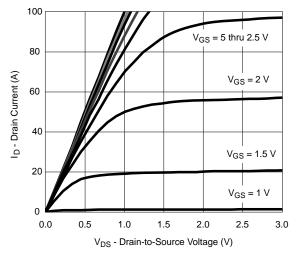


Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{GS} = 0 \text{ V}, I_{D} = -250 \mu\text{A}$	- 20			V	
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I - 250 uA		- 11		mV/°C	
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_{J}$	I <sub>D</sub> = - 250 μA		2.7			
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 0.8		- 2	V	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$			± 100	nA	
Zana Cata Valta da Busin Comunit	1	V <sub>DS</sub> = - 12 V, V <sub>GS</sub> = 0 V			- 1	μΑ	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = - 12 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			- 10		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \le -5 \text{ V}, V_{GS} = -4.5 \text{ V}$	- 20			Α	
		V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 5.3 A		0.006	0.007	Ω	
	D	V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 8.1 A		0.0065	0.008		
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 2.5 V, I <sub>D</sub> = - 5.3 A		0.007	0.008		
		V <sub>GS</sub> = - 2.5 V, I <sub>D</sub> = - 6 A		0.0054	0.009		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 10 V, I <sub>D</sub> = - 18.5 A		94		S	
Dynamic <sup>b</sup>	'			1			
Input Capacitance	C <sub>iss</sub>			4800			
Output Capacitance	C <sub>oss</sub>	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		850		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>			590		1	
Total Cata Channa	0	V <sub>DS</sub> = -6 V, V <sub>GS</sub> = -8 V, I <sub>D</sub> = -10 A		58	97	nC	
Total Gate Charge	$Q_g$	Q <sub>g</sub> 53 5 3 4 4 5 5 5 5 5 5 5 5 5 5 5 5 5 5		33	65		
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = -6 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -10 \text{ A}$		7			
Gate-Drain Charge	$Q_{gd}$			15.5			
Gate Resistance	$R_g$	f = 1 MHz		5		Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			20	30		
Rise Time	t <sub>r</sub>	$V_{DD}$ = - 6 V, $R_L$ = 0.75 $\Omega$		40	60		
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D\cong$ - 8 A, $V_{GEN}$ = - 4.5 V, $R_g$ = 1 $\Omega$		65	100	]	
Fall Time	t <sub>f</sub>			40	60	no	
Turn-On Delay Time	t <sub>d(on)</sub>			10	15	ns	
Rise Time	t <sub>r</sub>	$V_{DD}$ = - 6 V, $R_L$ = 0.75 $\Omega$		12	20		
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D\cong$ - 8 A, $V_{GEN}$ = - 8 V, $R_g$ = 1 $\Omega$		70	105		
Fall Time	t <sub>f</sub>			40	60		
<b>Drain-Source Body Diode Characterist</b>	ics						
Continuous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C			- 50	Α	
Pulse Diode Forward Current	I <sub>SM</sub>	-			200		
Body Diode Voltage	$V_{SD}$	$I_S = -8 \text{ A}, V_{GS} = 0 \text{ V}$		- 0.57	- 1.1	V	
Body Diode Reverse Recovery Time t <sub>rr</sub>				40	60	ns	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	$Q_{rr}$ $I_F = -8 \text{ A, di/dt} = 100 \text{ A/µs, T}_J = 25 °C$		20	30	nC	
Reverse Recovery Fall Time	t <sub>a</sub>	57., 3., 4. – 1507 v po, 13 – 20 O		14		ns	
Reverse Recovery Rise Time	t <sub>b</sub>			26			

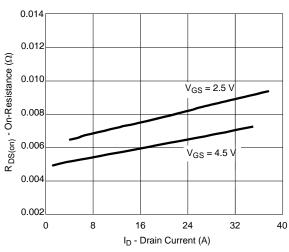
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %. b. Guaranteed by design, not subject to production testing.

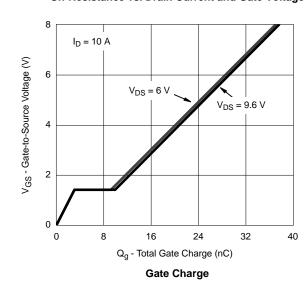


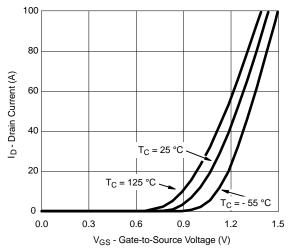


#### **Output Characteristics**

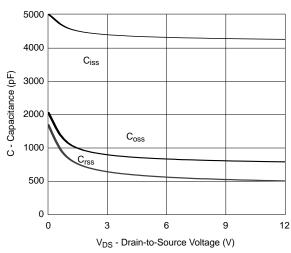


On-Resistance vs. Drain Current and Gate Voltage

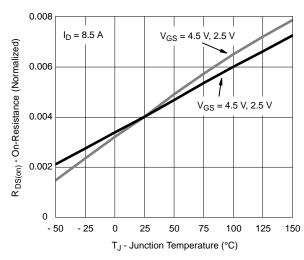




**Transfer Characteristics** 

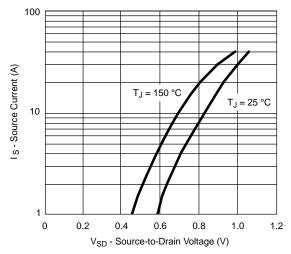


Capacitance

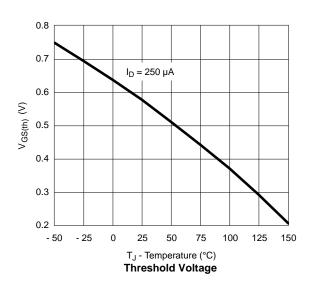


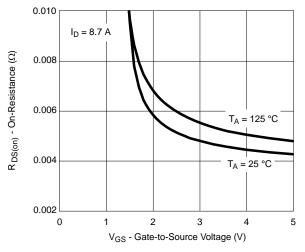
On-Resistance vs. Junction Temperature



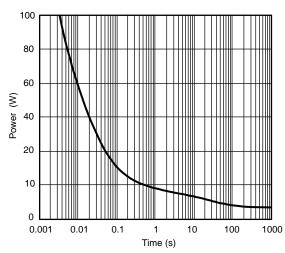


#### Soure-Drain Diode Forward Voltage

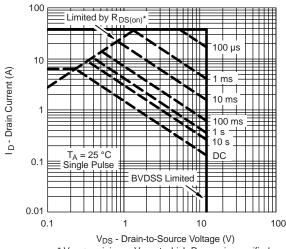




On-Resistance vs. Gate-to-Source Voltage



Single Pulse Power, Junction-to-Ambient

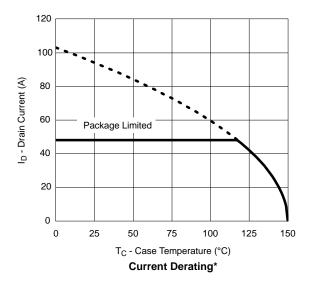


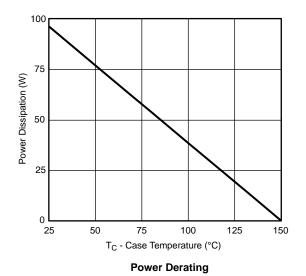
\*  $V_{GS}$  > minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified

Safe Operating Area, Junction-to-Ambient



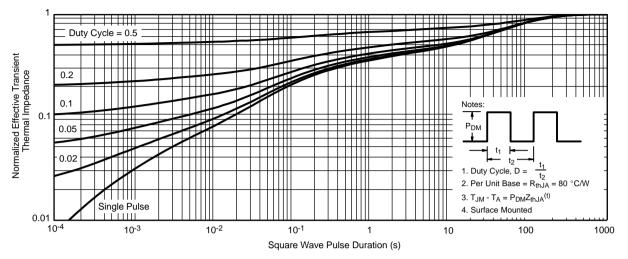




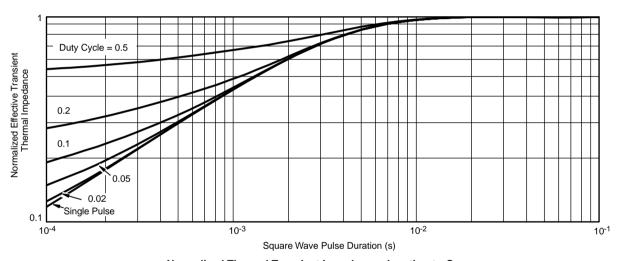


<sup>\*</sup> The power dissipation  $P_D$  is based on  $T_{J(max)}$  = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



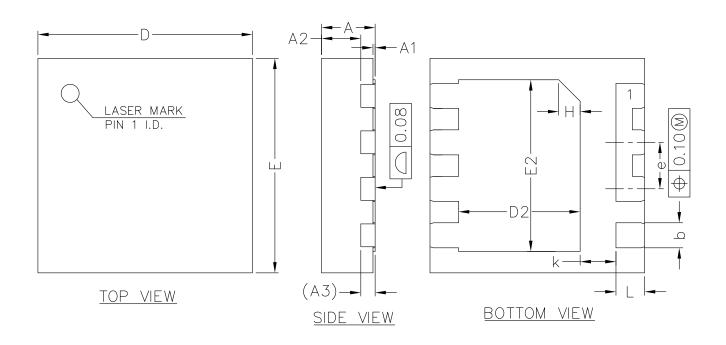


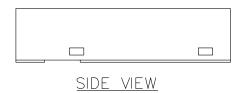
Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case







COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	NOM	MAX	
Α	0.70	0.75	0.80	
A1	0.00	0.02	0.05	
A2	0.50	0.55	0.60	
А3	0.20REF			
b	0.30	0.35	0.40	
D	2.90	3.00	3.10	
E	2.90	3.00	3.10	
D2	1.60	1.70	1.80	
E2	2.30	2.40	2.50	
е	0.55	0.65	0.75	
K	0.40	0.50	0.60	
L	0.35	0.40	0.45	





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