

N-Channel 100-V (D-S) MOSFET

PRODUCT	SUMMARY	
V _{(BR)DSS} (V)	r _{DS(on)} (Ω)	I _D (A)
100	0.037 at V _{GS} = 10 V	65 ^a

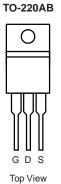
FEATURES

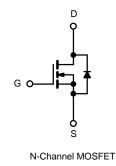
- TrenchFET[®] Power MOSFET
- 175 °C Junction Temperature
- Low Thermal Resistance Package
- 100 % Rg Tested

APPLICATIONS

• Isolated DC/DC Converters







ABSOLUTE MAXIMUM RATINGS Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V _{DS}	100	
Gate-Source Voltage		V _{GS}	± 20	- V
Continuous Drain Current ($T_1 = 175 ^{\circ}C$)	T _C = 25 °C		65 ^a	
Continuous Drain Current $(T_J = 175^{\circ}C)$	T _C = 125 °C	I _D	37 ^a	A
Pulsed Drain Current		I _{DM}	195	
Avalanche Current	L = 0.1 mH	I _{AS}	35	
Single Pulse Avalanche Energy ^b	L = 0.1 mm	E _{AS}	61	mJ
Mauianum Daura Diasia stia sh	T _C = 25 °C	- P _D -	375 ^c	w
Maximum Power Dissipation ^b	T _A = 25 °C ^d	ΓD Γ	3.75	v
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 175	°C

THERMAL RESISTANCE RA	TINGS			
Parameter		Symbol	Limit	Unit
Junction-to-Ambient	PCB Mount (TO-263) ^d	R _{thJA}	40	°C/W
Junction-to-Case (Drain)		R _{thJC}	0.4	0/11

Notes:

a. Package limited.

b. Duty cycle \leq 1 %.

c. See SOA curve for voltage derating.

d. When Mounted on 1" square PCB (FR-4 material).

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static	•						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{DS} = 0 V, I_{D} = 250 \mu A$	100			V	
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	1		4	v	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
		$V_{DS} = 100 \text{ V}, V_{GS} = 0 \text{ V}$			1	μA	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 100 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 125 \text{ °C}$			50		
		$V_{DS} = 100 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 175 ^{\circ}\text{C}$			250	1	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, \text{ V}_{GS} = 10 \text{ V}$	120			А	
		V _{GS} = 10 V, I _D = 30 A		0.037	0.041		
Drain-Source On-State Resistance ^a	r _{DS(on)}	V_{GS} = 10 V, I _D = 30 A, T _J = 125 °C		0.063	0.070	Ω	
		V_{GS} = 10 V, I _D = 30 A, T _J = 175 °C		0.084	0.093		
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 30 A	25			S	
Dynamic ^b							
Input Capacitance	C _{iss}			5100			
Output Capacitance	C _{oss}	V_{GS} = 0 V, V_{DS} = 25 V, f = 1 MHz	_S = 25 V, f = 1 MHz 480		pF		
Reverse Transfer Capacitance	C _{rss}			210		-	
Total Gate Charge ^c	Qg	Q_{gs} V _{DS} = 100 V, V _{GS} = 10 V, I _D = 65 A	90	130	nC		
Gate-Source Charge ^c	Q _{gs}		23				
Gate-Drain Charge ^c	Q _{gd}			34			
Gate Resistance	R _g		0.5	1.7	3.3	Ω	
Turn-On Delay Time ^c	t _{d(on)}			24	35		
Rise Time ^c	t _r	V_{DD} = 100 V, R _L = 1.5 Ω		220	330		
Turn-Off Delay Time ^c	t _{d(off)}	$\text{I}_\text{D}\cong$ 65 A, V_GEN = 10 V, R_g = 2.5 Ω		45	70	ns	
Fall Time ^c	t _f			200	300		
Source-Drain Diode Ratings and Cha	aracteristics 7	_C = 25 °C ^b		·	-		
Continuous Current	۱ _S				65	A	
Pulsed Current	I _{SM}			1	195		
Forward Voltage ^a	V _{SD}	$I_{F} = 65 \text{ A}, \text{ V}_{GS} = 0 \text{ V}$		1.0	1.5	V	
Reverse Recovery Time	t _{rr}			130	200	ns	
Peak Reverse Recovery Current	I _{RM(REC)}	I _F = 50 A, di/dt = 100 A/μs		8	12	А	
Reverse Recovery Charge	Q _{rr}			0.52	1.2	uС	

Notes:

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



I_D - Drain Current (A)

g _{fs} - Transconductance (S)

C - Capacitance (pF)

 $V_{GS} = 10$ thru 7 V

V_{DS} - Drain-to-Source Voltage (V) Output Characteristics

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6 V

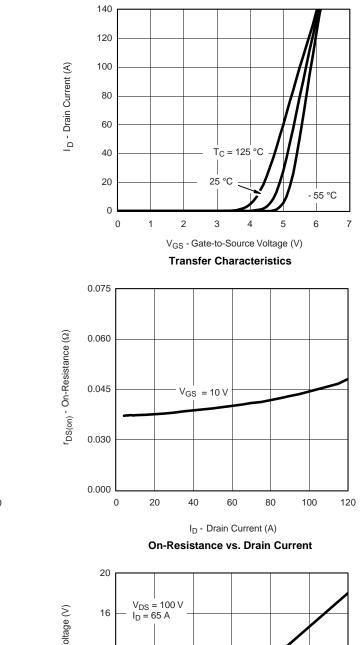
5 V

4 V

T_C = - 55 °C

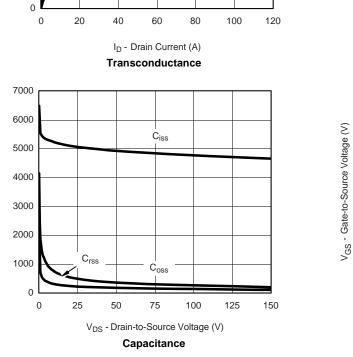
25 °C

125 °C



Q_q - Total Gate Charge (nC)

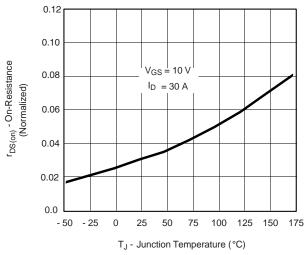
Gate Charge

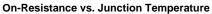


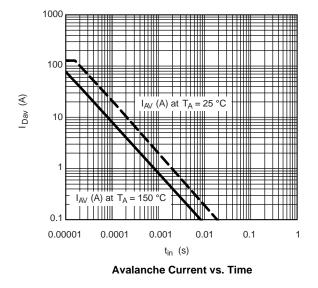


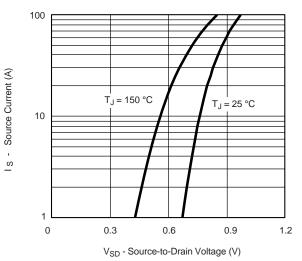
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

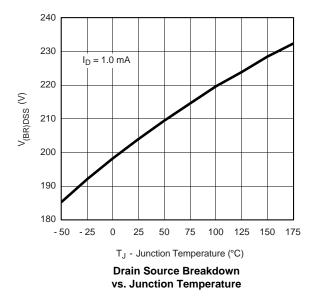








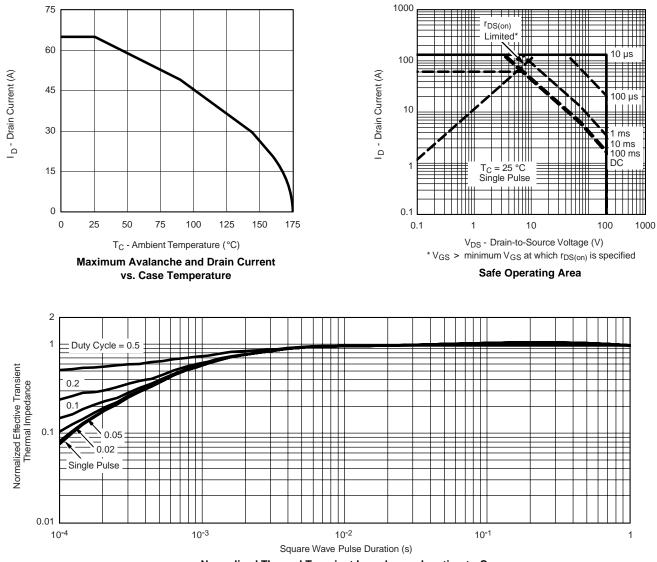
Source-Drain Diode Forward Voltage





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THERMAL RATINGS

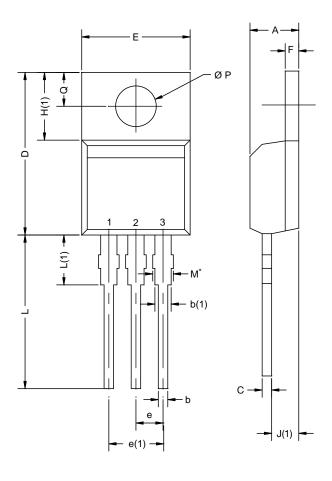


Normalized Thermal Transient Impedance, Junction-to-Case



Package Information www.din-tek.jp

TO-220AB



MIN.	MAX.	MIN.	MAX.
4.25	4.65	0.167	0.183
0.69	1.01	0.027	0.040
1.20	1.73	0.047	0.068
0.36	0.61	0.014	0.024
14.85	15.49	0.585	0.610
10.04	10.51	0.395	0.414
2.41	2.67	0.095	0.105
4.88	5.28	0.192	0.208
1.14	1.40	0.045	0.055
6.09	6.48	0.240	0.255
2.41	2.92	0.095	0.115
13.35	14.02	0.526	0.552
3.32	3.82	0.131	0.150
3.54	3.94	0.139	0.155
2.60	3.00	0.102	0.118
	4.25 0.69 1.20 0.36 14.85 10.04 2.41 4.88 1.14 6.09 2.41 13.35 3.32 3.54 2.60	4.25 4.65 0.69 1.01 1.20 1.73 0.36 0.61 14.85 15.49 10.04 10.51 2.41 2.67 4.88 5.28 1.14 1.40 6.09 6.48 2.41 2.92 13.35 14.02 3.32 3.82 3.54 3.94	4.254.650.1670.691.010.0271.201.730.0470.360.610.01414.8515.490.58510.0410.510.3952.412.670.0954.885.280.1921.141.400.0456.096.480.2402.412.920.09513.3514.020.5263.323.820.1313.543.940.1392.603.000.102

Notes

* M = 1.32 mm to 1.62 mm (dimension including protrusion) Heatsink hole for HVM



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