### Transistors

# -200mA / -30V Low VCE (sat) Digital transistors (with built-in resistors) **DTB743EE / DTB743EM**

### Applications

Inverter, Interface, Driver

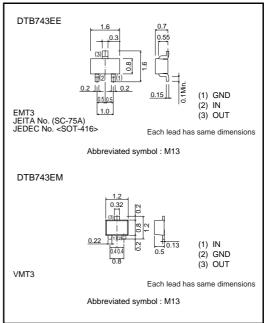
### Feature

- 1) VCE (sat) is lower than the conventional products.
- 2) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- 3) The bias resistors consist of thin-film resistors with complete isolation to allow positive biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- 4) Only the on / off conditions need to be set for operation, making the device design easy.

### Structure

PNP epitaxial plannar silicon transistor (Resistor built-in type)

### External dimensions (Unit : mm)



## Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits		Unit
Farameter	Symbol	DTB743EE	DTB743EM	Unit
Supply voltage	Vcc		30	V
Input voltage	Vin	-20 to +10 V		V
Collector current *1	IC (max)	2	00	mA
Power dissipation *2	PD	1:	50	mW
Junction temperature	Tj	1:	50	ື່
Storage temperature	Tstg	–55 to	o +150	ູນ

#### Packaging specifications

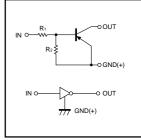
	Package	EMT3	VMT3
Packaging type		Taping	Taping
	Code	TL	T2L
Part No.	Basic ordering unit (pieces)	3000	8000
DTB743EE		0	-
DTB743EM		_	0

\*1 Characteristics of built-in transistor.
\*2 Each terminal mounted on a recommended land.

### Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Input voltage	VI(off)	-	-	-0.5	V	Vcc=-5V, Io=-100µA
	VI(on)	-2.5	-	-		Vo=-0.3V, Io=-20mA
Output voltage	VO(on)	-	-70	-300	mV	lo/l=-50mA / -2.5mA
Input current	h	-	-	-1.4	mA	VI= -5V
Output current	IO(off)	-	-	-500	nA	Vcc=-30V, VI=0V
DC current gain	Gi	115	-	-	-	Vo=-2V, Io=-100mA
Transition frequency *	fτ	-	260	-	MHz	Vce=-10V, Ie=5mA, f=100MHz
Input resistance	R1	3.29	4.7	6.11	kΩ	-
Resistance ratio	R2/R1	0.8	1.0	1.2	-	_

### Equivalent circuit



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