



DT1446-04SO

### **Product Summary**

IPP (max)	C <sub>T (typ)</sub>
4.7A	0.55pF
	IPP (max) 4.7A

### Description

The DT1446-04SO is a high performance device suitable for protecting four high speed I/Os and one  $V_{CC}$ . These devices are assembled in SOT26 package. They have high ESD surge capability and low capacitance.

# Applications

Typically Used for High Speed Ports such as:

- USB 2.0
- IEEE1394
- HDMI
- Laptop and Personal Computers
- Flat Panel Displays
- Video Graphics Displays
- SIM Ports

### 4 CHANNEL LOW CAPACITANCE TVS DIODE ARRAY

#### Features

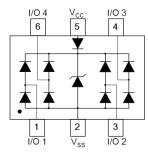
- IEC 61000-4-2 (ESD): Air ±19kV, Contact ±16kV
- Low Channel Input Capacitance of 0.55pF Max
- ESD Protection for four I/Os and one Vcc
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

## **Mechanical Data**

- Case: SOT26
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020 (Lead Free Plating). Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.016 grams (approximate)



Top View



**Device Schematic** 

## Ordering Information (Note 4)

Product	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
DT1446-04SO-7	Standard	BD5	7	8	3,000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

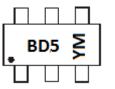
2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.



## **Marking Information**



BD5 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: A = 2013) M = Month (ex: 9 = September)

Date Code Key												
Year	20	13	20	14	20	15	20	16	20	17	20	18
Code	ŀ	Ą	E	3	(	2	[	)	E	-	I	-
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

#### Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	Conditions
Peak Pulse Current ,per IEC 61000-4-5	IPP_I/O	4.7	A	I/O to V <sub>SS</sub> , 8/20µs
Operating Voltage (DC)	V <sub>DC</sub>	6	V	V <sub>CC</sub> to V <sub>SS</sub>
ESD Protection – Contact Discharge	V <sub>ESD_I/O</sub>	±16	kV	I/O to V <sub>SS</sub> , per IEC 61000-4-2
LOD I Totection - Contact Discharge	V <sub>ESD</sub> _V <sub>CC</sub>	±30	kV	$V_{CC}$ to $V_{SS}$ , per IEC 61000-4-2
ESD Protection – Air Discharge, per IEC 61000-4-2	V <sub>ESD_I/O</sub>	±19	kV	I/O to V <sub>SS</sub> , per IEC 61000-4-2
Lob i fotection - All Discharge, per IEC 01000-4-2	$V_{ESD}V_{CC}$	±30	kV	$V_{CC}$ to $V_{SS}$ , per IEC 61000-4-2

# **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation Typical (Note 5)	PD	300	mW
Thermal Resistance, Junction to Ambient Typical (Note 5)	R <sub>θJA</sub>	417	°C/W
Operating and Storage Temperature Range	TJ, T <sub>STG</sub>	-55 to +150	°C

### Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Conditions
Reverse Working Voltage	Vrwm		—	5.0	V	V <sub>CC</sub> to V <sub>SS</sub>
Reverse Current (Note 6)	I <sub>R(VCC</sub> to V <sub>SS)</sub>	_	—	5.0	μA	$V_R = V_{RWM} = 5V, V_{CC} \text{ to } V_{SS}$
Reverse Current (Note 6)	I <sub>R(IO</sub> to V <sub>SS)</sub>	_	—	1.0	μA	$V_R = V_{RWM} = 5V$ , any I/O to $V_{SS}$
Reverse Breakdown Voltage	VBR	6.0	—	9.0	V	$I_R = 1mA$ , $V_{CC}$ to $V_{SS}$
Forward Clamping Voltage	VF	_	0.8	1.0	V	$I_F = 15 \text{mA}$ , $V_{SS}$ to $V_{CC}$
Reverse Clamping Voltage (Note 7)	Vc_I/O	—	8.5	—	V	IPP =4.7A, I/O to V <sub>SS</sub> , 8/20µS
ESD Clamping Voltage	$V_{ESD}V_{CC}$	_	10	—	V	TLP, 20A, tp = 100 ns, $V_{CC}$ to $V_{SS}$
	V <sub>ESD</sub> _I/O	_	12	—	V	TLP, 20A, tp = 100 ns, I/O to $V_{SS}$
Dynamic Resistance	R <sub>DIF</sub> _V <sub>CC</sub>	—	0.14	—	Ω	TLP, 20A, tp = 100 ns, $V_{CC}$ to $V_{SS}$
Dynamic Resistance	R <sub>DIF_I/O</sub>	_	0.3	—	Ω	TLP, 20A, tp = 100 ns, I/O to $V_{SS}$
Channel Input Capacitance	$C_{I/O}$ to $V_{SS}$	_	0.55	0.65	pF	$V_{R} = 2.5V, V_{CC} = 5V, f = 1MHz$
Channel Input Capacitance	$C_{I/O}$ to $V_{SS}$	_	0.65	—	pF	$V_R = 2.5V, V_{CC} = $ floating, f = 1MHz
Variation of Channel Input Capacitance	CI/OMAX-CI/OMIN	—	0.03	—	pF	$\label{eq:VCC} \begin{split} V_{CC} &= 5 V,  V_{SS} = 0 V,  I/O = 2.5 V,  f = 1 M H z, \\ T &= +25^\circ C  ,  C_{I/OMAX} \text{-} C_{I/OMIN} \end{split}$
Variation of Channel Input Capacitance	CI/OMAX-CI/OMIN		0.05	—	pF	$V_{CC}$ =floating , $V_{SS}$ = 0V, I/O = 2.5V, f = 1MHz, T = +25°C , $C_{I/OMAX}$ - $C_{I/OMIN}$

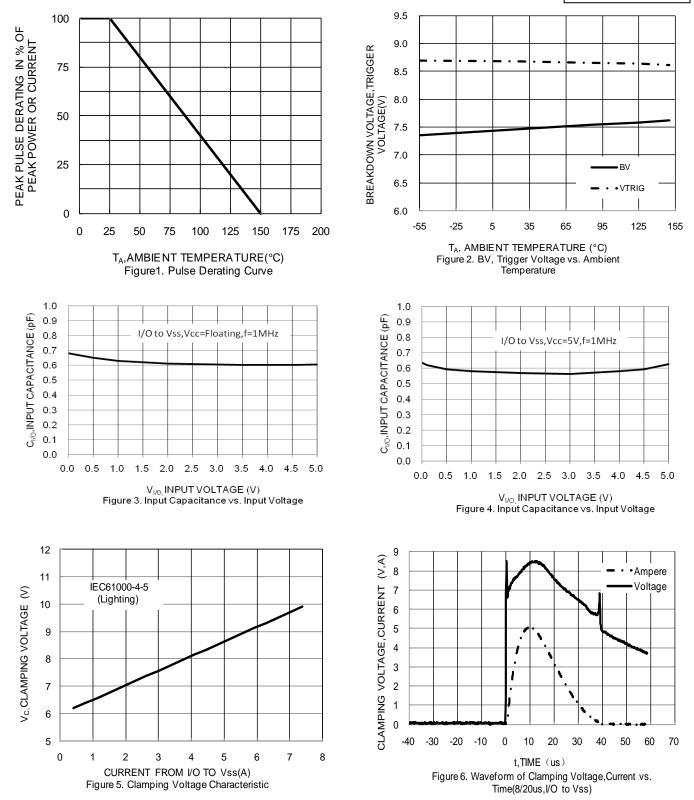
Notes: 5. Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes, Inc. suggested pad layout AP02001, which can be found on our website at http://www.diodes.com.

6. Short duration pulse test used to minimize self-heating effect.

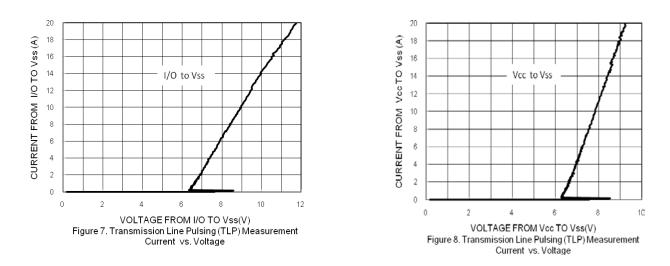
7. Clamping voltage value is based on an  $8x20\mu s$  peak pulse current ( $I_{pp}$ ) waveform.



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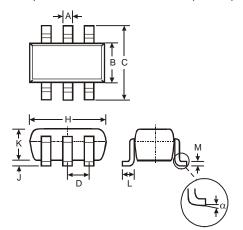






# **Package Outline Dimensions**

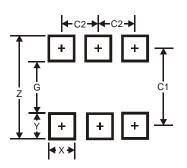
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



	SOT26							
Dim	Min	Тур						
Α	0.35	0.50	0.38					
В	1.50	1.70	1.60					
с	2.70	3.00	2.80					
D			0.95					
н	2.90	3.10	3.00					
<b>ر</b>	0.013	0.10	0.05					
κ	1.00 1.30 1.10							
1	0.35 0.55 0.40							
М	0.10	0.20	0.15					
α	α 0° 8° —							
All D	imensi	ons in	mm					

# **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	3.20
G	1.60
Х	0.55
Y	0.80
C1	2.40
C2	0.95



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