

DS8863/DS8963 MOS-to-LED 8-Digit Driver

General Description

The DS8863 and DS8963 are designed to be used in conjunction with MOS integrated circuits and common-cathode LED's in serially addressed multi-digit displays.

MOS compatibility (low input current)

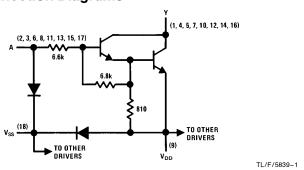
Features

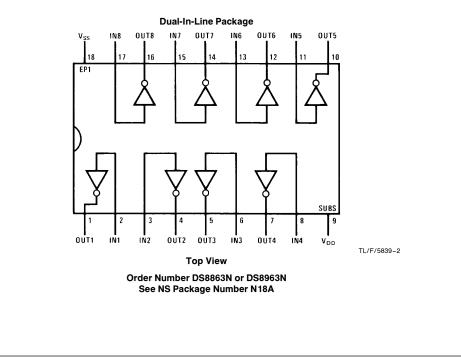
LED's in serially addressed multi-digit displays. The DS8863 is an 8-digit driver. Each driver is capable of High gain Darlington circuits

■ 500 mA sink capability per driver, DS8863, DS8963

sinking up to 500 mA. The DS8963 is identical to the DS8863 except it is intended for operation at up to 18V.

Schematic and Connection Diagrams





©1995 National Semiconductor Corporation TL/F/5839

RRD-B30M105/Printed in U. S. A.

DS8863/DS8963 MOS-to-LED 8-Digit Driver

June 1986

Absolute Maximum Ratings If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.			Collector (Output) Current Each Collector (Output)	DS8863	DS8963	
	DS8863	DS8963	All Collectors (Output)	600 mA	600 mA	
Input Voltage Range (Note 1)	-5V to Vss	-5V to V _{SS}	Continuous Total Dissipation	800 mW	800 mW	
Collector (Output) Voltage (Note 2)	10V	18V	Operating Temperature Range	0°C to +70°C	0°C to +70°C	
Collector (Output)-to-Input Voltage	10V	18V	Storage Temperature Range	−65°C to	o +150°C	
Emitter-to-Ground Voltage $(V_1 \ge 5V)$			Maximum Power Dissipation at 25°C			
Emitter-to-Input Voltage			Molded Package	1563 mW†	1563 mW†	
Voltage at V _{SS} Terminal With Respect to Any Other			Lead Temperature (Soldering, 4 sec.) †Derate molded package 12.5 mW	260°C	260°C	
Device Terminal	10V	18V	Derate molded package 12.5 mm	7 G above 25°G.		

Electrical Characteristics V_{SS} = 10V, T_A = 0°C to + 70°C unless otherwise noted

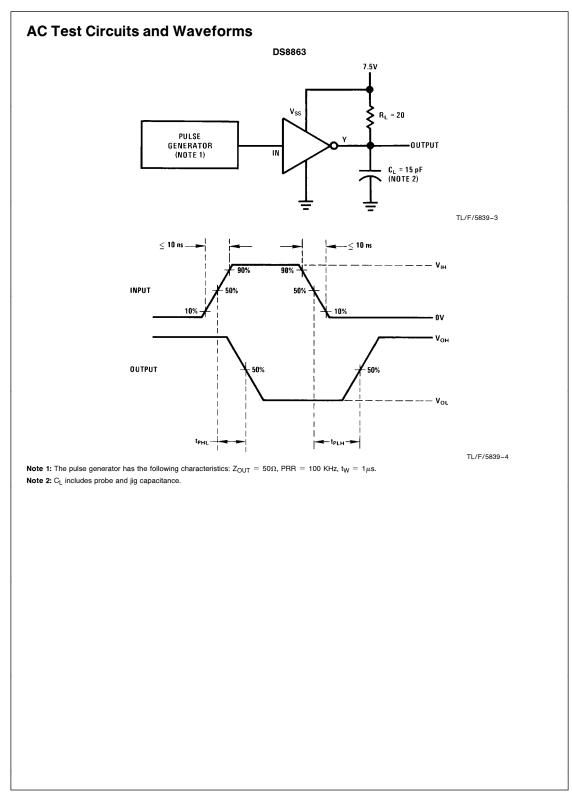
Symbol	Parameter	Conditions			Min	Тур	Max	Units
V _{OL}	Low Level Output Voltage	$V_{\text{IN}} = 7V, I_{\text{OUT}} = 500 \text{ mA} \qquad T_{\text{A}} = 25^{\circ}\text{C}$				1.5	V	
							1.6	V
I _{OH}	High Level Output Current	$V_{OH} = 10V^*$	$I_{IN} = 40 \ \mu A$				250	μΑ
			$V_{IN} = 0.5V$				250	μΑ
lj –	Input Current at Maximum Input Voltage	$V_{IN} = 10V, I_{OL} = 20 \text{ mA}$					2	mA
I _{SS}	Current into V _{SS} Terminal						1	mA

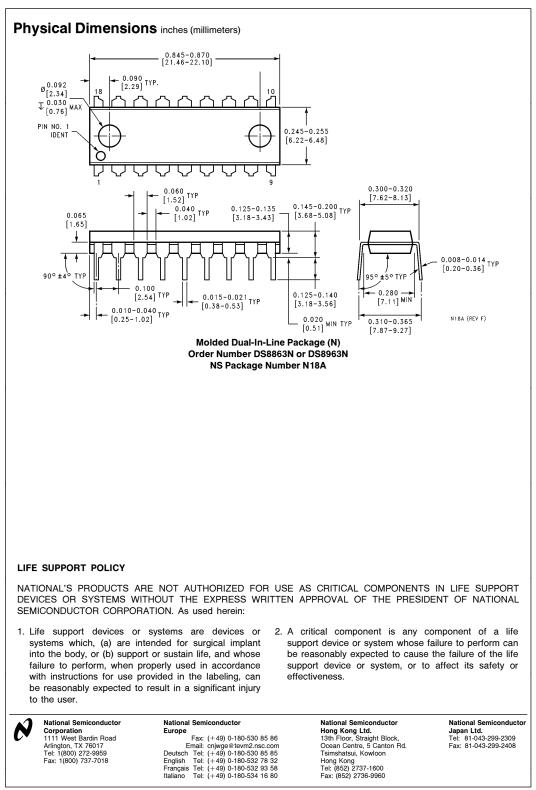
*18V for the DS8963

Switching Characteristics v_{SS} = 7.5V, T_A = 25°C

Symbol	Parameter	Conditions	Min	Тур	Max	Units
t _{PLH}	Propagation Delay Time, Low-to-High Level Output	$V_{IH}=8V, R_L=20\Omega,$		300		ns
t _{PHL}	Propagation Delay Time, High-to-Low Level Output	$C_L = 15 pF$		30		ns

Note 1: The input is the only device terminal which may be negative with respect to ground. Note 2: Voltage values are with respect to network ground terminal unless otherwise noted.





National does not assume any responsibility for use of any circuitry described, no circuit patent licenses are implied and National reserves the right at any time without notice to change said circuitry and specifications.