

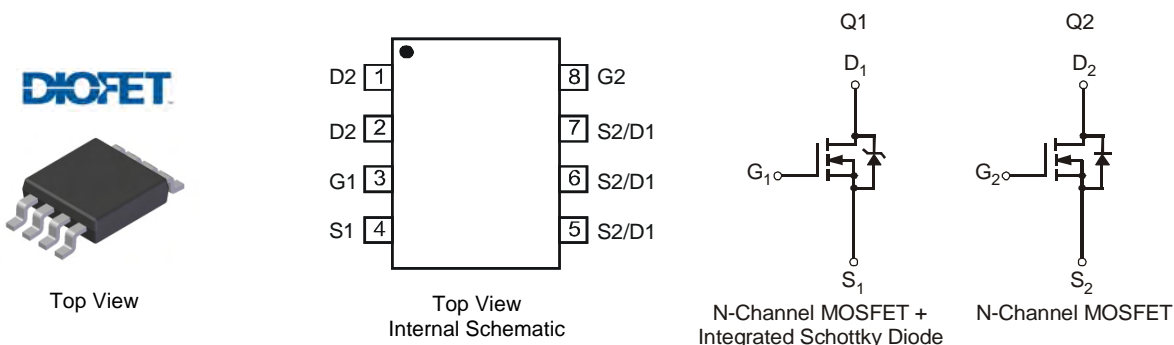
# ASYMMETRIC DUAL N-CHANNEL ENHANCEMENT MODE MOSFET

## Features

- DIOFET utilize a unique patented process to monolithically integrate a MOSFET and a Schottky in a single die to deliver:
  - Low  $R_{DS(on)}$  – minimizes conduction loss
  - Low  $V_{SD}$  – reducing the losses due to body diode construction
  - Low  $Q_{rr}$  – lower  $Q_{rr}$  of the integrated Schottky reduces body diode switching losses
  - Low gate capacitance ( $Q_g/Q_{gs}$ ) ratio – reduces risk of shoot-through or cross conduction currents at high frequencies
  - Avalanche rugged –  $I_{AR}$  and  $E_{AR}$  rated
- Lead Free By Design/RoHS Compliant (Note 1)**
- "Green" Device (Note 2)**
- Qualified to AEC-Q101 Standards for High Reliability**

## Mechanical Data

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram Below
- Weight: 0.072 grams (approximate)

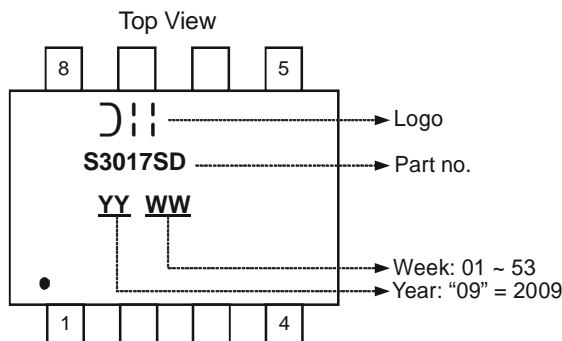


## Ordering Information (Note 3)

Part Number	Case	Packaging
DMS3017SSD-13	SO-8	2500 / Tape & Reel

- Notes:
- No purposefully added lead.
  - Diodes Inc.'s "Green" policy can be found on our website at [http://www.diodes.com/products/lead\\_free/index.php](http://www.diodes.com/products/lead_free/index.php).
  - For packaging details, go to our website at <http://www.diodes.com/datasheets/ap02007.pdf>.

## Marking Information



**Maximum Ratings – Q1** @TA = 25°C unless otherwise specified

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V <sub>DSS</sub>	30	V
Gate-Source Voltage			V <sub>GSS</sub>	±20	V
Continuous Drain Current (Note 4) V <sub>GS</sub> = 10V	Steady State	T <sub>A</sub> = 25°C	I <sub>D</sub>	8.0	A
		T <sub>A</sub> = 70°C		6.5	
Continuous Drain Current (Note 5) V <sub>GS</sub> = 10V	Steady State	T <sub>A</sub> = 25°C	I <sub>D</sub>	10	A
		T <sub>A</sub> = 70°C		7.8	
Continuous Drain Current (Note 5) V <sub>GS</sub> = 4.5V	Steady State	T <sub>A</sub> = 25°C	I <sub>D</sub>	8.7	A
		T <sub>A</sub> = 70°C		7.0	
Pulsed Drain Current (Note 6)			I <sub>DM</sub>	60	A
Avalanche Current (Notes 6 & 7)			I <sub>AR</sub>	16	A
Repetitive Avalanche Energy (Notes 6 & 7) L = 0.1mH			E <sub>AR</sub>	12.8	mJ

**Maximum Ratings – Q2** @TA = 25°C unless otherwise specified

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V <sub>DSS</sub>	30	V
Gate-Source Voltage			V <sub>GSS</sub>	±20	V
Continuous Drain Current (Note 4) V <sub>GS</sub> = 10V	Steady State	T <sub>A</sub> = 25°C	I <sub>D</sub>	6.0	A
		T <sub>A</sub> = 70°C		4.7	
Continuous Drain Current (Note 5) V <sub>GS</sub> = 10V	Steady State	T <sub>A</sub> = 25°C	I <sub>D</sub>	7.2	A
		T <sub>A</sub> = 70°C		6.0	
Continuous Drain Current (Note 5) V <sub>GS</sub> = 4.5V	Steady State	T <sub>A</sub> = 25°C	I <sub>D</sub>	6.0	A
		T <sub>A</sub> = 70°C		5.0	
Pulsed Drain Current (Note 6)			I <sub>DM</sub>	60	A
Avalanche Current (Notes 6 & 7)			I <sub>AR</sub>	16	A
Repetitive Avalanche Energy (Notes 6 & 7) L = 0.1mH			E <sub>AR</sub>	12.8	mJ

**Thermal Characteristics**

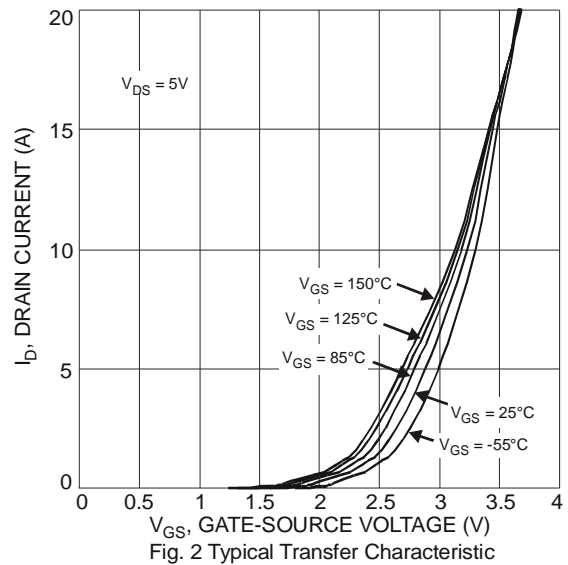
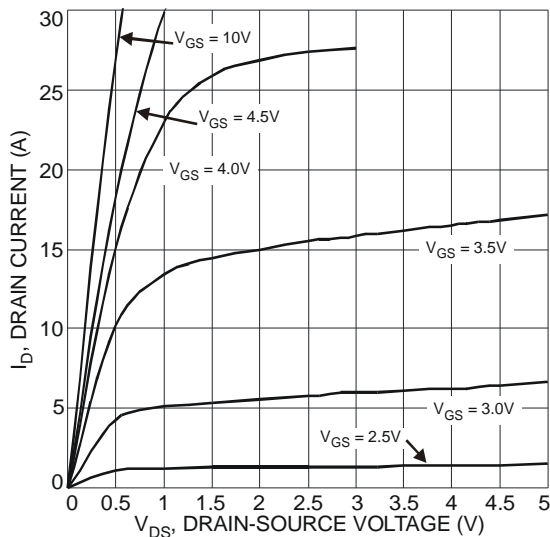
Characteristic	Symbol	Value	Unit
Power Dissipation (Note 4)	P <sub>D</sub>	1.19	W
Thermal Resistance, Junction to Ambient @T <sub>A</sub> = 25°C (Note 4)	R <sub>θJA</sub>	107	°C/W
Power Dissipation (Note 5)	P <sub>D</sub>	1.79	W
Thermal Resistance, Junction to Ambient @T <sub>A</sub> = 25°C (Note 5)	R <sub>θJA</sub>	70	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

- Notes:
4. Device mounted on FR-4 substrate PC board, with minimum recommended pad layout. The value in any given application depends on the user's specific board design. Device contains two active die running at equal power.
  5. Device mounted on 1 inch x 1 inch FR4 PCB with high coverage of single sided 1oz copper, in still air conditions. Device contains two active die running at equal power.
  6. Repetitive rating, pulse width limited by junction temperature.
  7. I<sub>AR</sub> and E<sub>AR</sub> rating are based on low frequency and duty cycles to keep T<sub>J</sub> = 25°C

**Electrical Characteristics – Q1** @  $T_A = 25^\circ\text{C}$  unless otherwise stated

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 8)</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	30	-	-	V	$V_{GS} = 0V, I_D = 250\mu A$
Zero Gate Voltage Drain Current	$I_{DSS}$	-	-	100	$\mu A$	$V_{DS} = 30V, V_{GS} = 0V$
Gate-Source Leakage	$I_{GSS}$	-	-	$\pm 100$	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
<b>ON CHARACTERISTICS (Note 8)</b>						
Gate Threshold Voltage	$V_{GS(th)}$	1.0	-	2.5	V	$V_{DS} = V_{GS}, I_D = 250\mu A$
Static Drain-Source On-Resistance	$R_{DS(on)}$	-	8.5	12	$m\Omega$	$V_{GS} = 10V, I_D = 9.5A$
			9.5	15		$V_{GS} = 4.5V, I_D = 8.8A$
Forward Transfer Admittance	$ Y_{fs} $	-	18	-	S	$V_{DS} = 5V, I_D = 9.5A$
Diode Forward Voltage	$V_{SD}$	-	0.45	0.60	V	$V_{GS} = 0V, I_S = 1A$
<b>DYNAMIC CHARACTERISTICS (Note 9)</b>						
Input Capacitance	$C_{iss}$	-	1276	-	pF	$V_{DS} = 15V, V_{GS} = 0V, f = 1.0MHz$
Output Capacitance	$C_{oss}$	-	160	-		
Reverse Transfer Capacitance	$C_{rss}$	-	136	-		
Gate Resistance	$R_g$	-	1.48	2.7	$\Omega$	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$
Total Gate Charge ( $V_{GS} = 4.5V$ )	$Q_g$	-	14.3	-	nC	$V_{DS} = 15V, V_{GS} = 4.5V, I_D = 8.8A$
Total Gate Charge ( $V_{GS} = 10V$ )	$Q_g$	-	30.6	-		$V_{DS} = 15V, V_{GS} = 10V, I_D = 8.8A$
Gate-Source Charge	$Q_{gs}$	-	3.4	-		
Gate-Drain Charge	$Q_{gd}$	-	4.3	-		
Turn-On Delay Time	$t_{D(on)}$	-	15.8	-	ns	$V_{GS} = 4.5V, V_{DS} = 15V, R_G = 1.8\Omega, I_D = 8.8A$
Turn-On Rise Time	$t_r$	-	27.8	-		
Turn-Off Delay Time	$t_{D(off)}$	-	29.7	-		
Turn-Off Fall Time	$t_f$	-	13.6	-		

Notes: 8. Short duration pulse test used to minimize self-heating effect.  
9. Guaranteed by design. Not subject to production testing.



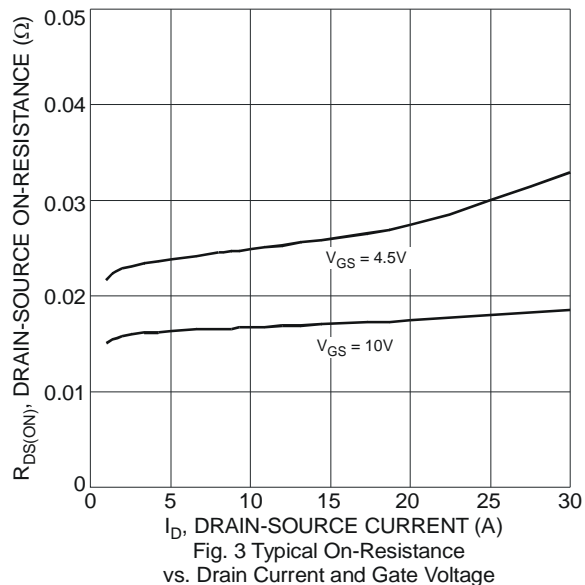


Fig. 3 Typical On-Resistance vs. Drain Current and Gate Voltage

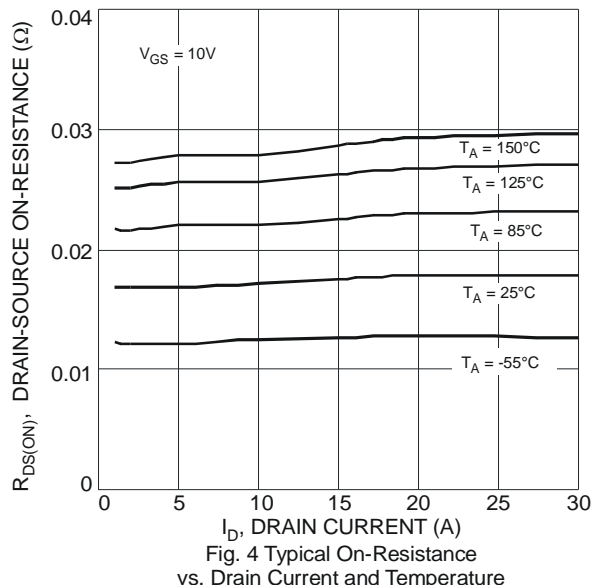


Fig. 4 Typical On-Resistance vs. Drain Current and Temperature

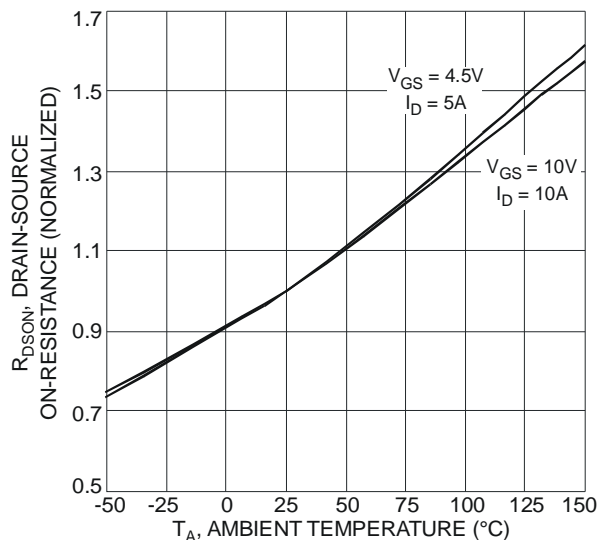


Fig. 5 On-Resistance Variation with Temperature

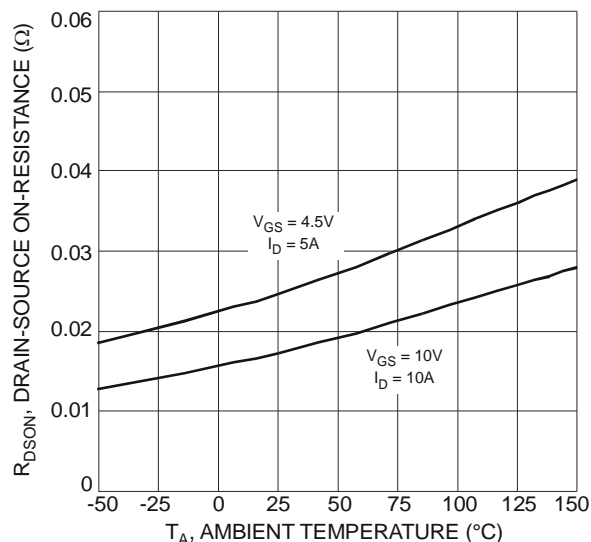


Fig. 6 On-Resistance Variation with Temperature

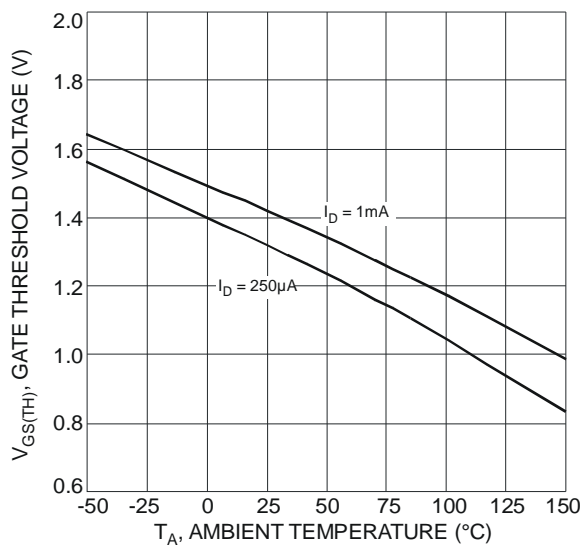


Fig. 7 Gate Threshold Variation vs. Ambient Temperature

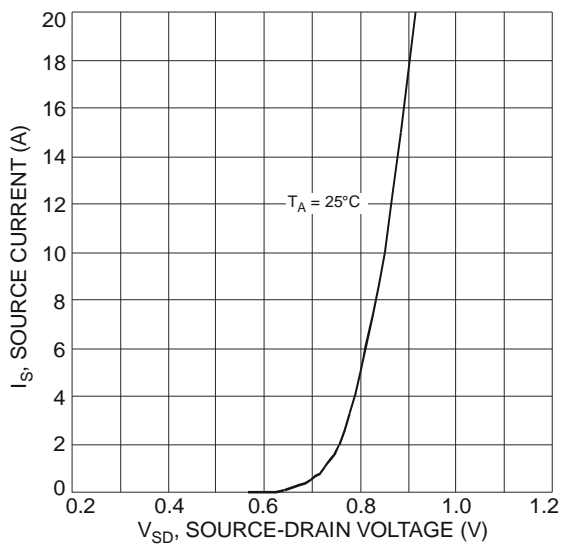
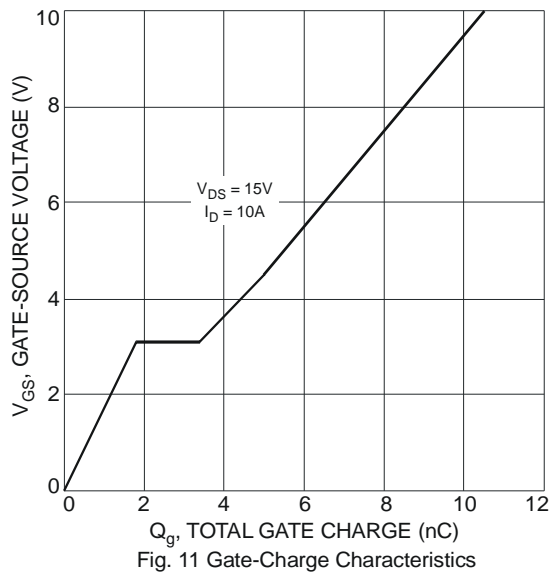
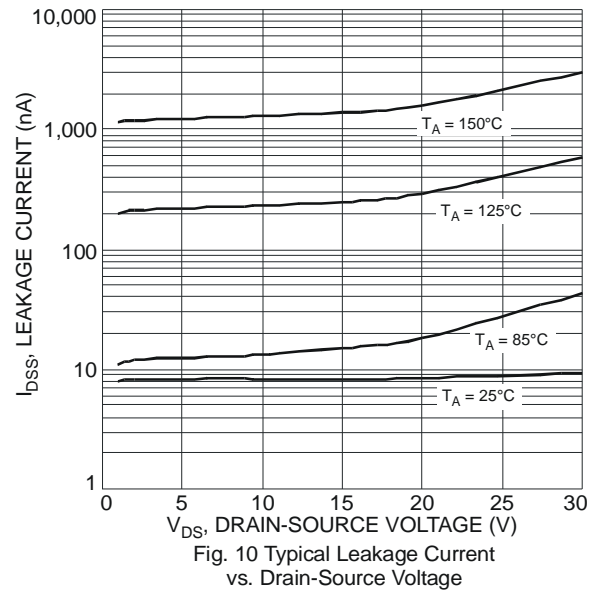
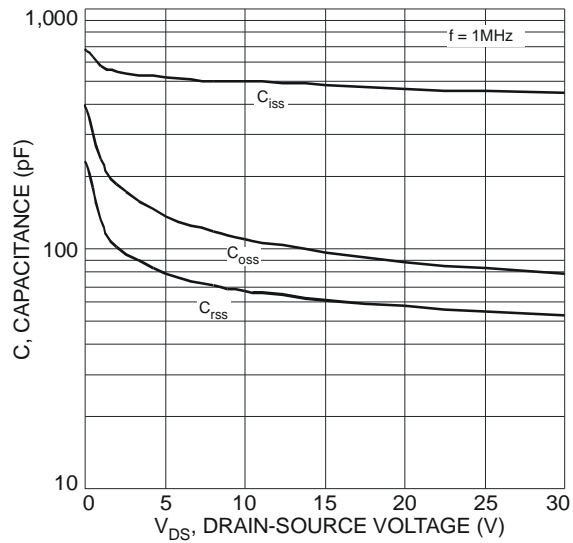


Fig. 8 Diode Forward Voltage vs. Current



**Electrical Characteristics – Q2** @  $T_A = 25^\circ\text{C}$  unless otherwise stated

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 8)</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	30	-	-	V	$V_{GS} = 0V, I_D = 1mA$
Zero Gate Voltage Drain Current	$I_{DSS}$	-	-	1	$\mu A$	$V_{DS} = 30V, V_{GS} = 0V$
Gate-Source Leakage	$I_{GSS}$	-	-	$\pm 100$	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
<b>ON CHARACTERISTICS (Note 8)</b>						
Gate Threshold Voltage	$V_{GS(th)}$	1.0	-	2.4	V	$V_{DS} = V_{GS}, I_D = 250\mu A$
Static Drain-Source On-Resistance	$R_{DS(on)}$	-	15	22	$m\Omega$	$V_{GS} = 10V, I_D = 8.8A$
			25	32		$V_{GS} = 4.5V, I_D = 7A$
Forward Transfer Admittance	$ Y_{fs} $	-	2.5	-	S	$V_{DS} = 5V, I_D = 8.8A$
Diode Forward Voltage	$V_{SD}$	-	0.7	1	V	$V_{GS} = 0V, I_S = 1A$
<b>DYNAMIC CHARACTERISTICS (Note 9)</b>						
Input Capacitance	$C_{iss}$	-	478.9	-	pF	$V_{DS} = 15V, V_{GS} = 0V, f = 1.0MHz$
Output Capacitance	$C_{oss}$	-	96.7	-		
Reverse Transfer Capacitance	$C_{rss}$	-	61.4	-		
Gate Resistance	$R_g$	-	1.1	-	$\Omega$	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$
Total Gate Charge ( $V_{GS} = 4.5V$ )	$Q_g$	-	5.0	-	nC	$V_{DS} = 15V, V_{GS} = 4.5V, I_D = 10A$
Total Gate Charge ( $V_{GS} = 10V$ )	$Q_g$	-	10.5	-		$V_{DS} = 15V, V_{GS} = 10V, I_D = 10A$
Gate-Source Charge	$Q_{gs}$	-	1.8	-		
Gate-Drain Charge	$Q_{gd}$	-	1.6	-		
Turn-On Delay Time	$t_{D(on)}$	-	2.9	-	ns	$V_{GS} = 10V, V_{DS} = 15V, R_G = 3\Omega, R_L = 1.5\Omega$
Turn-On Rise Time	$t_r$	-	7.9	-		
Turn-Off Delay Time	$t_{D(off)}$	-	14.6	-		
Turn-Off Fall Time	$t_f$	-	3.1	-		

Notes: 8. Short duration pulse test used to minimize self-heating effect.  
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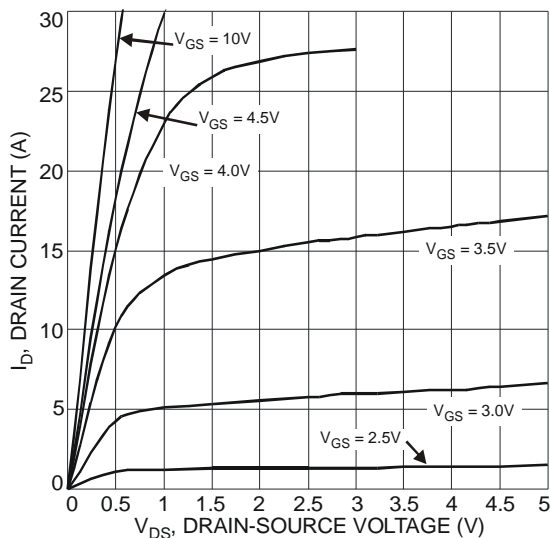


Fig. 12 Typical Output Characteristic

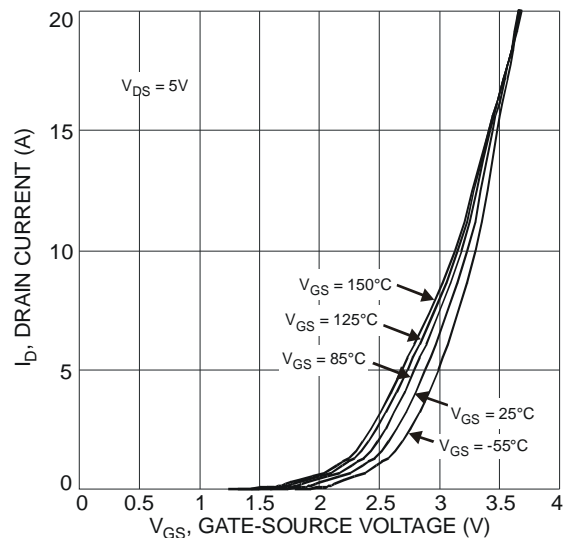


Fig. 13 Typical Transfer Characteristic

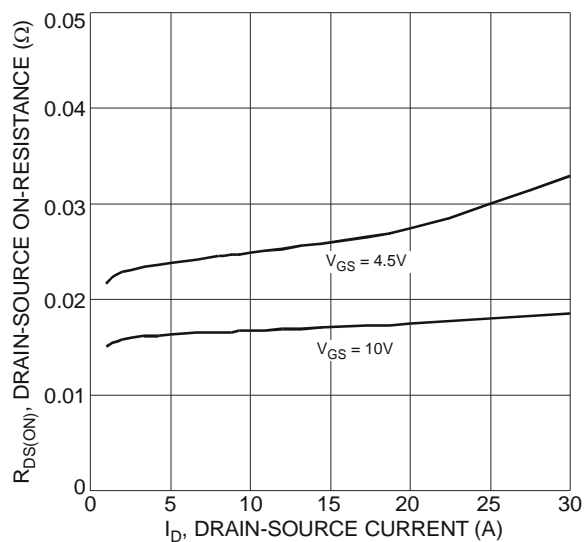


Fig. 14 Typical On-Resistance vs. Drain Current and Gate Voltage

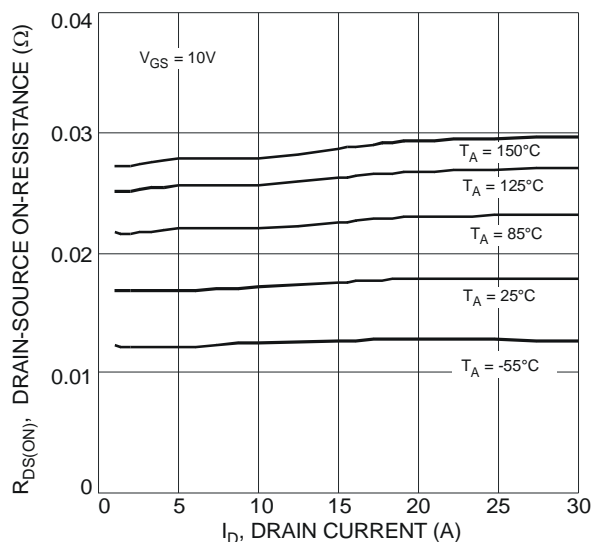


Fig. 15 Typical On-Resistance vs. Drain Current and Temperature

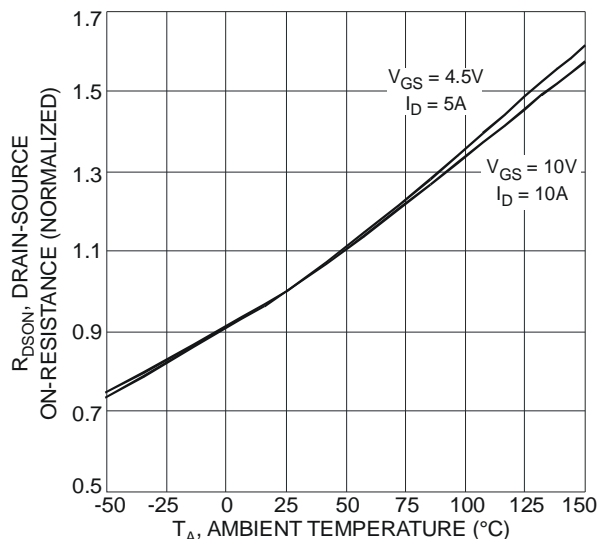


Fig. 16 On-Resistance Variation with Temperature

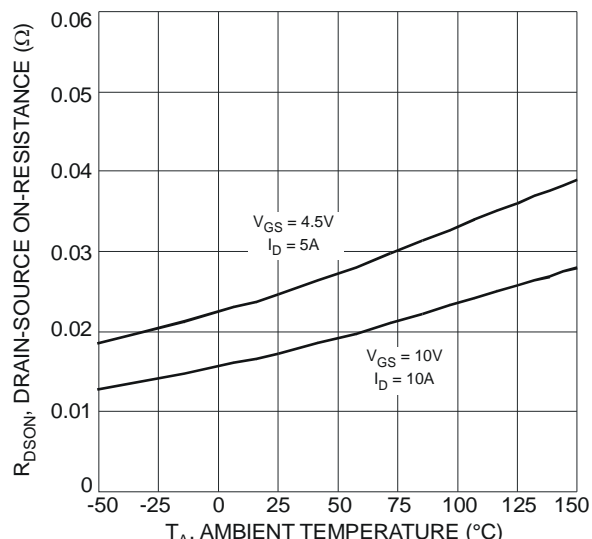


Fig. 17 On-Resistance Variation with Temperature

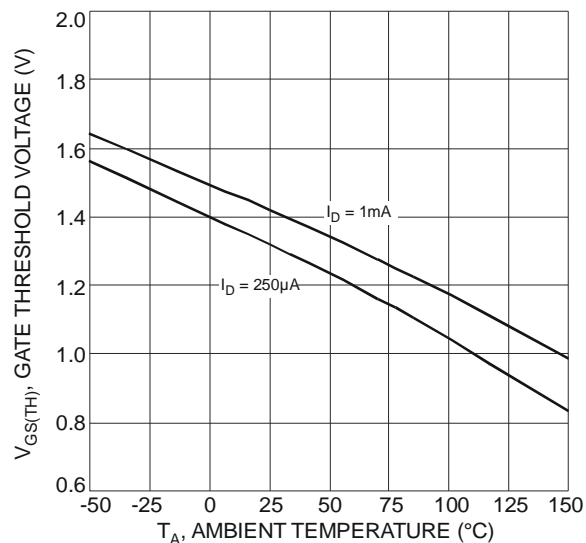


Fig. 18 Gate Threshold Variation vs. Ambient Temperature

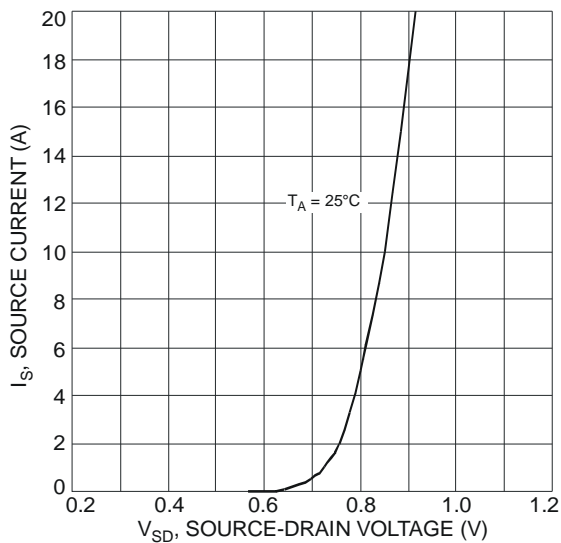


Fig. 19 Diode Forward Voltage vs. Current

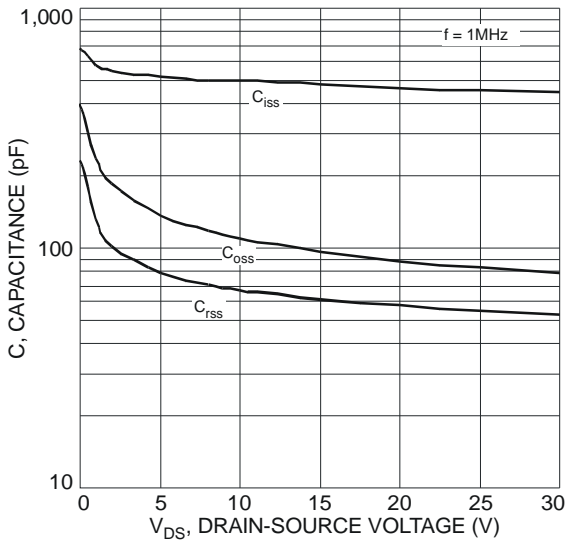


Fig. 20 Typical Total Capacitance

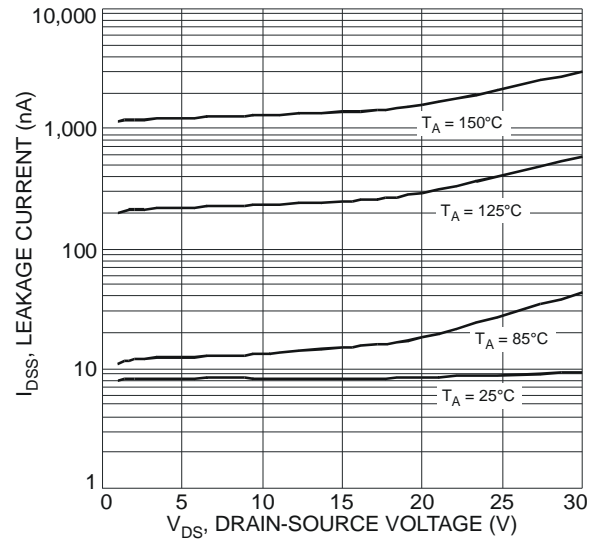


Fig. 21 Typical Leakage Current vs. Drain-Source Voltage

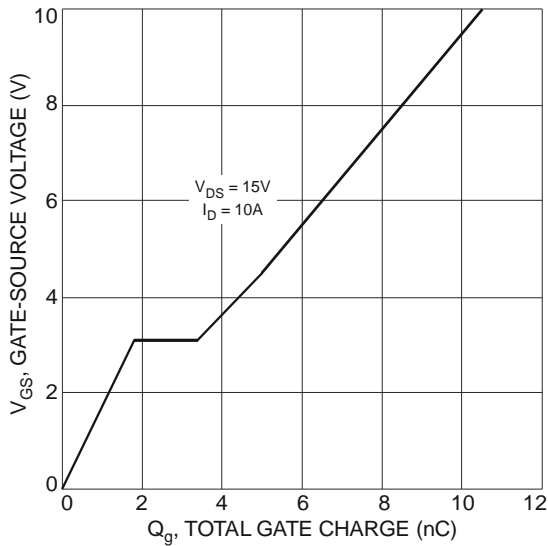


Fig. 22 Gate-Charge Characteristics

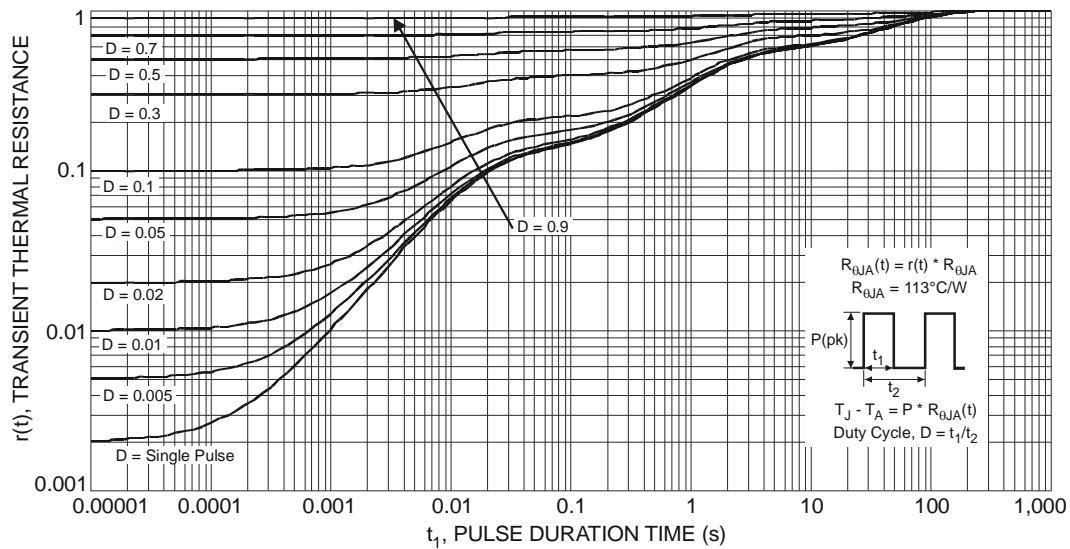
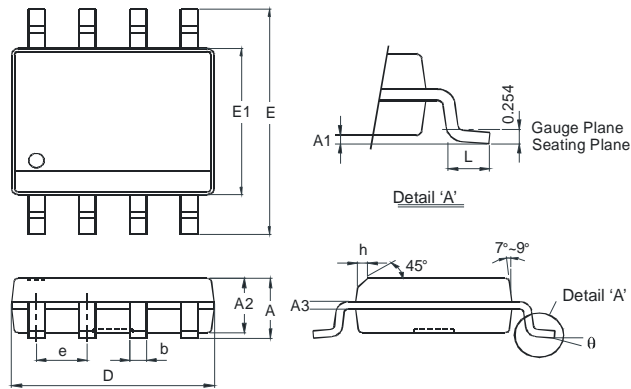


Fig. 23 Transient Thermal Response

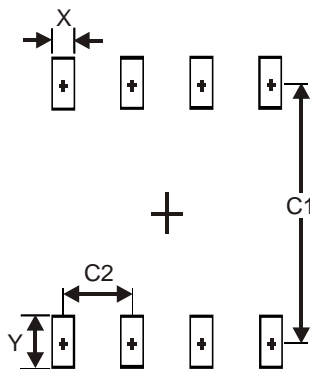


## Package Outline Dimensions



SO-8		
Dim	Min	Max
A	-	1.75
A1	0.10	0.20
A2	1.30	1.50
A3	0.15	0.25
b	0.3	0.5
D	4.85	4.95
E	5.90	6.10
E1	3.85	3.95
e	1.27 Typ	
h	-	0.35
L	0.62	0.82
θ	0°	8°
All Dimensions in mm		

## Suggested Pad Layout



Dimensions	Value (in mm)
X	0.60
Y	1.55
C1	5.4
C2	1.27

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