

DMS3012SFG

# 30V N-CHANNEL ENHANCEMENT MODE MOSFET WITH SCHOTTKY DIODE POWERDI®

### **Product Summary**

V <sub>(BR)DSS</sub>	R <sub>DS(ON)</sub>	Package	I <sub>D</sub> T <sub>A</sub> = +25°C
30V	10mΩ @ V <sub>GS</sub> = 10V	POWERDI	12A
307	15mΩ @ V <sub>GS</sub> = 4.5V	3333-8	9.5A

#### **Description**

This MOSFET is designed to minimize on-state resistance ( $R_{DS(on)}$ ) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

### **Applications**

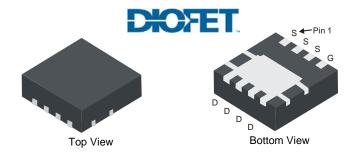
- Backlighting
- Power Management Functions
- DC-DC Converters

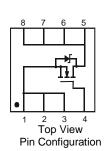
#### **Features**

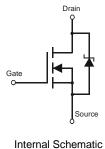
- DIOFET utilizes a unique patented process to monolithically integrate a MOSFET and a Schottky in a single die to deliver:
  - Low R<sub>DS(ON)</sub> minimize conduction losses
  - Low V<sub>SD</sub> reducing the losses due to body diode conduction
  - Low Q<sub>rr</sub> lower Q<sub>rr</sub> of the integrated Schottky reduces body diode switching losses
  - Low gate capacitance (Q<sub>g</sub>/Q<sub>gs</sub>) ratio reduces risk of shoot through or cross conduction currents at high frequencies
- Small form factor thermally efficient package enables higher density end products
- Occupies just 33% of the board area occupied by SO-8, enabling smaller end product
- 100% UIS (Avalanche) rated
- 100% Rg tested
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

#### **Mechanical Data**

- Case: POWERDI3333-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See diagram
   Terminals: Finish Matte Tin annealed over Copper leadframe.
   Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.072 grams (approximate)







**Ordering Information** (Note 4)

Part Number	Case	Packaging
DMS3012SFG-7	POWERDI3333-8	2000/Tape & Reel
DMS3012SFG-13	POWERDI3333-8	3000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http"//www.diodes.com/products/packages.html.



# **Marking Information**



N12 = Product Type Marking Code YYWW = Date Code Marking YY = Last digit of year (ex: 11 = 2011) WW = Week code (01 ~ 53)

# **Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units		
Drain-Source Voltage	V <sub>DSS</sub>	30	V		
Gate-Source Voltage			V <sub>GSS</sub>	±20	V
Continuous Dusin Courset (Note C) // 40 //	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I <sub>D</sub>	12 9.5	А
Continuous Drain Current (Note 6) V <sub>GS</sub> = 10V	t < 10s	$T_A = +25$ °C $T_A = +70$ °C	I <sub>D</sub>	16.0 12.7	А
Outliness David Outline (NV)	Steady State	$T_A = +25$ °C $T_A = +70$ °C	I <sub>D</sub>	9.5 7.5	А
Continuous Drain Current (Note 6) V <sub>GS</sub> = 4.5V	t < 10s	$T_A = +25$ °C $T_A = +70$ °C	I <sub>D</sub>	13.0 10.3	А
Pulsed Drain Current (10µs pulse, duty cycle = 1%)	I <sub>DM</sub>	90	Α		
Maximum Continuous Body Diode Forward Current (	I <sub>S</sub>	3.5	Α		
Avalanche Current (Note 7) L = 0.1mH			I <sub>AS</sub>	17	Α
Avalanche Energy (Note 7) L = 0.1mH			Eas	43	mJ

# **Thermal Characteristics**

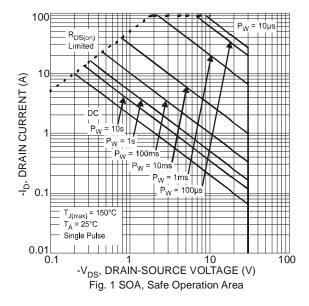
Characteristic	Symbol	Value	Units	
Total Power Dissipation (Note 5)	$T_A = +25^{\circ}C$	0	0.89	W
Total Power Dissipation (Note 5)	$T_A = +70^{\circ}C$	$P_{D}$	0.55	
Thermal Resistance, Junction to Ambient (Note 5)	Steady state	6	145	°C/W
Thermal Resistance, Junction to Ambient (Note 5)	t < 10s	$R_{\theta JA}$	74	
Total Power Dissipation (Note 6)	$T_A = +25^{\circ}C$	0	2.2	W
Total Power Dissipation (Note 6)	$T_A = +70^{\circ}C$	$P_{D}$	1.3	
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	6	58	°C/W
Thermal Resistance, Junction to Ambient (Note 6)	t < 10s	$R_{ heta JA}$	31	
Thermal Resistance, Junction to Case (Note 6)		$R_{ heta JC}$	11	
Operating and Storage Temperature Range		$T_{J_i}T_{STG}$	-55 to +150	°C

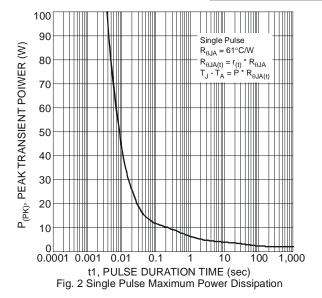
Notes:

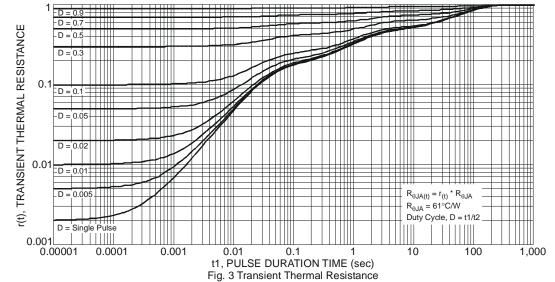
- 5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout. 6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate. 7.  $I_{AS}$  and  $E_{AS}$  rating are based on low frequency and duty cycles to keep  $T_J$  = +25°C.











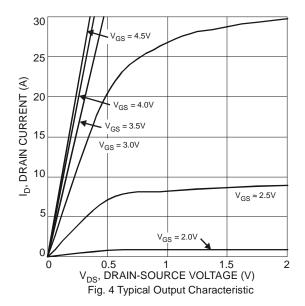


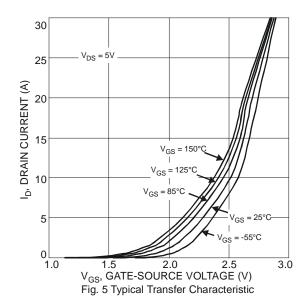
### Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	30	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>		_	100	μΑ	$V_{DS} = 30V, V_{GS} = 0V$	
Gate-Source Leakage				±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V <sub>GS(th)</sub>	1.0	1.5	2.5	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$	
Static Drain-Source On-Resistance		l	7.3	10	mΩ	$V_{GS} = 10V, I_D = 13.5A$	
Static Dialif-Source Off-Nesistance	R <sub>DS</sub> (ON)		10	15	11122	$V_{GS} = 4.5V, I_D = 11A$	
Forward Transfer Admittance	Y <sub>fs</sub>		30	_	S	$V_{DS} = 5V, I_{D} = 10.0A$	
Diode Forward Voltage	$V_{SD}$	_	0.45	0.55	V	$V_{GS} = 0V, I_{S} = 1A$	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	C <sub>iss</sub>		1296	4310	pF	151/1/	
Output Capacitance	Coss	_	415	_	pF	$V_{DS} = 15V, V_{GS} = 0V,$ of = 1.0MHz	
Reverse Transfer Capacitance	C <sub>rss</sub>	_	204	_	pF	1 = 1.0WHZ	
Gate Resistance	$R_g$	0.26	1.6	2.6	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$	
Total Gate Charge V <sub>GS</sub> = 4.5V	Qg		14.7	_	nC		
Total Gate Charge V <sub>GS</sub> = 10V	Qg		31.6	_	nC	\/ 45\/ \/ 40\/   42.5A	
Gate-Source Charge	$Q_{gs}$	_	3.5	_	nC	$V_{DS} = 15V, V_{GS} = 10V, I_D = 13.5A$	
Gate-Drain Charge	$Q_{gd}$	-	5.0	_	nC		
Turn-On Delay Time	t <sub>D(on)</sub>	_	15.8	_	ns		
Turn-On Rise Time	t <sub>r</sub>	_	27.8	_	ns	$V_{GS} = 10V, V_{DS} = 15V,$	
Turn-Off Delay Time	t <sub>D(off)</sub>	_	29.7	_	ns	$R_G = 3\Omega, I_D = 8.8A$	
Turn-Off Fall Time	t <sub>f</sub>		13.6	_	ns	1	
Reverse Recovery Time	t <sub>rr</sub>	_	13.1	_	ns	$I_F = 13.5A$ , di/dt = 100A/ $\mu$ s	
Reverse Recovery Charge	Q <sub>rr</sub>		4.3	_	nC	$I_F = 13.5A$ , $di/dt = 100A/\mu s$	

Notes: 8. Short duration pulse test used to minimize self-heating effect.

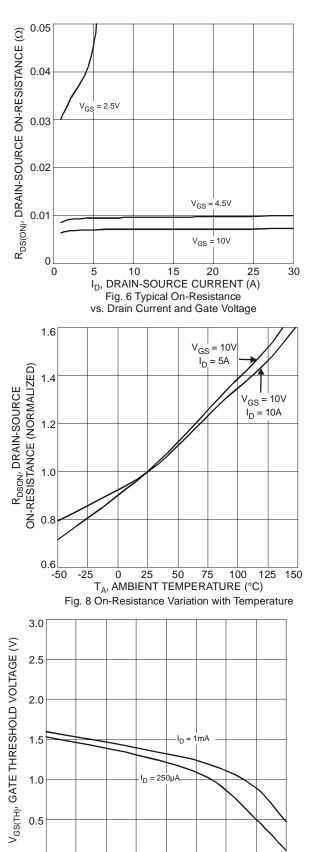
9. Guaranteed by design. Not subject to product testing.





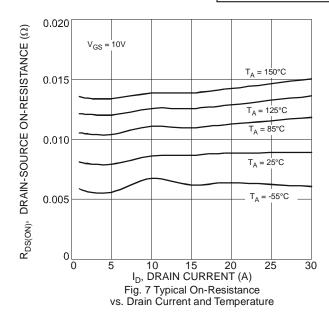


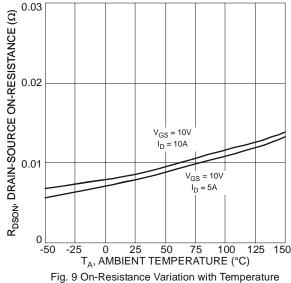




5 0 25 50 75 100 T<sub>A</sub>, AMBIENT TEMPERATURE (°C) Fig. 10 Gate Threshold Variation vs. Ambient Temperature

100 125





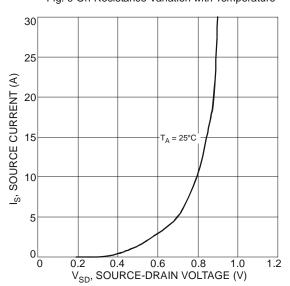
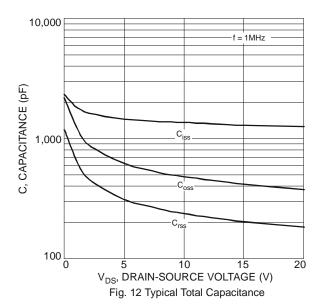
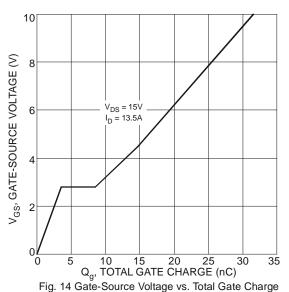
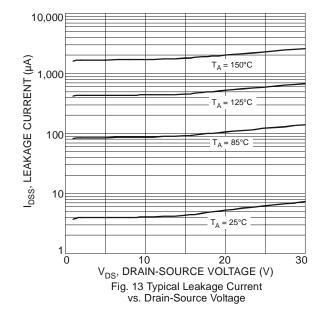


Fig. 11 Diode Forward Voltage vs. Current





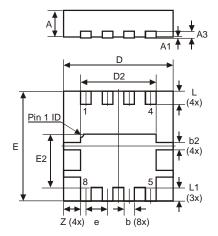






# **Package Outline Dimensions**

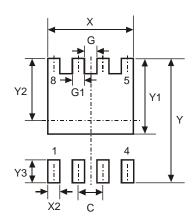
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



POWERDI3333-8				
Dim	Min	Max	Тур	
D	3.25	3.35	3.30	
Е	3.25	3.35	3.30	
D2	2.22	2.32	2.27	
E2	1.56	1.66	1.61	
Α	0.75	0.85	0.80	
A1	0	0.05	0.02	
A3	_	_	0.203	
b	<b>b</b> 0.27 0.3		0.32	
b2	_	_	0.20	
L	0.35	0.45	0.40	
L1	_	_	0.39	
е	_	_	0.65	
Z	_	_	0.515	
All I	All Dimensions in mm			

# **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)				
C	0.650				
G	0.230				
G1	0.420				
Υ	3.700				
Y1	2.250				
Y2	1.850				
Y3	0.700				
Х	2.370				
X2	0.420				



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