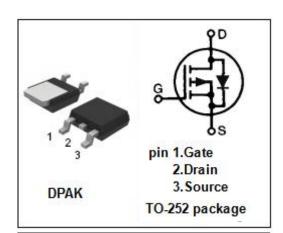


# isc P-Channel MOSFET Transistor

## DMP4025LK3

#### **FEATURES**

- Drain Current –I<sub>D</sub>= -8.6A@ T<sub>C</sub>=25°C
- · Drain Source Voltage-
- : V<sub>DSS</sub>= -40V(Min)
- · Static Drain-Source On-Resistance
  - :  $R_{DS(on)} = 25m \Omega (Max)$
- · 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation



#### **DESCRIPTION**

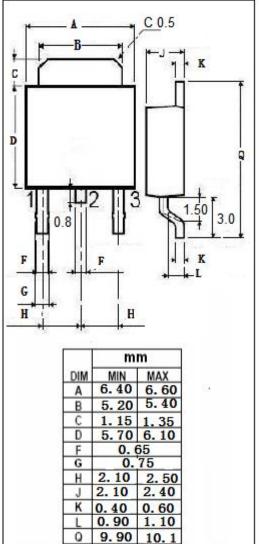
• Designed for use in switch mode power supplies and general purpose applications.

### ABSOLUTE MAXIMUM RATINGS(Ta=25°C)

ABGGEGTE IMPARIMONITATING G(Ta 200)						
SYMBOL	PARAMETER		UNIT			
V <sub>DSS</sub>	Drain-Source Voltage	-40	V			
V <sub>G</sub> s	Gate-Source Voltage-Continuous	±20	V			
Ι <sub>D</sub>	Drain Current-Continuous	-8.6	Α			
I <sub>DM</sub>	Drain Current-Single Pluse		А			
P <sub>D</sub>	Total Dissipation @T <sub>C</sub> =25℃	50	W			
TJ	Max. Operating Junction Temperature	-55~150	$^{\circ}$			
T <sub>stg</sub>	Storage Temperature	-55~150	$^{\circ}$			

### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
R <sub>th j-c</sub>	Thermal Resistance, Junction to Case	2.5	°C/W





# isc P-Channel MOSFET Transistor

## **DMP4025LK3**

#### **ELECTRICAL CHARACTERISTICS**

Tc=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> = 0; I <sub>D</sub> = -0.25mA	-40		V
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{DS}$ = $V_{GS}$ ; $I_D$ = -0.25mA	-0.8	-1.8	V
R <sub>DS(on)</sub>	Drain-Source On-Resistance	V <sub>GS</sub> = -10V; I <sub>D</sub> = -3A		25	mΩ
Igss	Gate-Body Leakage Current	V <sub>GS</sub> = ±20V;V <sub>DS</sub> = 0		±100	nA
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = -40V; V <sub>GS</sub> = 0		-1.0	μА
V <sub>SD</sub>	Forward On-Voltage	I <sub>S</sub> = -1.0A; V <sub>GS</sub> = 0		-1.0	V

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