



30V P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(on)} Max	I _D Max @ T _A = +25°C		
-30V	2.4Ω @ $V_{GS} = -10V$	-400mA		
	4Ω @ $V_{GS} = -4.5V$	-300mA		
	16Ω @ $V_{GS} = -2.5V$	-50mA		

Description

This MOSFET has been designed to minimize the on-state resistance $(R_{DS(on)})$ and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Load Switch
- Portable Applications
- Power Management Functions

Features

- Low On-Resistance
- Ultra-Small Surfaced Mount Package
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

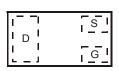
- Case: X1-DFN1006-3
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish NiPdAu over Copper leadframe. Solderable per MIL-STD-202, Method 208 @
- Weight: 0.001 grams (approximate)



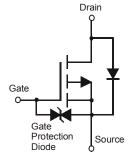




Bottom View



Top View



Equivalent Circuit

Ordering Information (Note 4)

Product	Marking	Reel size (inches)	Quantity per reel
DMP32D4SFB-7B	XP	7	10,000

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http"//www.diodes.com/products/packages.html.

Marking Information



Top View Bar Denotes Gate and Source Side

XP = Product Type Marking Code



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	-30	V
Gate-Source Voltage			V _{GSS}	±20	V
Continuous Drain Current (Note 5)	V _{GS} = -10V	T _A = +25°C T _A = +70°C	I _D	-400 -300	mA
Continuous Drain Current (Note 6) $V_{GS} = -10V$ $T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$			I _D	-500 -400	mA
Pulsed Drain Current (Note 5)			I _{DM}	-1	Α
Maximum Body Diode continuous Current			Is	-800	mA

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units		
Total Power Dissipation	(Note 5)	D	0.5	- w	
Total Power Dissipation	(Note 6)	P_{D}	1.2		
Thermal Resistance, Junction to Ambient	(Note 5)	0	273	°C/W	
Thermal Resistance, Junction to Ambient	(Note 6)	$R_{\theta JA}$	105		
Operating and Storage Temperature Range	$T_{J_i} T_{STG}$	-55 to 150	°C		

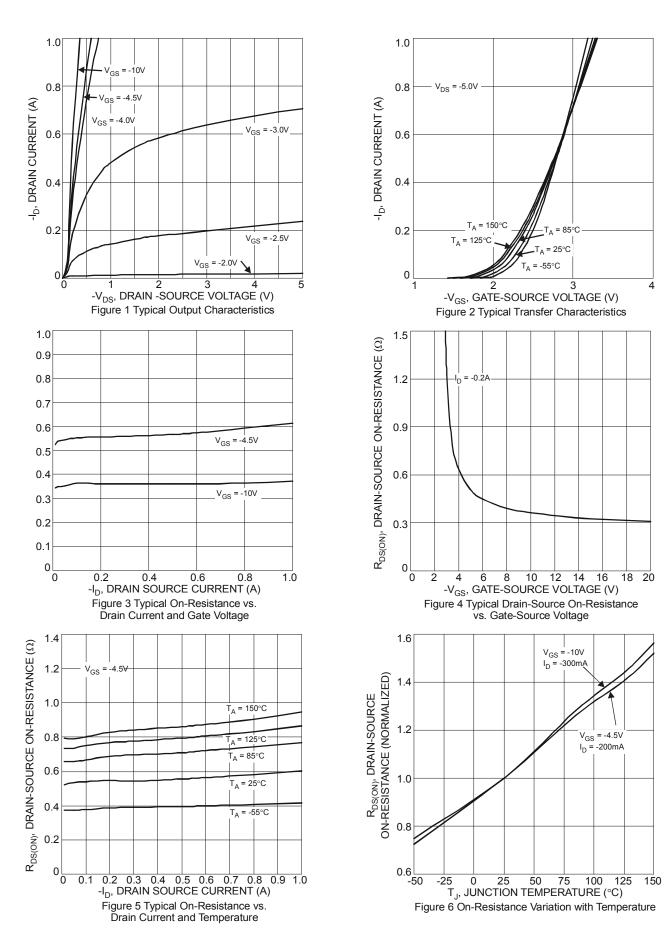
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	-30	-	-	V	$V_{GS} = 0V$, $I_D = -1mA$	
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	-	-	-1	μΑ	$V_{DS} = -30V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	-	-	±10	μΑ	$V_{GS} = \pm 16V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	$V_{GS(th)}$	-1.3	-	-2.3	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	
				2.4		$V_{GS} = -10V, I_D = -200mA$	
Static Drain-Source On-Resistance	R _{DS (ON)}	-	-	4	Ω	$V_{GS} = -4.5V$, $I_{D} = -200$ mA	
				16		$V_{GS} = -2.5V$, $I_{D} = -10mA$	
Forward Transfer Admittance	Y _{fs}	-	6	-	S	$V_{DS} = -10V, I_{D} = -400mA$	
Diode Forward Voltage	V_{SD}	-	8.0	1.2	V	$V_{GS} = 0V, I_{S} = -300mA$	
DYNAMIC CHARACTERISTICS (Note 8)		-					
Input Capacitance	C _{iss}	-	51	-	pF	V _{DS} = -15V, V _{GS} = 0V, f = 1.0MHz	
Output Capacitance	Coss	-	11	-	pF		
Reverse Transfer Capacitance	C _{rss}	-	9	-	pF		
Total Gate Charge	Qg	-	0.6	-	nC	V _{GS} = -4.5V	
Total Gate Charge	Q_g	-	1.3	-	nC	$V_{DS} = -10V$,	
Gate-Source Charge	Q_{gs}	-	0.2	-	nC	$V_{GS} = -10V$ $I_{D} = -200 \text{mA}$	
Gate-Drain Charge	Q_{gd}	-	0.2	-	nC		
Turn-On Delay Time	t _{D(on)}	-	3.6	-	ns	V_{DS} = -15V, I_{D} = -500mA V_{GS} = -10V, R_{G} = 1 Ω	
Turn-On Rise Time	t _r	-	8.5	-	ns		
Turn-Off Delay Time	t _{D(off)}	-	31.3	-	ns		
Turn-Off Fall Time	t _f	-	20.2	-	ns		

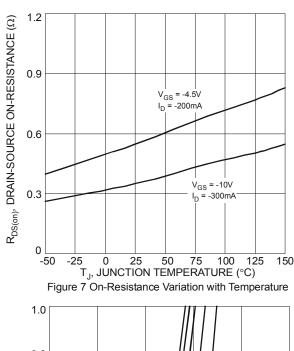
Notes:

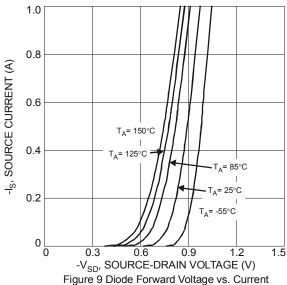
- 5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
 6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad layout
 7 .Short duration pulse test used to minimize self-heating effect.
- 8. Guaranteed by design. Not subject to production testing.

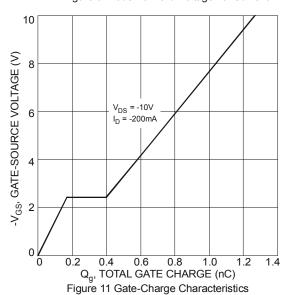












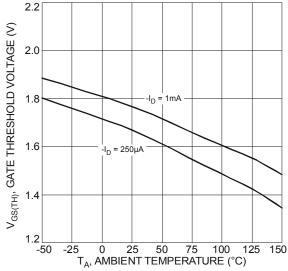
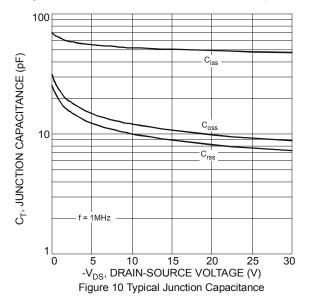


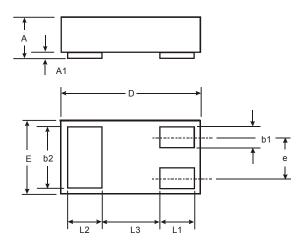
Figure 8 Gate Threshold Variation vs. Ambient Temperature





Package Outline Dimensions

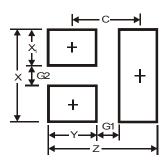
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



X1-DFN1006-3					
Dim	Min	Max	Тур		
Α	0.47	0.53	0.50		
A1	0	0.05	0.03		
b1	0.10	0.20	0.15		
b2	0.45	0.55	0.50		
D	0.95	1.075	1.00		
Е	0.55	0.675	0.60		
е	_		0.35		
L1	0.20	0.30	0.25		
L2	0.20	0.30	0.25		
L3	_	_	0.40		
All Dimensions in mm					

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	1.1
G1	0.3
G2	0.2
X	0.7
X1	0.25
Y	0.4
С	0.7



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