



#### 30V P-CHANNEL ENHANCEMENT MODE MOSFET

#### **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON) MAX</sub>	I <sub>D MAX</sub> T <sub>C</sub> = +25°C	
-30V	$25m\Omega$ @ $V_{GS} = -10V$	-27A	
	$38m\Omega @ V_{GS} = -4.5V$	-22A	

## **Features**

- 100% Unclamped Inductive Switch (UIS) Test In Production
- Low On-Resistance
- · Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Note 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

## **Description and Applications**

This MOSFET is designed to meet the stringent requirements of Automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

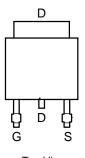
- Backlighting
- DC-DC Converters
- Power Management Functions

#### **Mechanical Data**

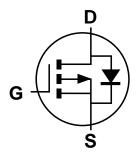
- Case: TO252 (DPAK)
- Case Material: Molded Plastic, "Green" Molding Compound.
   UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish Annealed over Copper Leadframe.
   Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.33 grams (Approximate)







Top View Pin-Out



**Equivalent Circuit** 

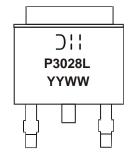
## Ordering Information (Note 5)

Product	Case	Packaging
DMP3028LK3Q-13	TO252 (DPAK)	2.500/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to http://www.diodes.com/product\_compliance\_definitions.html.
- 5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

#### **Marking Information**



D!! = Manufacturer's Marking
P3028L= Product Type Marking Code
YYWW = Date Code Marking
YY = Year (ex: 16 = 2016)
WW = Week (01 to 53)



# **Maximum Ratings** (@ $T_A = +25^{\circ}C$ , unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	V <sub>DSS</sub>	-30	V		
Gate-Source Voltage			V <sub>GSS</sub>	±20	V
Steady $T_C = +25^{\circ}C$ State $T_C = +70^{\circ}C$		I <sub>D</sub>	-27 -22	А	
Continuous Drain Current (Note 7) V <sub>GS</sub> = -10V	t<10s	$T_A = +25$ °C $T_A = +70$ °C	I <sub>D</sub>	-11 -8.6	А
Maximum Body Diode Continuous Current	Is	-2.5	Α		
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I <sub>DM</sub>	-40	Α
Avalanche Current (Note 8) L = 0.1mH			I <sub>AS</sub>	-22	Α
Avalanche Energy (Note 8) L = 0.1mH			E <sub>AS</sub>	24	mJ

## Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 6)	T <sub>A</sub> = +25°C	D-	1.6	W
Total Fower Dissipation (Note o)	T <sub>A</sub> = +70°C	$P_{D}$	1.0	
Thermal Resistance, Junction to Ambient (Note 6)	Steady state	$R_{\theta,JA}$	77	°C/W
Thermal Nesistance, Junction to Ambient (Note 0)	t<10s	KAJA	34	
Total Power Dissipation (Note 7)	$T_A = +25$ °C	P <sub>D</sub>	2.8	°C/W
Total Fower Dissipation (Note 7)	$T_A = +70^{\circ}C$	PD	1.8	
Thermal Resistance, Junction to Ambient (Note 7)	Steady state	D	45	
Thermal Resistance, Junction to Ambient (Note 1)	t<10s	$R_{\theta JA}$	29	
Thermal Resistance, Junction to Case (Note 7)	$R_{ heta JC}$	4.5		
Operating and Storage Temperature Range		T <sub>J,</sub> T <sub>STG</sub>	-55 to +150	°C

## Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 9)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-30	_	_	V	$V_{GS} = 0V, I_D = -250\mu A$	
Zero Gate Voltage Drain Current T <sub>J</sub> = +25°C	I <sub>DSS</sub>	_	_	-1	μA	$V_{DS} = -30V, V_{GS} = 0V$	
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 9)							
Gate Threshold Voltage	V <sub>GS(TH)</sub>	-1	1	-2.4	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	
Static Drain-Source On-Resistance	D		20	25	mΩ	$V_{GS} = -10V, I_D = -7A$	
Static Dialif-Source Off-Resistance	R <sub>DS(ON)</sub>	_	29	38	11122	$V_{GS} = -4.5V, I_D = -6.2A$	
Diode Forward Voltage	$V_{SD}$	_	-0.7	-1.2	V	$V_{GS} = 0V, I_{S} = -2.1A$	
DYNAMIC CHARACTERISTICS (Note 10)							
Input Capacitance	C <sub>iss</sub>	_	1241	_	pF	451/1/ 01/	
Output Capacitance	Coss	_	147	_	pF	$V_{DS} = -15V, V_{GS} = 0V$ f = 1.0MHz	
Reverse Transfer Capacitance	Crss	_	110	_	pF	1 = 1.000112	
Gate Resistance	$R_{G}$	_	15	_	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$	
Total Gate Charge (V <sub>GS</sub> = -10V)	Qg	_	22	_	nC		
Total Gate Charge (V <sub>GS</sub> = -4.5V)	Qg	_	11	_	nC	7	
Gate-Source Charge	Qgs	_	3.5	_	nC	V <sub>DS</sub> = -15V, I <sub>D</sub> = -7A	
Gate-Drain Charge	$Q_{gd}$	_	4.7	_	nC		
Turn-On Delay Time	t <sub>D(ON)</sub>	_	9.7	_	ns	V 40V V 45V	
Turn-On Rise Time	t <sub>R</sub>	-	17.1	_	ns	$V_{GS} = -10V, V_{DD} = -15V,$ $R_{GEN} = 6\Omega$	
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	60.5	_	ns	$-I_{D} = -7A$	
Turn-Off Fall Time	t <sub>F</sub>	_	40.4	_	ns		

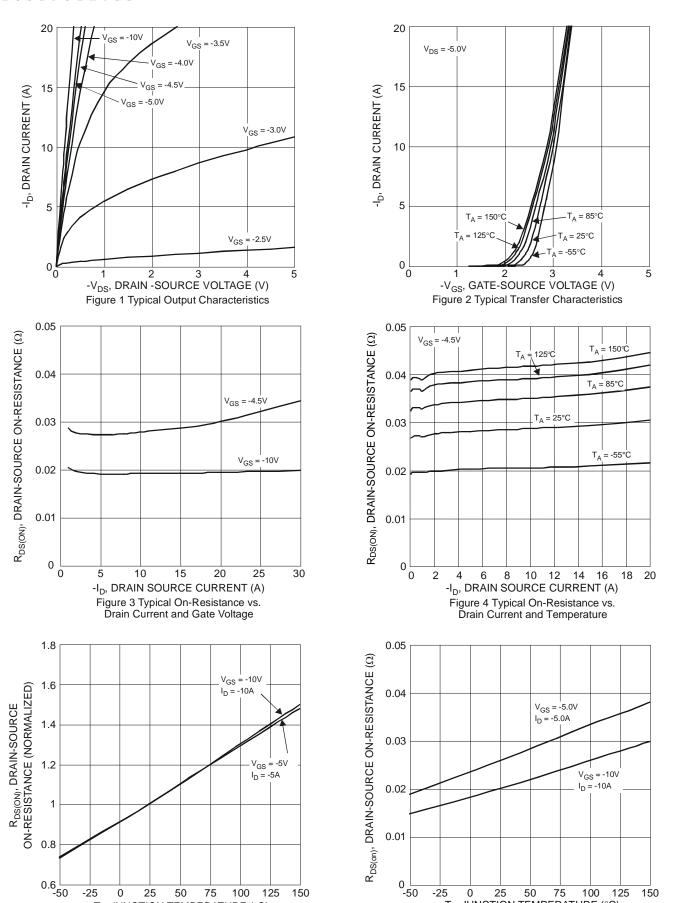
6. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout. Notes:

<sup>7.</sup> Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

<sup>8.</sup>  $I_{AS}$  and  $E_{AS}$  ratings are based on low frequency and duty cycles to keep  $T_J = +25$ °C.

Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing.





 $\mathsf{T}_\mathsf{J},\mathsf{JUNCTION}$  TEMPERATURE (°C) Figure 6 On-Resistance Variation with Temperature

50

25

75

100

125

25

50

T<sub>J</sub>, JUNCTION TEMPERATURE (°C)

Figure 5 On-Resistance Variation with Temperature

75

100

125



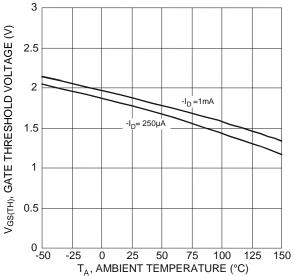


Figure 7 Gate Threshold Variation vs. Ambient Temperature

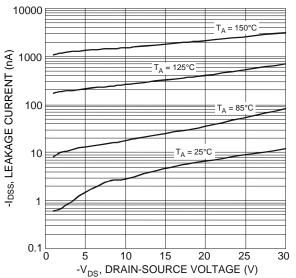
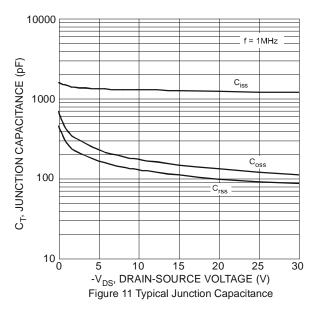


Figure 9 Typical Drain-Source Leakage Current vs. Voltage



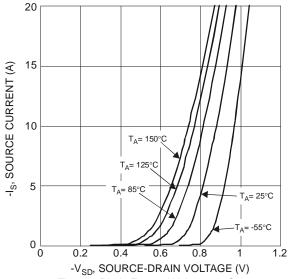
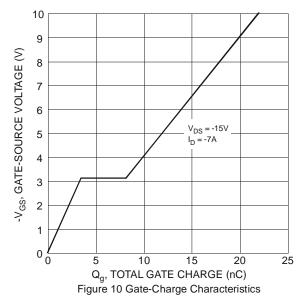


Figure 8 Diode Forward Voltage vs. Current



100
R<sub>DS(ON)</sub>
Limited

10
P<sub>W</sub> = 10s
P<sub>W</sub> = 10ms
P<sub>W</sub>

Figure 12 SOA, Safe Operation Area



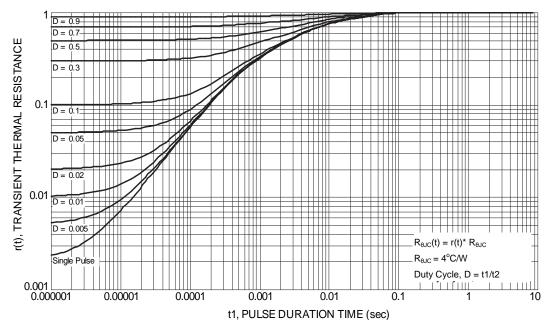


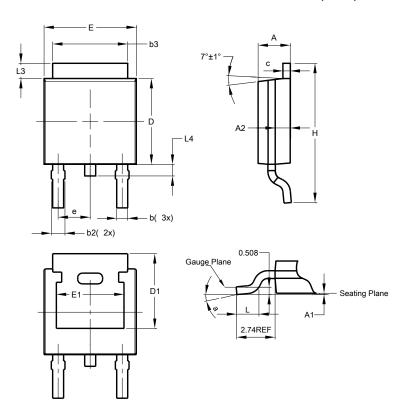
Figure 13 Transient Thermal Resistance



# **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### TO252 (DPAK)

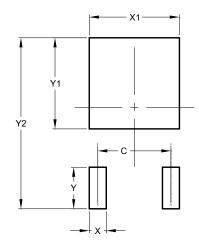


TO252 (DPAK)					
Dim	Min	Max	Тур		
Α	2.19	2.39	2.29		
<b>A</b> 1	0.00	0.13	0.08		
A2	0.97	1.17	1.07		
b	0.64	0.88	0.783		
b2	0.76	1.14	0.95		
b3	5.21	5.46	5.33		
С	0.45	0.58	0.531		
D	6.00	6.20	6.10		
D1	5.21	-	-		
е	-	-	2.286		
Е	6.45	6.70	6.58		
E1	4.32	-	-		
Н	9.40	10.41	9.91		
L	1.40	1.78	1.59		
L3	0.88	1.27	1.08		
L4	0.64	1.02	0.83		
а	0°	10°	-		
All Dimensions in mm					

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

## TO252 (DPAK)



Dimensions	Value (in mm)		
С	4.572		
Х	1.060		
X1	5.632		
Y	2.600		
Y1	5.700		
Y2	10.700		



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