



SINGLE P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} max	I _D max T _A = +25°C
	14mΩ @ V _{GS} = -10V	-12.0A
-30V	$25m\Omega$ @ $V_{GS} = -4.5V$	-8.5A

Description

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Backlighting
- Power Management Functions
- DC-DC Converters

Features

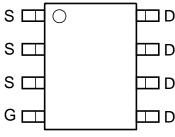
- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

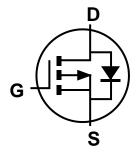
- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals Connections: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Lead Frame. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.074g (Approximate)



Top View







Equivalent Circuit

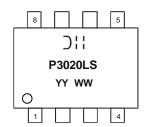
Ordering Information (Note 4)

Part Number	Case	Packaging
DMP3020LSS-13	SO-8	2500/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



);; = Manufacturer's Marking
P3020LS = Product Type Marking Code
YYWW = Date Code Marking
YY or YY = Year (ex: 16 = 2016)
WW = Week (01 to 53)



Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	-30	V
Gate-Source Voltage			V _{GSS}	±25	V
Drain Current (Note 5)	Steady State	T _A = +25°C T _A = +70°C	ID	-12 -9	А
Pulsed Drain Current (Note 6)			I _{DM}	-80	Α

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P_{D}	2.5	W
Thermal Resistance, Junction to Ambient	$R_{ hetaJA}$	50	°C/W
Operating and Storage Temperature Range	$T_{J_i}T_{STG}$	-55 to +150	°C

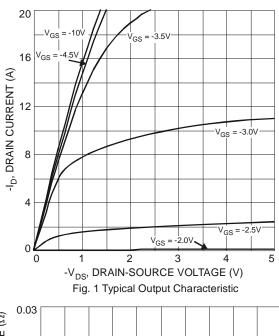
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

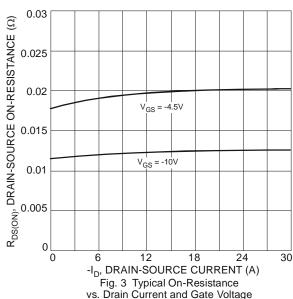
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	-30	_	_	V	$V_{GS} = 0V, I_D = -250\mu A$
Zero Gate Voltage Drain Current	I _{DSS}	_	_	-1	μA	$V_{DS} = -30V, V_{GS} = 0V$
Cata Sauraa Laakaga		_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
Gate-Source Leakage	I _{GSS}	_	_	±800		$V_{GS} = \pm 25V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	-1	_	-2	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$
Static Drain-Source On-Resistance	D	_	11.6 14	14	m()	$V_{GS} = -10V, I_D = -8A$
Static Drain-Source On-Resistance	R _{DS(ON)}		18.6	25		$V_{GS} = -4.5V, I_D = -5A$
Forward Transconductance	9 _{fs}		12	_	S	$V_{DS} = -10V, I_{D} = -12A$
Diode Forward Voltage (Note 7)	V_{SD}	-0.5	_	-1.1	V	$V_{GS} = 0V, I_{S} = -2A$
DYNAMIC CHARACTERISTICS						
Input Capacitance	C _{iss}		1802	_	pF	
Output Capacitance	Coss	_	415	_	pF	$V_{DS} = -15V$, $V_{GS} = 0V$, $f = 1.0MHz$
Reverse Transfer Capacitance	C _{rss}	_	295	_	pF	
Gate Resistance	R_{G}	_	2.3	_	Ω	$V_{GS} = 0V$, $V_{DS} = 0V$, $f = 1MHz$
SWITCHING CHARACTERISTICS						
Total Gate Charge	Qg	_	15.3		nC	$V_{DS} = -15V$, $V_{GS} = -4.5V$, $I_{D} = -8A$
Total Gate Charge			30.7			$V_{DS} = -15V$, $V_{GS} = -10V$, $I_{D} = -8A$
Gate-Source Charge	Q_{gs}		3.5	_		$V_{DS} = -15V$, $V_{GS} = -10V$, $I_{D} = -8A$
Gate-Drain Charge	Q_{gd}		7.9	_		$V_{DS} = -15V$, $V_{GS} = -10V$, $I_{D} = -8A$
Turn-On Delay Time	t _{D(ON)}	_	5.1	_		
Rise Time	t _R	_	8	_		$V_{GS} = -10V, V_{DS} = -15V,$
Turn-Off Delay Time	t _{D(OFF)}	_	46	_	ns	$R_D = 15\Omega$, $R_G = 6\Omega$
Fall Time	t _F	1	30	_		

Notes: 5. Device mounted on 2 oz. copper pads on FR-4 PCB with $R_{\theta JA}$ = 50°C/W.

^{6.} Pulse width ≤10µs, Duty Cycle ≤1%.
7. Short duration pulse test used to minimize self-heating effect.







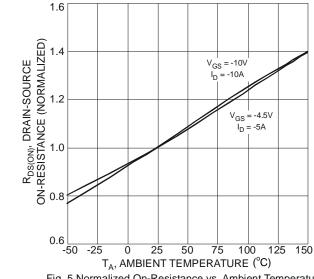
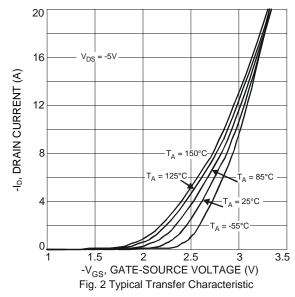
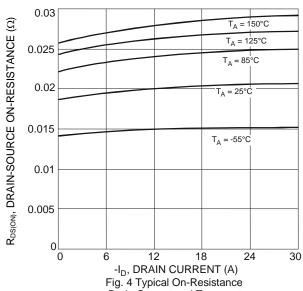
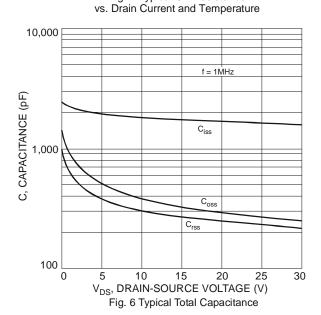


Fig. 5 Normalized On-Resistance vs. Ambient Temperature









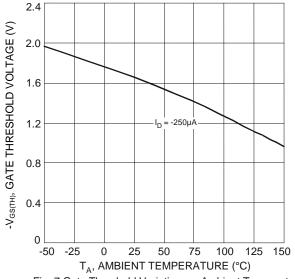
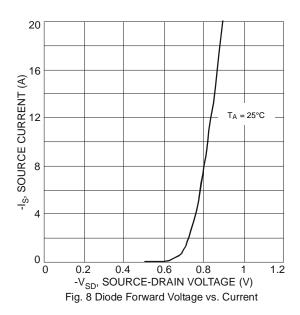
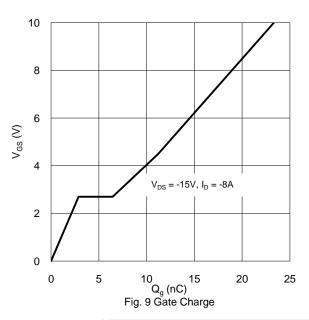
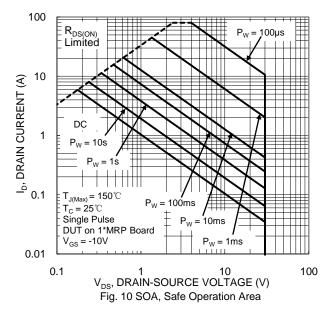


Fig. 7 Gate Threshold Variation vs. Ambient Temperature







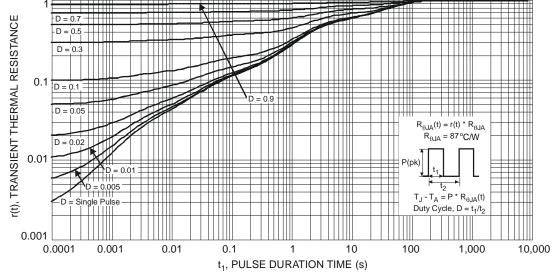


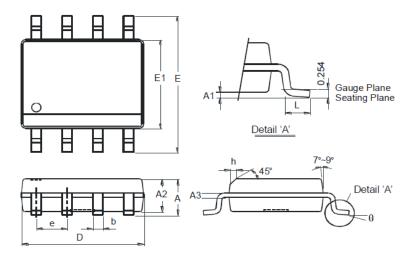
Fig. 11 Transient Thermal Response



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SO-8

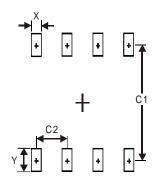


	SO-8			
Dim	Min	Max		
Α	-	1.75		
A1	0.10	0.20		
A2	1.30	1.50		
A3	0.15	0.25		
b	0.3	0.5		
D	4.85	4.95		
Е	5.90	6.10		
E1	3.85	3.95		
е	1.27 Typ			
h		0.35		
L	0.62	0.82		
θ	0°	8°		
All Dimensions in mm				

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SO-8



Dimensions	Value (in mm)
Χ	0.60
Υ	1.55
C1	5.4
C2	1.27



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