



20V P-CHANNEL ENHANCEMENT MODE MOSFET POWERDI®

Product Summary

V _{(BR)DSS}	R _{DS(ON)} Max	I _D Max T _C = +25°C
-20V	$6.7 \text{m}\Omega @ V_{GS} = -4.5 \text{V}$	-40A
-200	$9.0 \text{m}\Omega$ @ $V_{GS} = -2.5V$	-40A

Description

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

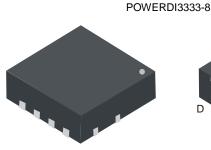
- Load Switch
- Power Management Functions

Features

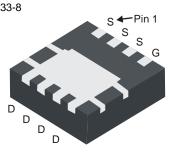
- Low R_{DS(ON)} ensures on state losses are minimized
- Small form factor thermally efficient package enables higher density end products
- Occupies just 33% of the board area occupied by SO-8 enabling smaller end product
- ESD HBM Protected up to 1KV
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Mechanical Data

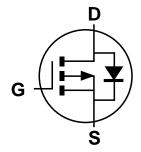
- Case: POWERDI3333-8
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.008 grams (Approximate)







Bottom View



Equivalent Circuit

Ordering Information (Note 4)

Part Number	Case	Packaging
DMP26M7UFG-7	POWERDI3333-8	2000/Tape & Reel
DMP26M7UFG-13	POWERDI3333-8	3000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information





S42 or S47= Product Type Marking Code YYWW = Date Code Marking YY = Last Digit of Year (ex: 14 = 2014) WW = Week Code (01 to 53)



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	V_{DSS}	-20	V		
Gate-Source Voltage	V_{GSS}	±10	V		
Continuous Drain Current (Note 5) V _{GS} = -4.5V	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$ $T_C = +25^{\circ}C$	I _D	-18.0 -14.5 -40	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	-80	Α		
Maximum Continuous Body Diode Forward Current (Note 5)			Is	-2.2	Α
Avalanche Current (Note 7) L=0.1mH			I _{AS}	-23	Α
Avalanche Energy (Note 7) L=0.1mH			E _{AS}	28	mJ

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Total Bower Dissinction (Note 5)	$T_A = +25^{\circ}C$	ם	2.3	- W
Total Power Dissipation (Note 5)	$T_C = +25$ °C	P_{D}	41	
Thermal Resistance, Junction to Ambient	(Note 5)	D	56	°C/W
Thermal Resistance, Junction to Ambient	(Note 6)	$R_{\theta JA}$	124	
Thermal Resistance, Junction to Case	R ₀ JC	8.8		
Operating and Storage Temperature Range	$T_{J_i}T_{STG}$	-55 to +150	°C	

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

		-				-	
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV _{DSS}	-20	_	_	V	$V_{GS} = 0V, I_D = -250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}		_	-1	μA	$V_{DS} = -16V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 8V$, $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V _{GS(TH)}	-0.4	_	-1.0	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	
			4.2	6.7		$V_{GS} = -4.5V$, $I_{D} = -15A$	
Static Drain-Source On-Resistance	R _{DS(ON)}		5.4	9.0	mΩ	$V_{GS} = -2.5V$, $I_D = -10A$	
		-	7	_		$V_{GS} = -1.8V, I_{D} = -1A$	
Diode Forward Voltage	V_{SD}	_	-0.7	-1.2	V	$V_{GS} = 0V, I_{S} = -10A$	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	Ciss		5940	_		V _{DS} = -10V, V _{GS} = 0V f = 1.0MHz	
Output Capacitance	Coss	_	835	_	pF		
Reverse Transfer Capacitance	Crss	_	728	_			
Gate Resistance	Rg	_	3.0	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1.0MHz$	
Total Gate Charge (V _{GS} = -4.5V)	Q_g	_	75	_			
Total Gate Charge (V _{GS} = -10V)	Q_g	_	156	_	nC	$V_{DD} = -10V$, $I_{D} = -20A$	
Gate-Source Charge	Q_{gs}	_	8.8	_	IIC	VDD = -10V, ID = -20A	
Gate-Drain Charge	Q_{qd}	_	22	_			
Turn-On Delay Time	t _{D(ON)}	_	10.7	_		$V_{GS} = -4.5V, V_{DD} = -10V,$ $R_G = 1\Omega, I_D = -10A$	
Turn-On Rise Time	t _R	_	23	_			
Turn-Off Delay Time	t _{D(OFF)}	_	121	_	ns		
Turn-Off Fall Time	t _F	-	109	_	1		
Reverse Recovery Time	t _{RR}	_	60	_	ns	I _F = -10A, di/dt = 100A/μs	
Reverse Recovery Charge	Q _{RR}	_	47	_	nC	I _F = -10A, di/dt = 100A/μs	

Notes:

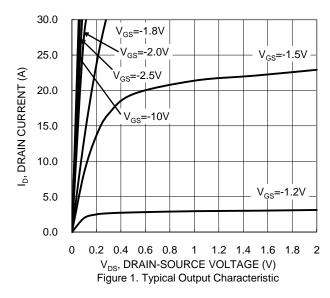
5. R_{BJA} is determined with the device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate. R_{BJC} is guaranteed by design while $R_{\theta JA}$ is determined by the user's board design.

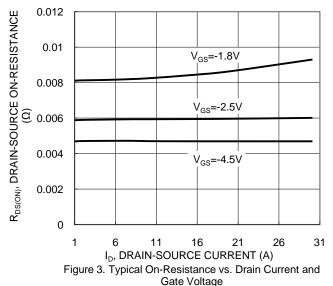
2 of 6

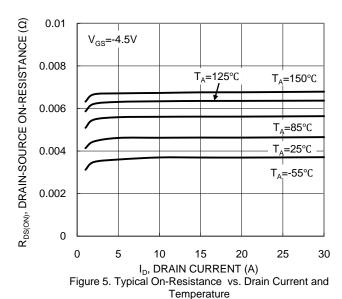
www.diodes.com

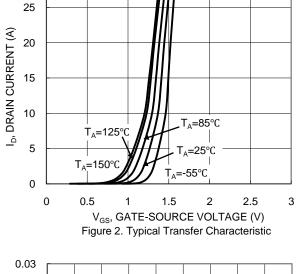
- 6. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
- 7 .UIS in production with L =0.1mH, $T_J = +25^{\circ} C$
- 8. Short duration pulse test used to minimize self-heating effect.
- 9. Guaranteed by design. Not subject to product testing.





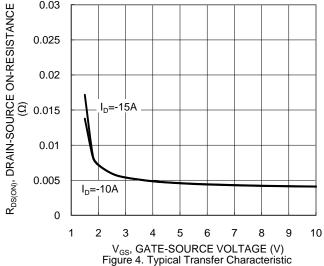






30

V_{DS}=-5.0V



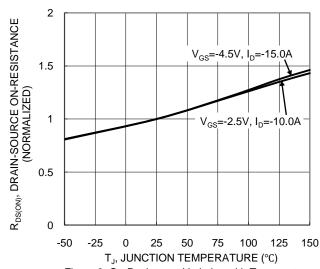


Figure 6. On-Resistance Variation with Temperature



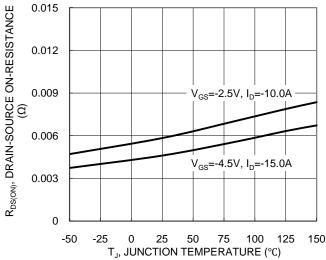
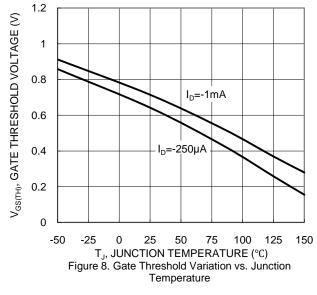
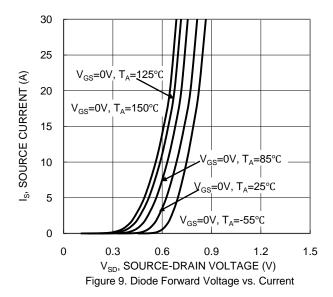
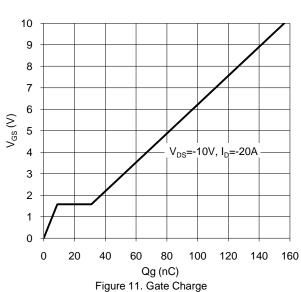
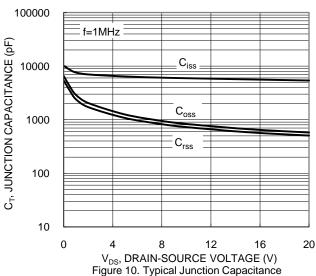


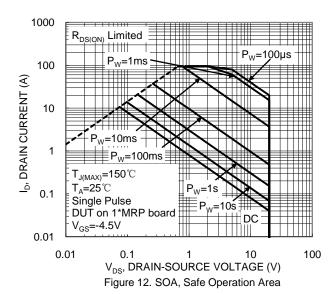
Figure 7. On-Resistance Variation with Temperature



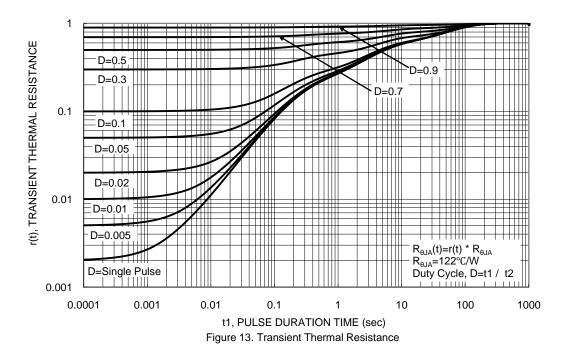






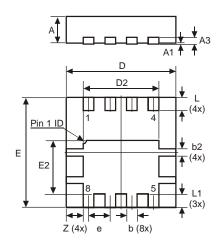






Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.

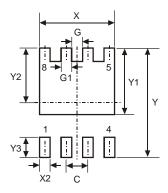


POWERDI3333-8					
Dim	Min	Max	Тур		
D	3.25	3.35	3.30		
Е	3.25	3.35	3.30		
D2	2.22	2.32	2.27		
E2	1.56	1.66	1.61		
Α	0.75	0.85	0.80		
A1	0	0.05	0.02		
A3	1	1	0.203		
b	0.27	0.37	0.32		
b2	-	-	0.20		
٦	0.35	0.45	0.40		
L1	_	_	0.39		
е	_	_	0.65		
Z	_	Ī	0.515		
All Dimensions in mm					



Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)			
С	0.650			
G	0.230			
G1	0.420			
Y	3.700			
Y1	2.250			
Y2	1.850			
Y3	0.700			
Х	2.370			
X2	0.420			

IMPORTANT NOTICE

DIODES INCORPORATED MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. Diodes Incorporated does not assume any liability arising out of the application or use of this document or any product described herein; neither does Diodes Incorporated convey any license under its patent or trademark rights, nor the rights of others. Any Customer or user of this document or products described herein in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on Diodes Incorporated website, harmless against all damages.

Diodes Incorporated does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel. Should Customers purchase or use Diodes Incorporated products for any unintended or unauthorized application, Customers shall indemnify and hold Diodes Incorporated and its representatives harmless against all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized application.

Products described herein may be covered by one or more United States, international or foreign patents pending. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks.

This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes Incorporated.

LIFE SUPPORT

Diodes Incorporated products are specifically not authorized for use as critical components in life support devices or systems without the express written approval of the Chief Executive Officer of Diodes Incorporated. As used herein:

- A. Life support devices or systems are devices or systems which:
 - 1. are intended to implant into the body, or
 - 2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.
- B. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or to affect its safety or effectiveness.

Customers represent that they have all necessary expertise in the safety and regulatory ramifications of their life support devices or systems, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of Diodes Incorporated products in such safety-critical, life support devices or systems, notwithstanding any devices- or systems-related information or support that may be provided by Diodes Incorporated. Further, Customers must fully indemnify Diodes Incorporated and its representatives against any damages arising out of the use of Diodes Incorporated products in such safety-critical, life support devices or systems.

6 of 6

www.diodes.com

Copyright © 2015, Diodes Incorporated

www.diodes.com