

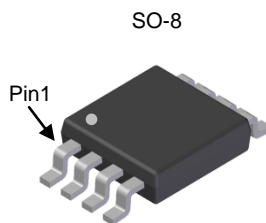
## Product Summary

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> MAX	I <sub>D</sub> T <sub>A</sub> = +25°C
40V	24mΩ @V <sub>GS</sub> = 10V	7.5A
	32mΩ @V <sub>GS</sub> = 4.5V	6.5A

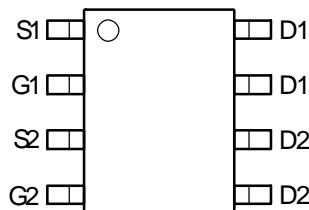
## Description and Applications

This MOSFET is designed to meet the stringent requirements of Automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

- Motor Control
- Backlighting
- Power Management Functions
- DC-DC Converters



Top View



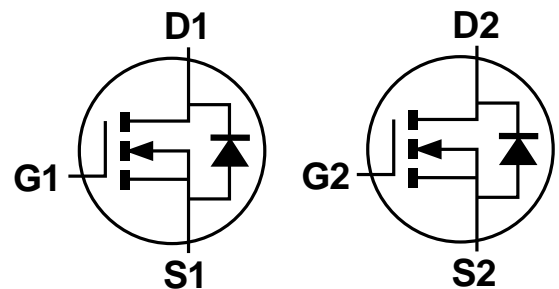
Top View  
Internal Schematic

## Features

- Rated to +175°C – Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching – Ensures More Reliable And Robust End Application
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**
- **PPAP Capable (Note 4)**

## Mechanical Data

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram Below
- Terminals: Finish — Matte Tin Annealed Over Copper Lead Frame. Solderable per MIL-STD-202, Method 208 **(e3)**
- Weight: 0.074 grams (Approximate)



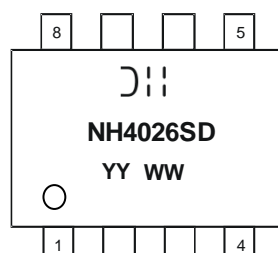
Equivalent Circuit

## Ordering Information (Note 5)

Part Number	Case	Packaging
DMNH4026SSDQ-13	SO-8	2,500/Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
  2. See [http://www.diodes.com/quality/lead\\_free.html](http://www.diodes.com/quality/lead_free.html) for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to [http://www.diodes.com/product\\_compliance\\_definitions.html](http://www.diodes.com/product_compliance_definitions.html).
  5. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

## Marking Information



⌋⌋⌋ = Manufacturer's Marking  
 NH4026SD = Product Type Marking Code  
 YYWW = Date Code Marking  
 YY = Year (ex: 16 = 2016)  
 WW = Week (01 to 53)

**Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V <sub>DSS</sub>	40	V
Gate-Source Voltage			V <sub>GSS</sub>	±20	V
Continuous Drain Current (Note 7) V <sub>GS</sub> = 10V	Steady State	T <sub>A</sub> = +25°C T <sub>A</sub> = +100°C	I <sub>D</sub>	7.5 5.3	A
Maximum Continuous Body Diode Forward Current (Note 7)			I <sub>S</sub>	2.5	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I <sub>DM</sub>	60	A
Avalanche Current (Note 8) L = 0.1mH			I <sub>AS</sub>	18	A
Avalanche Energy (Note 8) L = 0.1mH			E <sub>AS</sub>	18	mJ

**Thermal Characteristics**

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 6)	T <sub>A</sub> = +25°C	P <sub>D</sub>	1.5	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R <sub>θJA</sub>	101	°C/W
	t < 10s		59	
Total Power Dissipation (Note 7)	T <sub>A</sub> = +25°C	P <sub>D</sub>	2.0	W
Thermal Resistance, Junction to Ambient (Note 7)	Steady State	R <sub>θJA</sub>	74	°C/W
	t < 10s		43	
Thermal Resistance, Junction to Case (Note 7)		R <sub>θJC</sub>	10.5	
Operating and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	-55 to +175	°C

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 9)</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	40	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250µA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	—	—	1	µA	V <sub>DS</sub> = 40V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	—	—	±100	nA	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V
<b>ON CHARACTERISTICS (Note 9)</b>						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	1	—	3	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250µA
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	—	15	24	mΩ	V <sub>GS</sub> = 10V, I <sub>D</sub> = 6A
		—	20	32		V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 5A
Diode Forward Voltage	V <sub>SD</sub>	—	0.7	1.0	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 1.0A
<b>DYNAMIC CHARACTERISTICS (Note 10)</b>						
Input Capacitance	C <sub>iss</sub>	—	1060	—	pF	V <sub>DS</sub> = 20V, V <sub>GS</sub> = 0V, f = 1.0MHz
Output Capacitance	C <sub>oss</sub>	—	84	—		
Reverse Transfer Capacitance	C <sub>rss</sub>	—	58	—		
Gate Resistance	R <sub>g</sub>	—	1.6	—	Ω	V <sub>DS</sub> = 0V, V <sub>GS</sub> = 0V, f = 1.0MHz
Total Gate Charge (V <sub>GS</sub> = 4.5V)	Q <sub>g</sub>	—	8.8	—	nC	V <sub>DS</sub> = 20V, I <sub>D</sub> = 8A
Total Gate Charge (V <sub>GS</sub> = 10V)	Q <sub>g</sub>	—	19.1	—		
Gate-Source Charge	Q <sub>gs</sub>	—	3.0	—		
Gate-Drain Charge	Q <sub>gd</sub>	—	2.5	—		
Turn-On Delay Time	t <sub>D(ON)</sub>	—	5.3	—	ns	V <sub>DD</sub> = 25V, R <sub>L</sub> = 2.5Ω V <sub>GS</sub> = 10V, R <sub>g</sub> = 3Ω
Turn-On Rise Time	t <sub>r</sub>	—	7.1	—		
Turn-Off Delay Time	t <sub>D(OFF)</sub>	—	15.1	—		
Turn-Off Fall Time	t <sub>f</sub>	—	4.8	—		
Body Diode Reverse Recovery Time	t <sub>RR</sub>	—	10.5	—	ns	I <sub>F</sub> = 8A, di/dt = 100A/µs
Body Diode Reverse Recovery Charge	Q <sub>RR</sub>	—	4.15	—	nC	I <sub>F</sub> = 8A, di/dt = 100A/µs

- Notes:
6. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
  7. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1 inch square copper plate.
  8. I<sub>AS</sub> and E<sub>AS</sub> rating are based on low frequency and duty cycles to keep T<sub>J</sub> = +25°C.
  9. Short duration pulse test used to minimize self-heating effect.
  10. Guaranteed by design. Not subject to product testing.

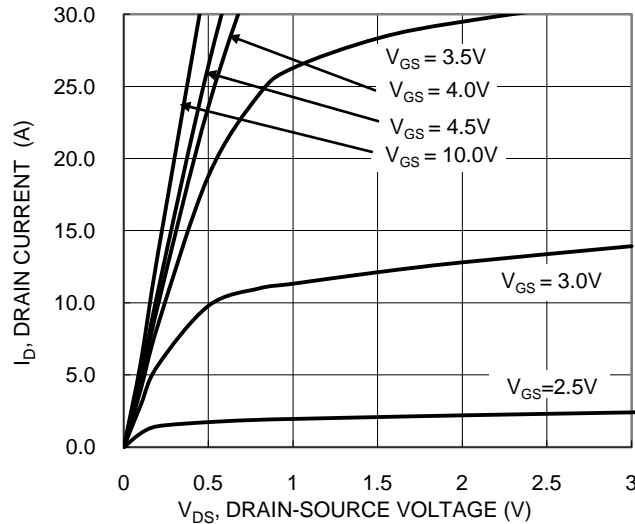


Figure 1. Typical Output Characteristic

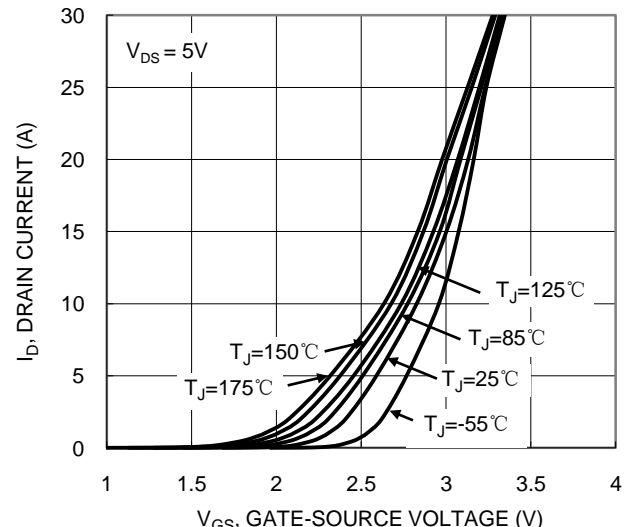


Figure 2. Typical Transfer Characteristic

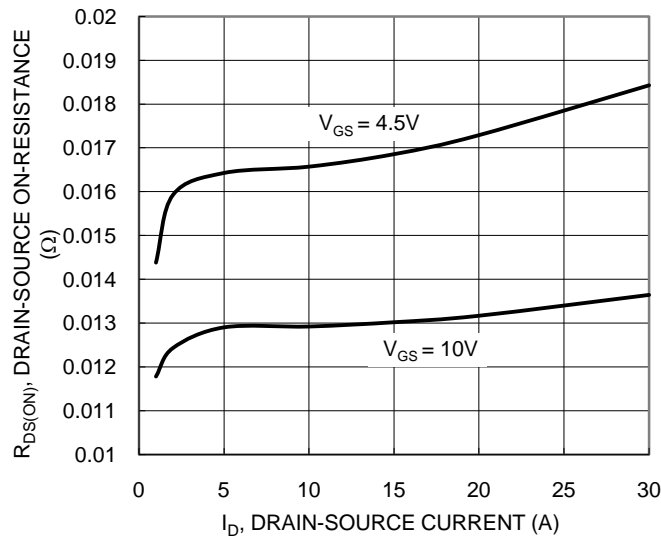


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

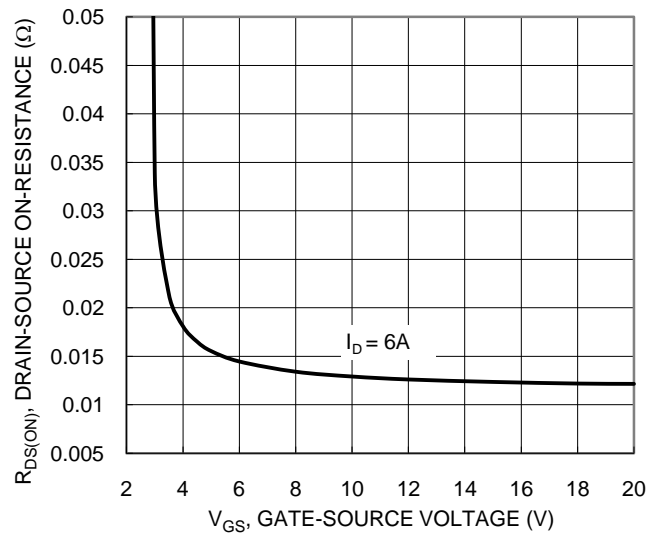


Figure 4. Typical Transfer Characteristic

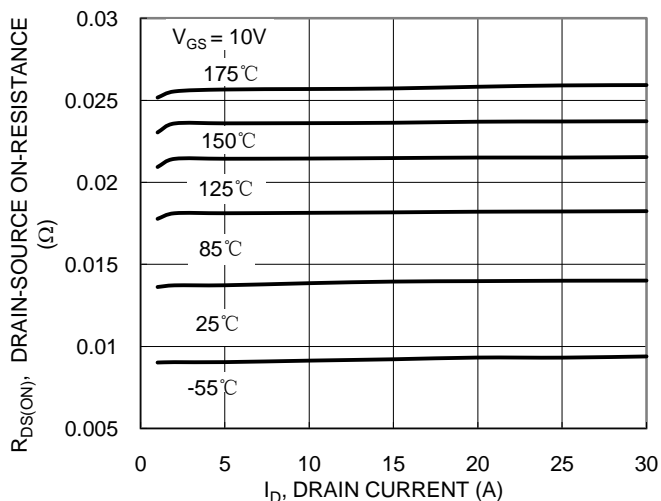


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

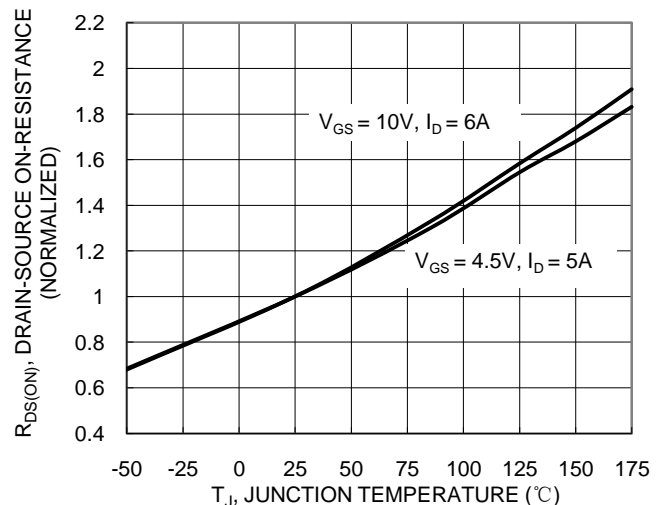
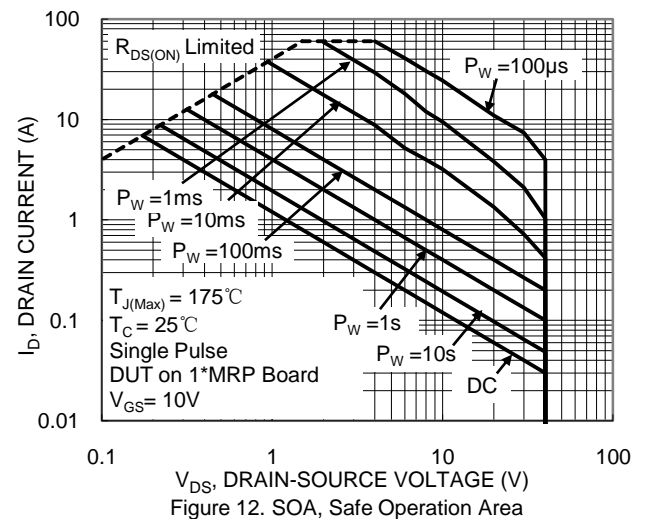
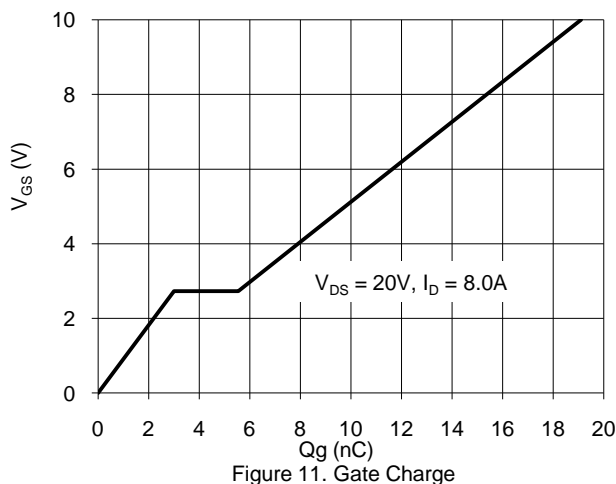
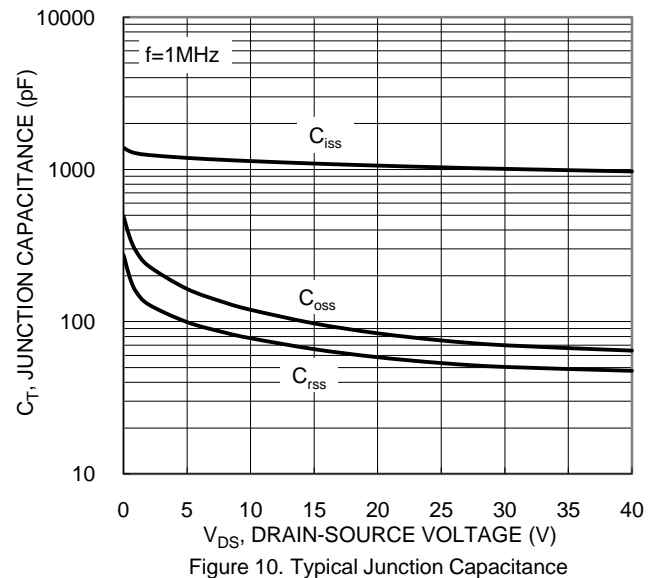
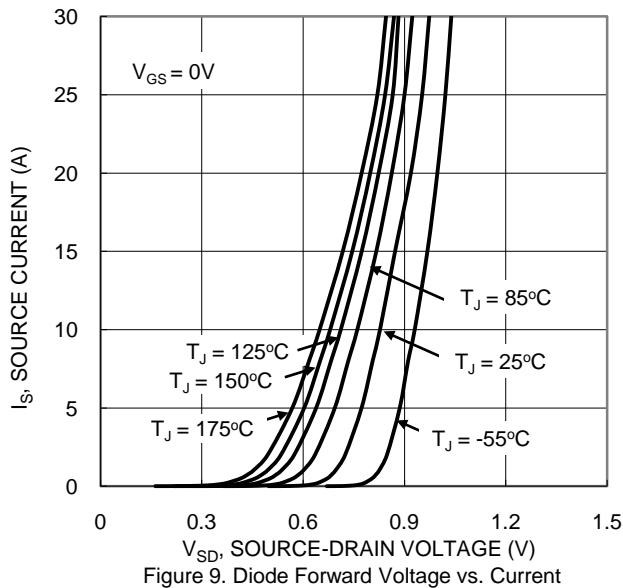
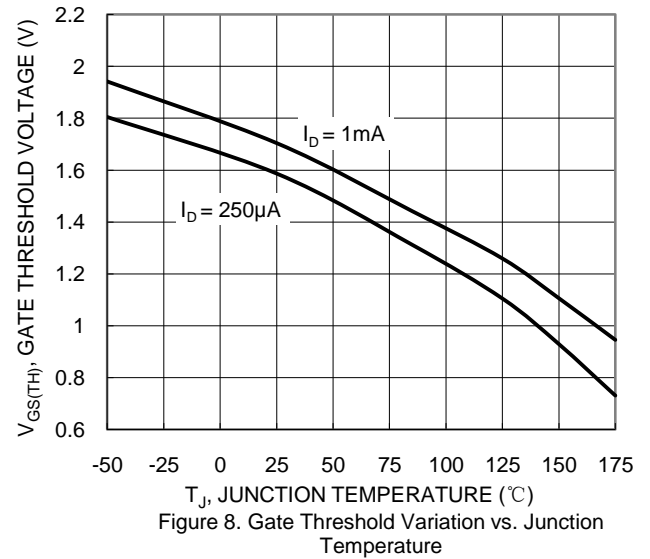
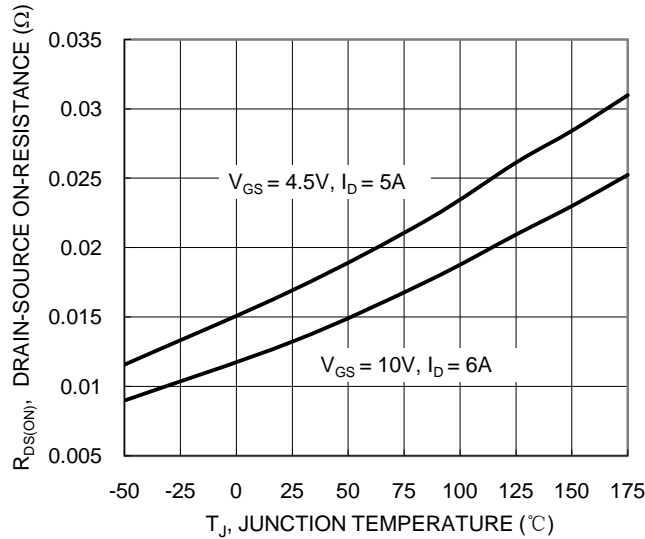


Figure 6. On-Resistance Variation with Temperature



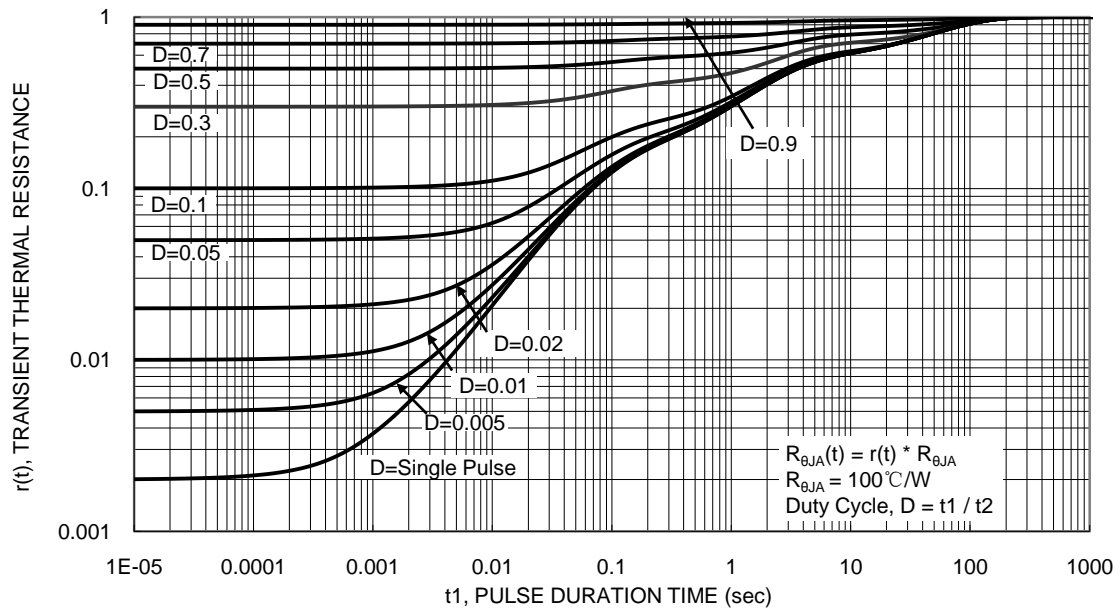
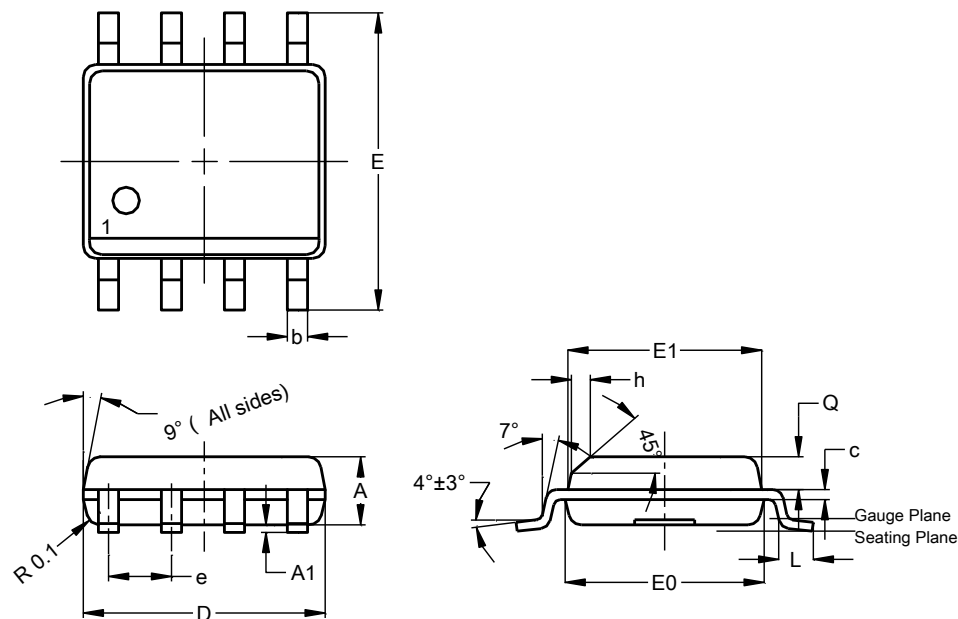


Figure 13. Transient Thermal Resistance

## Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

### SO-8

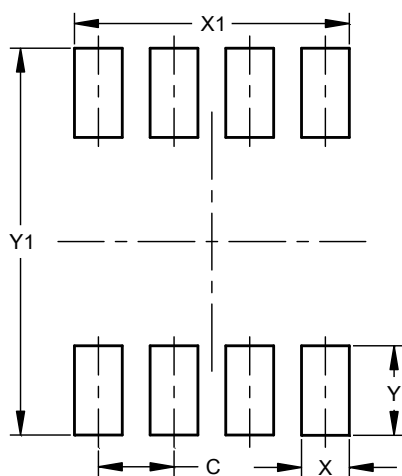


SO-8			
Dim	Min	Max	Typ
A	1.40	1.50	1.45
A1	0.10	0.20	0.15
b	0.30	0.50	0.40
c	0.15	0.25	0.20
D	4.85	4.95	4.90
E	5.90	6.10	6.00
E1	3.80	3.90	3.85
E0	3.85	3.95	3.90
e	--	--	1.27
h	-	--	0.35
L	0.62	0.82	0.72
Q	0.60	0.70	0.65
All Dimensions in mm			

## Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

### SO-8



Dimensions	Value (in mm)
C	1.27
X	0.802
X1	4.612
Y	1.505
Y1	6.50

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