



#### **DUAL N-CHANNEL ENHANCEMENT MODE MOSFET**

## **Product Summary**

V <sub>(BR)DSS</sub>	R <sub>DS(ON)</sub> max	I <sub>D</sub> max T <sub>A</sub> = +25°C
60V	$2\Omega$ @ $V_{GS} = 5.0V$	350mA
00 V	$2.5\Omega$ @ $V_{GS} = 2.5V$	SSUIIA

### **Description**

This MOSFET is designed to minimize the on-state resistance (R<sub>DS(ON)</sub>) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

### **Applications**

- Motor Control
- Power Management Functions

### **Features**

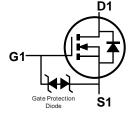
- Dual N-Channel MOSFET
- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Ultra-Small Surface Mount Package
- ESD Protected Up To 2kV
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

#### **Mechanical Data**

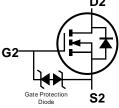
- Case: SOT363
- Case Material: Molded Plastic. "Green" Molding Compound.
   UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish Annealed over Alloy 42 Leadframe.
   Solderable per MIL-STD-202, Method 208 (3)
- Terminal Connections: See Diagram
- Weight: 0.006 grams (Approximate)



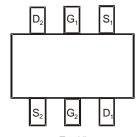




O1 N-Channel



Q2 N-Channel



Top View

Equivalent Circuit

Top View Pin out

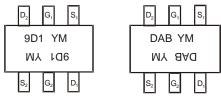
### Ordering Information (Note 4)

Part Number	Case	Packaging
DMN61D9UDW-7	SOT363	3000/Tape & Reel
DMN61D9UDW-13	SOT363	10000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

### Marking Information



9D1 or DAB = Product Type Marking Code YM = Date Code Marking Y or Y = Year (ex: C = 2015) M = Month (ex: 9 = September)

Date Code Key

24.0 0040.												
Year	2014	2015	2016	2017	2018	2019	2020	2021	2022	2023	2024	2025
Code	В	С	D	Е	F	G	Н		J	K	L	М
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



# **Maximum Ratings** ( $@T_A = +25^{\circ}C$ , unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage			$V_{DSS}$	60	V
Gate-Source Voltage			V <sub>GSS</sub>	±20	V
Continuous Drain Current (Note 6) $V_{GS} = 5V$ Steady $T_A = +25^{\circ}C$ State $T_A = +70^{\circ}C$			I <sub>D</sub>	350 290	mA
Maximum Continuous Body Diode Forward Current	t (Note 6)	Is	0.4	Α	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%	I <sub>DM</sub>	1.2	Α		

# Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		$P_{D}$	320	mW
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	397	°C/W
Total Power Dissipation (Note 6)		$P_{D}$	410	mW
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\theta JA}$	306	°C/W
Operating and Storage Temperature Range		$T_{J_i}T_{STG}$	-55 to +150	°C

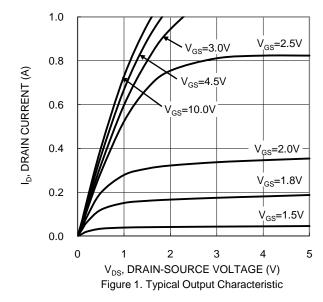
# **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

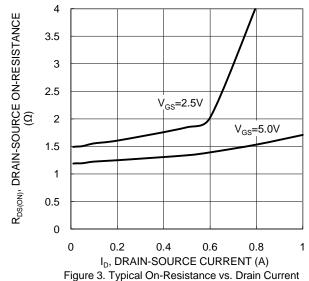
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)				•	•	
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	60	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_	_	1.0	μΑ	$V_{DS} = 60V$ , $V_{GS} = 0V$
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±10	μΑ	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	$V_{GS(TH)}$	0.5	_	1.0	V	$V_{DS} = 10V, I_{D} = 250\mu A$
			1.2	2.0		$V_{GS} = 5.0V, I_D = 0.05A$
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>		1.6	2.5	Ω	$V_{GS} = 2.5V, I_D = 0.05A$
			2.5	3.5		$V_{GS} = 1.8V, I_D = 0.05A$
Forward Transconductance	Y <sub>fs</sub>	200	_	_	mS	$V_{DS} = 10V, I_{D} = 0.2A$
Diode Forward Voltage	V <sub>SD</sub>	_	0.75	1.4	V	$V_{GS} = 0V, I_{S} = 115mA$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C <sub>iss</sub>	_	28.5	-	рF	.,
Output Capacitance	Coss	_	3.9	_	рF	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V f = 1.0MHz
Reverse Transfer Capacitance	$C_{rss}$	_	2.5	_	рF	1 - 1.000112
Gate Resistance	$R_g$	_	65	_	Ω	$f = 1MHz$ , $V_{GS} = 0V$ , $V_{DS} = 0V$
Total Gate Charge	Qg	_	0.4	_	nC	457777 4077
Gate-Source Charge	Q <sub>qs</sub>	_	0.1	_	nC	$V_{GS} = 4.5V, V_{DS} = 10V,$
Gate-Drain Charge	Q <sub>gd</sub>	_	0.1	_	nC	I <sub>D</sub> = 250mA
Turn-On Delay Time	t <sub>D(ON)</sub>	_	2.1	-	ns	
Turn-On Rise Time	t <sub>R</sub>	_	1.8	-	ns	$V_{DD} = 30V, V_{GS} = 10V,$
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	14.4	_	ns	$R_G = 25\Omega$ , $I_D = 200 \text{mA}$
Turn-Off Fall Time	t <sub>F</sub>	_	8.4	_	ns	

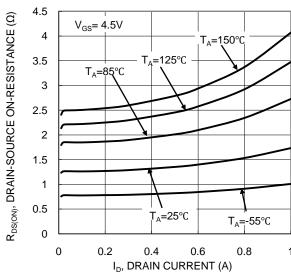
Notes:

- 5. Device mounted on FR-4 PCB, with minimum recommended pad layout.6. Device mounted on 1" x 1" FR-4 PCB with high coverage 2oz. Copper, single sided.
- 7. Short duration pulse test used to minimize self-heating effect.
- 8. Guaranteed by design. Not subject to product testing.



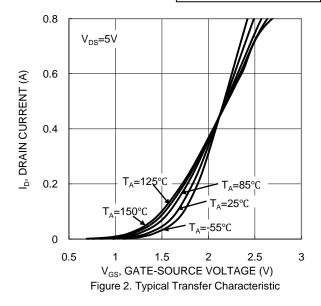


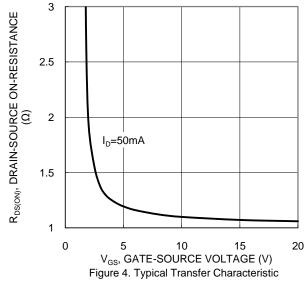


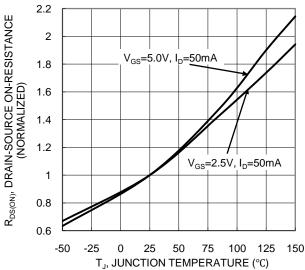


and Gate Voltage

Figure 5. Typical On-Resistance vs. Drain Current and Temperature







T<sub>J</sub>, JUNCTION TEMPERATURE (°C)
Figure 6. On-Resistance Variation with Junction
Temperature



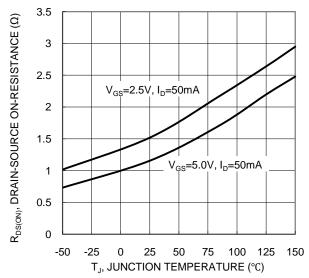
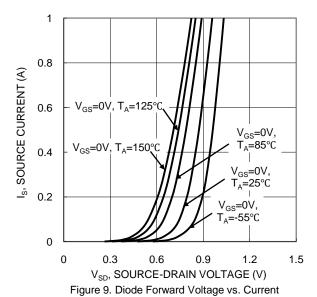
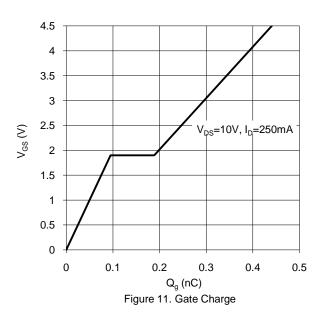


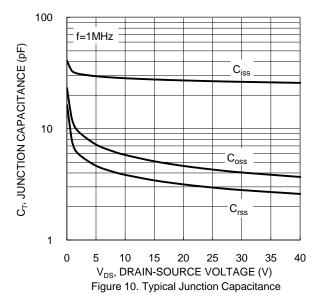
Figure 7. On-Resistance Variation with Junction Temperature

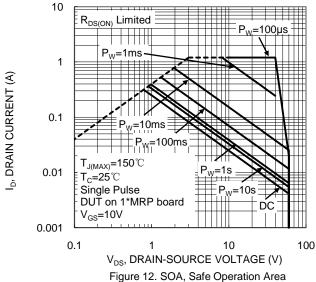




1.2  $V_{\text{GS(TH)}},$  GATE THRESHOLD VOLTAGE (V) 1  $I_D=1mA$ 0.8  $I_{D} = 250 \mu A$ 0.6 0.4 -50 -25 0 25 50 75 100 125 150 T<sub>.i</sub>, JUNCTION TEMPERATURE (°C)

Figure 8. Gate Threshold Variation vs. Junction Temperature







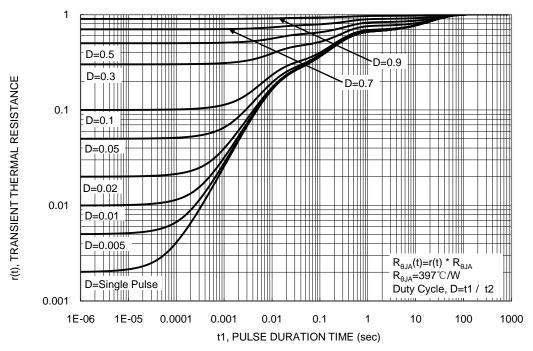
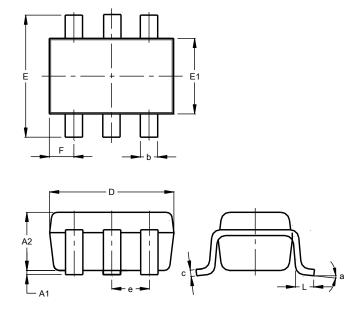


Figure 13. Transient Thermal Resistance

# **Package Outline Dimensions**

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.

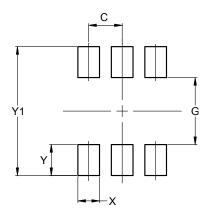


SOT363								
Dim	Min Max Typ							
A1	0.00	0.10	0.05					
A2	0.90	1.00	1.00					
b	0.10	0.30	0.25					
С	0.10	0.22	0.11					
D	1.80	2.20	2.15					
Е	2.00	2.20	2.10					
E1	1.15	1.30						
е	0.650 BSC							
F	0.40 0.45 0.425							
L	0.25 0.40 0.30							
а	<b>a</b> 8°							
All Dimensions in mm								



### Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
С	0.650
G	1.300
X	0.420
Υ	0.600
Y1	2.500

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