



### N-CHANNEL ENHANCEMENT MODE MOSFET

### **Product Summary**

BV <sub>DSS</sub>	Rds(on) Max	I <sub>D</sub> Max T <sub>A</sub> = +25°C
60V	2.0Ω @ Vgs = 10V	300mA

## **Description and Applications**

This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

- Motor controls
- Power-management functions
- Backlighting

### **Features and Benefits**

- Low On-Resistance: RDS(ON)
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The DMN601WKQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

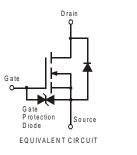
https://www.diodes.com/quality/product-definitions/

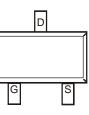
### **Mechanical Data**

- Package: SOT323
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Alloy 42 Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Terminal Connections: See Diagram
- Weight: 0.006 grams (Approximate)









Top View Pin Out Configuration

## Ordering Information (Note 4)

Part Number	Packago	Packing		
Fait Nulliber	Package	Qty.	Carrier	
DMN601WKQ-7	SOT323	3000	Tape & Reel	
DMN601WKQ-13	SOT323	10,000	Tape & Reel	

SOT323

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and

Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

## **Marking Information**

Date Code Key		к7	K7K = Product Type Marking Code     YM = Date Code Marking     Y or Y = Year (ex: K = 2023)     M = Month (ex: 8 = August)									
Year	2016	-	2023	2024	2025	2026	2027	2028	2029	2030	2031	2032
Code	D	-	K	L	М	N	Р	R	S	Т	U	V
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



# Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units	
Drain-Source Voltage		Vdss	60	V
Gate-Source Voltage		V <sub>GSS</sub>	±20	V
Drain Current (Note 5)	Continuous Pulsed (Note 6)	lo	300 800	mA

### **Thermal Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 5)	PD	200	mW
Thermal Resistance, Junction to Ambient	R <sub>0JA</sub>	625	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-65 to +150	°C

### Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

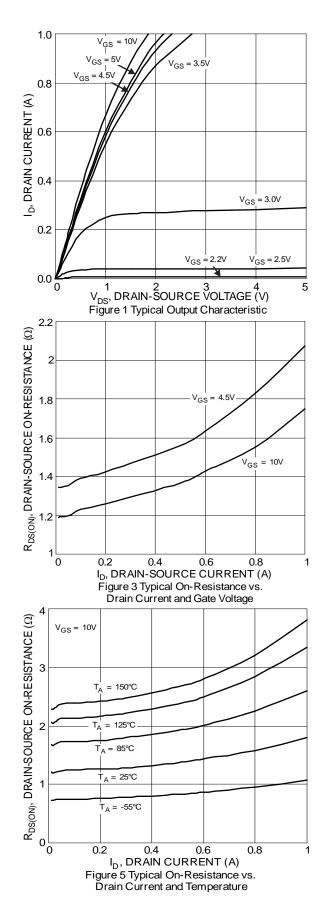
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition		
OFF CHARACTERISTICS (Note 7)								
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	60			V	$V_{GS} = 0V, I_{D} = 10\mu A$		
Zero Gate Voltage Drain Current	IDSS			1.0	μA	V <sub>DS</sub> = 60V, V <sub>GS</sub> = 0V		
Gate-Source Leakage	lgss	_		±10	μA	$V_{GS} = \pm 20V, V_{DS} = 0V$		
ON CHARACTERISTICS (Note 7)	ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	Vgs(th)	1.0	1.6	2.5	V	V <sub>DS</sub> = 10V, I <sub>D</sub> = 1mA		
Chatia Drain Caura On Desistance	6			2.0	0	Vgs = 10V, Id = 0.5A		
Static Drain-Source On-Resistance	RDS(ON)	_		3.0	Ω	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 0.2A		
Forward Transfer Admittance	Yfs	80			ms	V <sub>DS</sub> = 10V, I <sub>D</sub> = 0.2A		
DYNAMIC CHARACTERISTICS (Note 8)								
Input Capacitance	Ciss		—	50	pF			
Output Capacitance	Coss	_		25	pF	V <sub>DS</sub> = 25V, V <sub>GS</sub> = 0V, f = 1.0MHz		
Reverse Transfer Capacitance	Crss	_		5.0	pF	]		

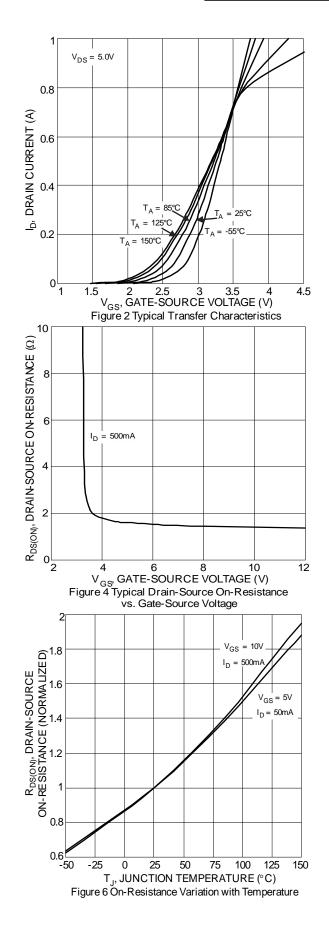
5. Device mounted on FR-4 PCB.

Notes:

Bevice induited of 11(4+1) Cole
Pulse width ≤ 10µs. Duty Cycle ≤ 1%.
Short duration pulse test used to minimize self-heating effect.
Guaranteed by design. Not subject to production testing.

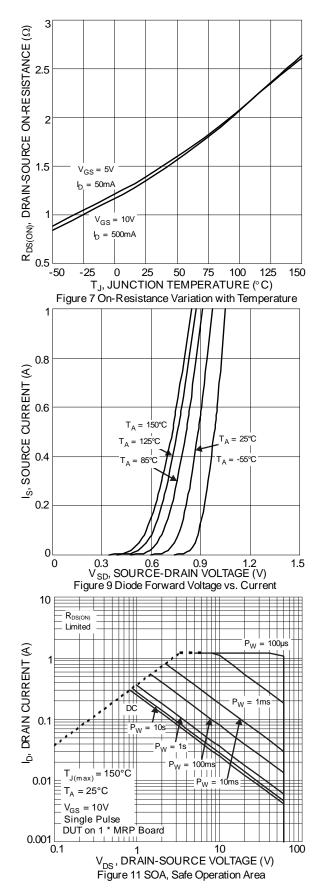


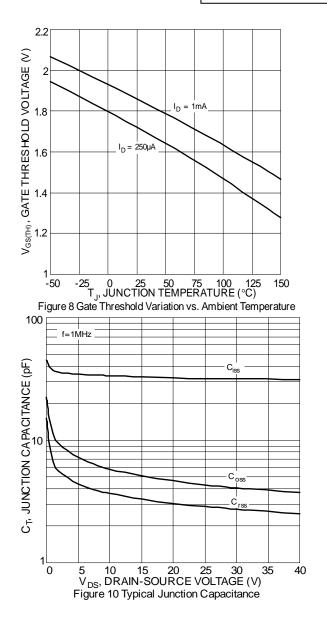




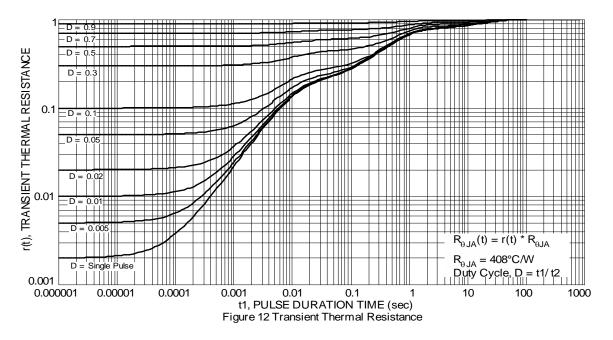








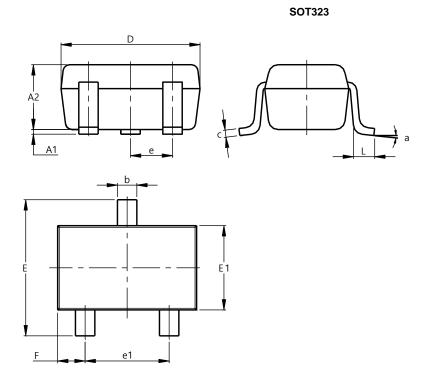






### **Package Outline Dimensions**

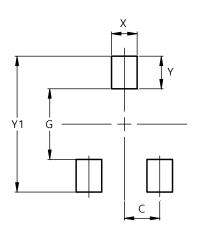
Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT323								
Dim	Min	Max	Тур					
A1	0.00	0.10	0.05					
A2	0.90	1.00	0.95					
b	0.25	0.40	0.30					
C	0.10	0.18	0.11					
D	1.80	2.20	2.15					
Е	2.00	2.20	2.10					
E1	1.15	1.35	1.30					
е	C	).650 B	SC					
e1	1.20	1.40	1.30					
F	0.375	0.475	0.425					
L	0.25	0.40	0.30					
а	8°							
All	All Dimensions in mm							

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	0.650
G	1.300
X	0.470
Y	0.600
Y1	2.500

SOT323



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