





SINGLE N-CHANNEL ENHANCEMENT MODE MOSFET

Features

- Low On-Resistance
 - $30m\Omega @ V_{GS} = 10V$
 - 40mΩ @ V_{GS} = 4.5V
 - 63mΩ @ V_{GS} = 2.5V
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Lead Free By Design/RoHS Compliant (Note 2)
- "Green" Device (Note 4)
- Qualified to AEC-Q101 Standards for High Reliability

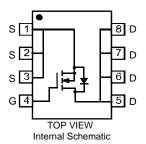


- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals Connections: See Diagram
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Marking Information: See Page 3
- Ordering Information: See Page 3
- Weight: 0.072 grams (approximate)





TOP VIEW



Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic			Symbol	Value	Units	
Drain-Source Voltage			V _{DSS}	30	V	
Gate-Source Voltage			V _{GSS}	±12	V	
Drain Current (Note 1)	Steady State	T _A = 25°C T _A = 70°C	I _D	7.1 5.7	А	
Pulsed Drain Current (Note 3)			I _{DM}	28	А	

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 1)	P_{D}	2.5	W
Thermal Resistance, Junction to Ambient	$R_{ hetaJA}$	50	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

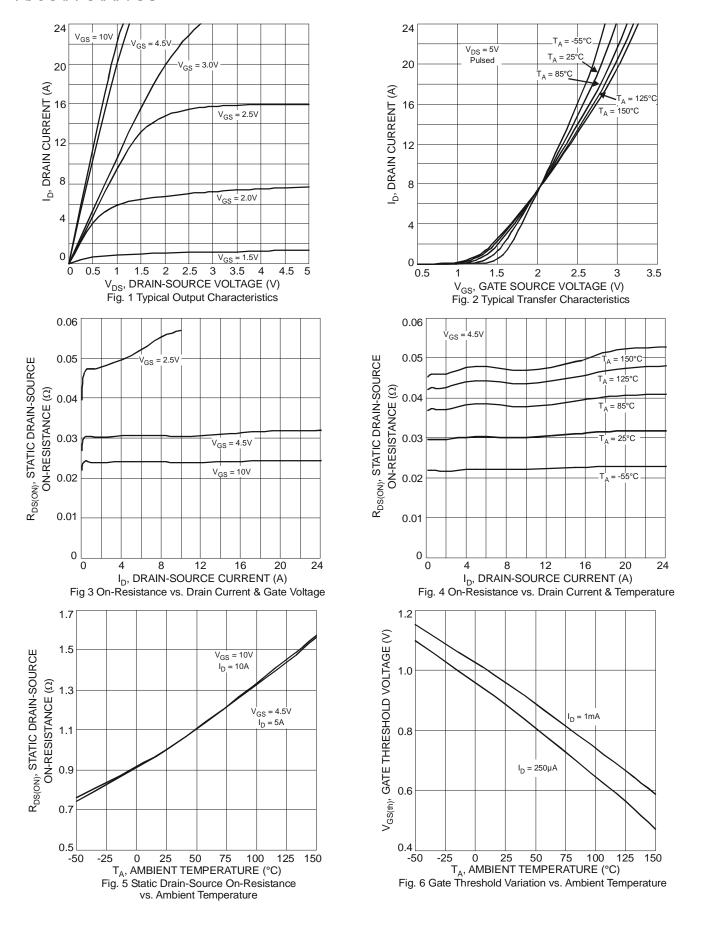
Electrical Characteristics @TA = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 5)							
Drain-Source Breakdown Voltage	BV _{DSS}	30			V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	_		1	μΑ	$V_{DS} = 30V$, $V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_		±80	nA	$V_{GS} = \pm 12V$, $V_{DS} = 0V$	
Cato Courso Lourage	1033	_	_	±800	10 ($V_{GS} = \pm 19V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 5)							
Gate Threshold Voltage	V _{GS(th)}	0.62	0.9	1.2	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
			24	30		$V_{GS} = 10V, I_D = 7.1A$	
Static Drain-Source On-Resistance	R _{DS} (ON)		30	40	$m\Omega$	$V_{GS} = 4.5V, I_D = 6.4A$	
	` ,		50	63		$V_{GS} = 2.5V, I_D = 5.0A$	
Forward Transconductance	9 _{fs}	_	10		S	$V_{DS} = 5V, I_D = 5.1A$	
Diode Forward Voltage (Note 5)	V_{SD}	_	0.78	1.16	V	$V_{GS} = 0V, I_{S} = 2.1A$	
DYNAMIC CHARACTERISTICS							
Input Capacitance	C _{iss}	_	555		рF	., 51/1/ 01/	
Output Capacitance	Coss		109		pF	$V_{DS} = 5V, V_{GS} = 0V$ -f = 1.0MHz	
Reverse Transfer Capacitance	C _{rss}		82	_	pF	1 – 1.0001112	

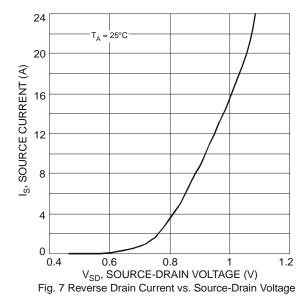
Notes: 1. Device mounted on 2 oz copper pad layout with $R_{0JA} = 50^{\circ}C/W$.

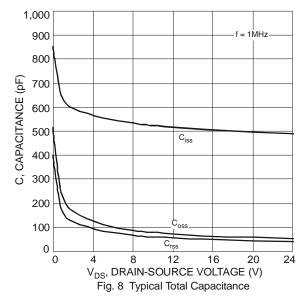
- 2. No purposefully added lead.
- 3. Pulse width $\leq 10 \mu S$, Duty Cycle $\leq 1\%$.
- 4. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.
- 5. Short duration pulse test used to minimize self-heating effect.

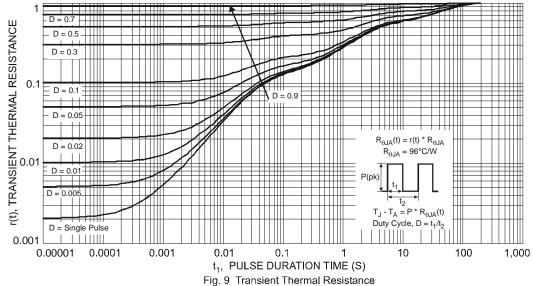










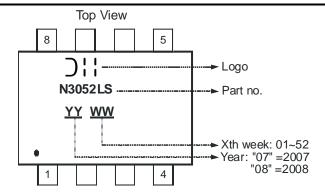


Ordering Information (Note 6)

ſ	Part Number	Case	Packaging
	DMN3052LSS-13	SO-8	2500/Tape & Reel

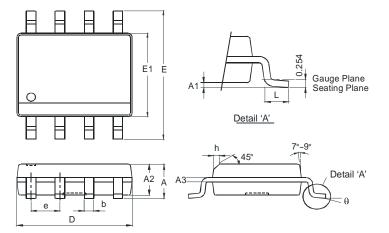
Notes: 6. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information



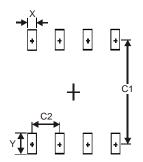


Package Outline Dimensions



SO-8				
Dim	Min	Max		
Α	-	1.75		
A1	0.10	0.20		
A2	1.30	1.50		
А3	0.15	0.25		
b	0.3	0.5		
D	4.85	4.95		
Е	5.90	6.10		
E1	3.85	3.95		
е	1.27 Typ			
h	-	0.35		
L	0.62	0.82		
θ	0°	8°		
All Di	All Dimensions in mm			

Suggested Pad Layout



Dimensions	Value (in mm)
Х	0.60
Υ	1.55
C1	5.4
C2	1.27



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