



#### **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> Max	I <sub>D</sub> T <sub>A</sub> = +25°C
201/	$23m\Omega @ V_{GS} = 10V$	6.6A
30V	$30m\Omega @ V_{GS} = 4.5V$	5.8A

### Description

This new generation MOSFET is designed to minimize the on-state resistance ( $R_{DS(ON)}$ ), yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

# Applications

- DC-DC Converters
- Power management functions
- Backlighting

#### **30V N-CHANNEL ENHANCEMENT MODE MOSFET**

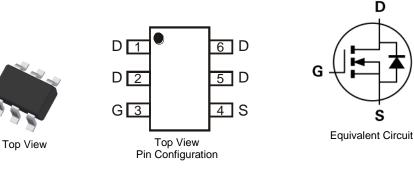
#### **Features and Benefits**

- Low Input Capacitance
- Low On-Resistance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

### **Mechanical Data**

- Case: TSOT26
- Case Material: Molded Plastic, "Green" Molding Compound; UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Tin Finish Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (c3)
- Weight: 0.013 grams (Approximate)

#### TSOT26



## Ordering Information (Note 5)

Part Number	Case	Packaging
DMN3026LVTQ-7	TSOT26	3,000/Tape & Reel
DMN3026LVTQ-13	TSOT26	10,000/Tape & Reel

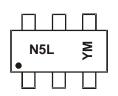
Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to http://www.diodes.com/quality/product\_compliance\_definitions/.

5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.



N5L = Product Type Marking Code YM = Date Code Marking Y = Year (ex: A = 2013) M = Month (ex: 9 = September)

Date Code Key												
Year	2010		2014	201	5 20 <sup>-</sup>	16 2	017	2018	2019	2020	2021	2022
Code	Х		В	С	D	)	E	F	G	Н		J
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

# Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units		
Drain-Source Voltage	V <sub>DSS</sub>	30	V		
Gate-Source Voltage	V <sub>GSS</sub>	±20	V		
Continuous Drain Current (Note 7) \/10\/	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	ID	6.6 5.3	A
Continuous Drain Current (Note 7) $V_{GS} = 10V$	t<10s	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	ID	8.5 6.8	A
Maximum Body Diode Forward Current (Note 7)	ls	3.0	A		
Pulsed Drain Current (10µs pulse, duty cycle = 1%)	IDM	35	А		

## **Thermal Characteristics**

Characteristic	Symbol	Value	Units		
Total Power Dissipation (Note 6)	T <sub>A</sub> = +25°C	Р	1.2	W	
Total Power Dissipation (Note 6)	T <sub>A</sub> = +70°C	PD	0.8	VV	
Thermal Resistance, Junction to Ambient (Note 6)	Steady state	Devi	100	°C/W	
Thermal Resistance, Junction to Ambient (Note 6)	t<10s	R <sub>θJA</sub>	60	°C/W	
Total Power Dissipation (Note 7)	T <sub>A</sub> = +25°C	D	1.5	W	
Total Power Dissipation (Note 7)	T <sub>A</sub> = +70°C	PD	1.0		
Thermal Resistance, Junction to Ambient (Note 7)	Steady state	Devi	83	°C/W	
Thermal Resistance, Junction to Ambient (Note T)	t<10s	R <sub>0JA</sub>	50	°C/W	
Thermal Resistance, Junction to Case (Note 7)		R <sub>θJC</sub>	14.5	°C/W	
Operating and Storage Temperature Range		T <sub>J,</sub> T <sub>STG</sub>	-55 to +150	°C	



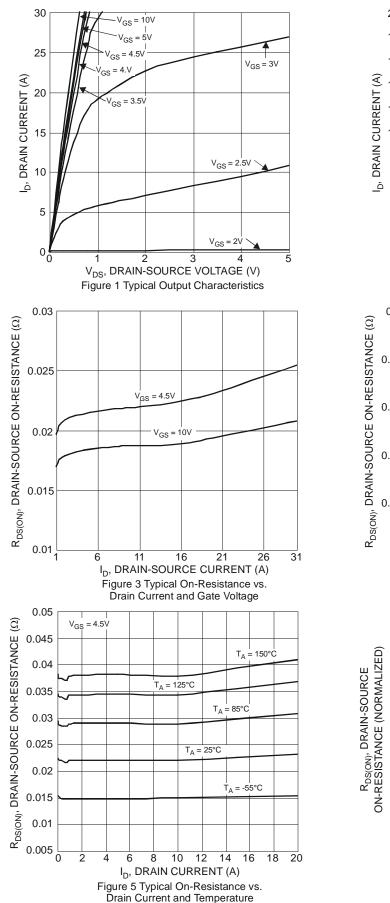
# Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition		
OFF CHARACTERISTICS (Note 8)	Symbol	IVIIII	тур	IVIAX	Unit	Test condition		
			1	1				
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	30			V	$V_{GS} = 0V, I_D = 250\mu A$		
Zero Gate Voltage Drain Current	IDSS		—	1.0	μA	$V_{DS} = 30V, V_{GS} = 0V$		
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$		
ON CHARACTERISTICS (Note 8)								
Gate Threshold Voltage	V <sub>GS(th)</sub>	1.0	1.5	2.0	V	$V_{DS} = V_{GS}$ , $I_D = 250 \mu A$		
Static Drain-Source On-Resistance	R DO (ONI)		19	23	mΩ	$V_{GS} = 10V, I_D = 6.5A$		
	R <sub>DS(ON)</sub>		22	30	11152	$V_{GS} = 4.5V, I_D = 6.0A$		
Diode Forward Voltage	V <sub>SD</sub>	_	0.7	1.2	V	$V_{GS} = 0V, I_{S} = 1.0A$		
DYNAMIC CHARACTERISTICS (Note 9)								
Input Capacitance	Ciss		643	_		$V_{DS} = 15V, V_{GS} = 0V$ f = 1.0MHz		
Output Capacitance	C <sub>oss</sub>	_	65	—	pF			
Reverse Transfer Capacitance	C <sub>rss</sub>		49	—				
Gate Resistance	R <sub>G</sub>	_	2.5	—	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$		
Total Gate Charge (V <sub>GS</sub> = 4.5V)	Qg	_	5.7	_				
Total Gate Charge (V <sub>GS</sub> = 10V)	Qg		12.5	_	nC	$V_{DS} = 15V, I_D = 4.0A$		
Gate-Source Charge	Q <sub>gs</sub>	_	1.7	_				
Gate-Drain Charge	Q <sub>gd</sub>	_	1.8	—				
Turn-On Delay Time	t <sub>D(on)</sub>		2.2	_				
Turn-On Rise Time	tr		2.5	—	nS	$V_{GS} = 10V, V_{DD} = 15V, R_G = 6.0\Omega,$		
Turn-Off Delay Time	t <sub>D(off)</sub>		12.1	—	113	I <sub>D</sub> = 6.5A		
Turn-Off Fall Time	t <sub>f</sub>		3.0	—	]			
Body Diode Reverse Recovery Time	t <sub>rr</sub>		6.5		nS	I <sub>F</sub> = 6.5A, dI/dt = 100A/µs		
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	_	1.7	_	nC	I <sub>F</sub> = 6.5A, dl/dt = 100A/µs		

 Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
Device mounted on FR-4 substrate PC board, 2oz copper, with 1-inch square copper plate.
Short duration pulse test used to minimize self-heating effect.
Guaranteed by design. Not subject to product testing. Notes:







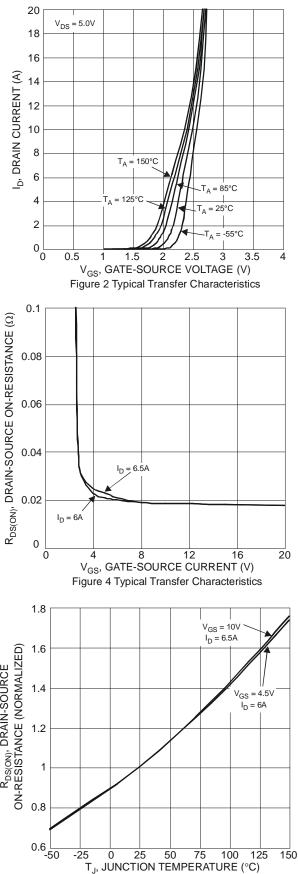
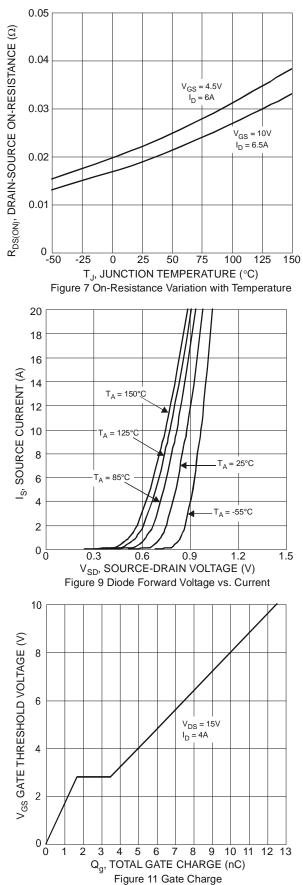
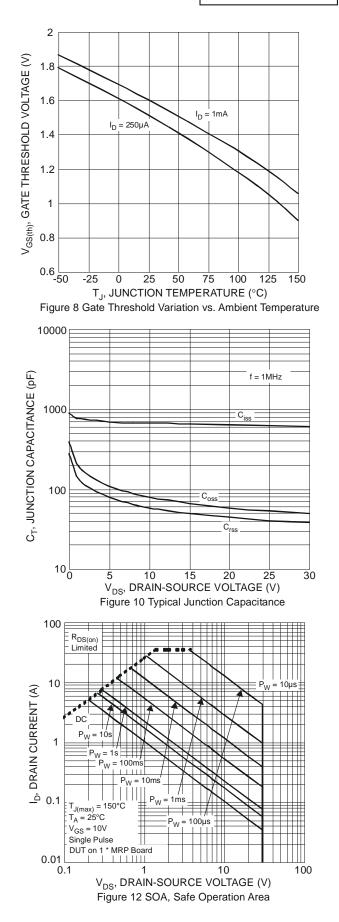


Figure 6 On-Resistance Variation with Temperature

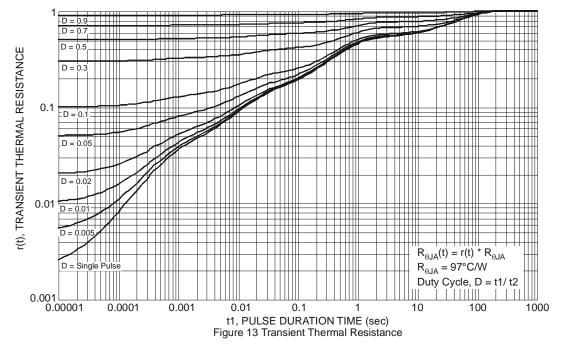
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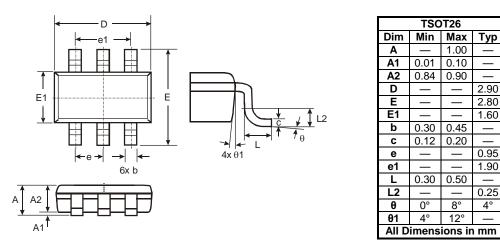




### **Package Outline Dimensions**

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.

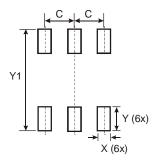
TSOT26



# **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.

TSOT26



Dimensions	Value (in mm)
С	0.950
Х	0.700
Y	1.000
Y1	3.199

Max

1.00

0.10

0.90

0.45

0.20

8°

12°

Тур

\_\_\_\_

\_\_\_\_

2.90

2.80

1.60

0.95

1.90

\_\_\_\_

0.25

4°

\_\_\_\_



### DMN3026LVTQ

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