

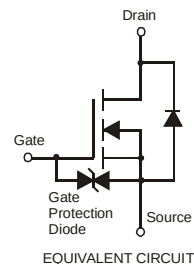
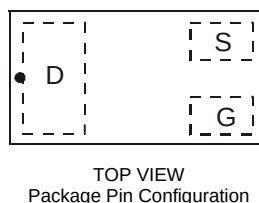
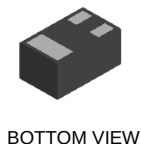
Features

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Ultra-Small Surface Mount Package
- Ultra-Low Package Profile, 0.4mm Maximum Package Height
- **Lead Free By Design/RoHS Compliant (Note 1)**
- **ESD Protected up to 1.5kV**
- **"Green" Device (Note 2)**
- **Qualified to AEC-Q101 standards for High Reliability**

Mechanical Data

- Case: DFN1006H4-3
- Case Material: Molded Plastic, "Green" Molding Compound.
UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: Collector Dot
- Terminals: Finish — NiPdAu over Copper leadframe. Solderable
per MIL-STD-202, Method 208
- Weight: 0.001 grams (approximate)

DFN1006H4-3

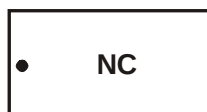


Ordering Information (Note 3)

Part Number	Case	Packaging
DMN2400UFB4-7	DFN1006H4-3	3000/Tape & Reel

- Notes:
1. No purposefully added lead.
 2. Diodes Inc.'s "Green" policy can be found on our website at <http://www.diodes.com>.
 3. For packaging details, go to our website at <http://www.diodes.com>.

Marking Information



NC = Product Type Marking Code
Dot Denotes Drain Side

Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V _{DSS}	20	V
Gate-Source Voltage			V _{GSS}	±12	V
Continuous Drain Current (Note 4) V _{GS} = 4.5V	Steady State	T _A = 25°C	I _D	0.75	A
		T _A = 85°C		0.55	
Pulsed Drain Current (Notes 4 & 5)			I _{DM}	3	A

Thermal Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 4)	P _D	0.47	mW
Thermal Resistance, Junction to Ambient	R _{θJA}	258	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 6)						
Drain-Source Breakdown Voltage	BV _{DSS}	20	-	-	V	V _{GS} = 0V, I _D = 250μA
Zero Gate Voltage Drain Current T _J = 25°C	I _{DSS}	-	-	100	nA	V _{DS} = 20V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	-	-	±1.0	μA	V _{GS} = ±4.5V, V _{DS} = 0V
Gate-Source Leakage	I _{GSS}	-	-	±50	μA	V _{GS} = ±10V, V _{DS} = 0V
ON CHARACTERISTICS (Note 6)						
Gate Threshold Voltage	V _{GS(th)}	0.5	-	0.9	V	V _{DS} = V _{GS} , I _D = 250μA
Static Drain-Source On-Resistance	R _{DS(on)}	-	-	0.55	Ω	V _{GS} = 4.5V, I _D = 600mA
		-	-	0.75		V _{GS} = 2.5V, I _D = 500mA
		-	-	0.9		V _{GS} = 1.8V, I _D = 350mA
		-	-	0.9		V _{GS} = 1.8V, I _D = 350mA
Forward Transfer Admittance	Y _{fs}	-	1.0	-	S	V _{DS} = 10V, I _D = 400mA
Diode Forward Voltage (Note 6)	V _{SD}	-	0.7	1.2	V	V _{GS} = 0V, I _S = 150mA
DYNAMIC CHARACTERISTICS (Note 7)						
Input Capacitance	C _{iss}	-	36.0	-	pF	V _{DS} = 16V, V _{GS} = 0V, f = 1.0MHz
Output Capacitance	C _{oss}	-	5.7	-	pF	
Reverse Transfer Capacitance	C _{rss}	-	4.2	-	pF	
Total Gate Charge	Q _g	-	0.5	-	nC	V _{GS} = 4.5V, V _{DS} = 10V, I _D = 250mA
Gate-Source Charge	Q _{gs}	-	0.07	-	nC	
Gate-Drain Charge	Q _{gd}	-	0.1	-	nC	
Turn-On Delay Time	t _{D(on)}	-	4.11	-	ns	V _{DD} = 10V, V _{GS} = 4.5V, R _L = 47Ω, R _G = 10Ω, I _D = 200mA
Turn-On Rise Time	t _r	-	3.82	-	ns	
Turn-Off Delay Time	t _{D(off)}	-	14.8	-	ns	
Turn-Off Fall Time	t _f	-	9.6	-	ns	

- Notes:
4. Device mounted on FR-4 PCB, with minimum recommended pad layout, single sided.
 5. Device mounted on minimum recommended pad layout test board, 10μs pulse duty cycle = 1%.
 6. Short duration pulse test used to minimize self-heating effect.
 7. Guaranteed by design. Not subject to product testing.

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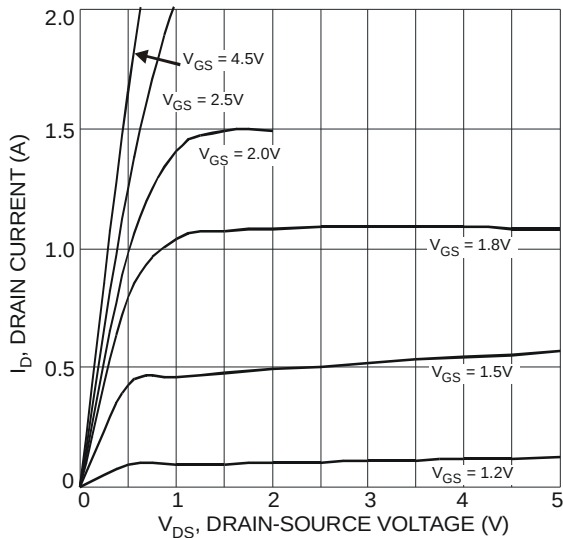


Fig. 1 Typical Output Characteristics

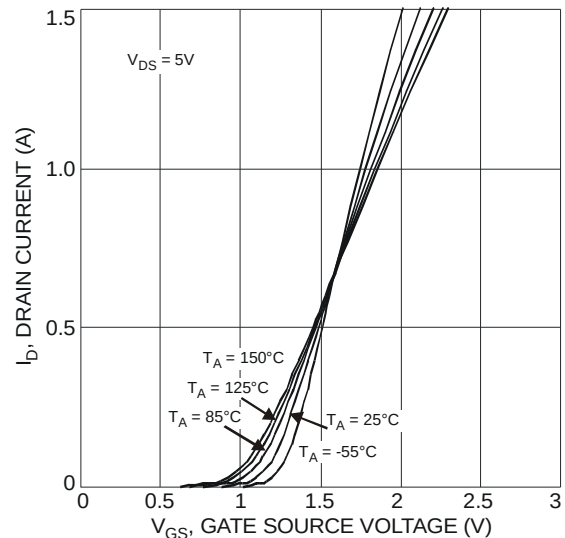


Fig. 2 Typical Transfer Characteristics

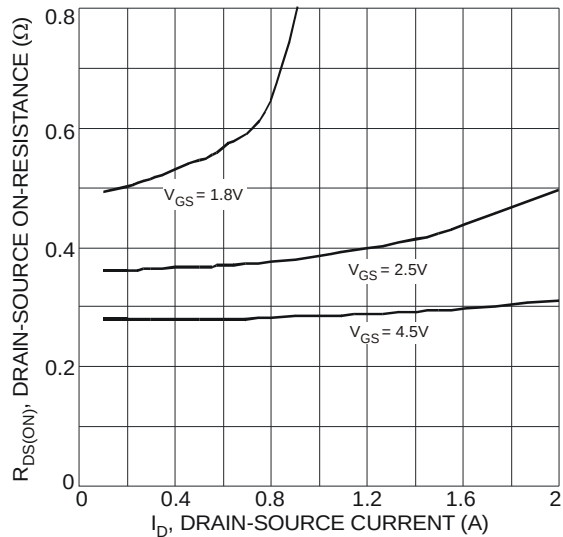


Fig. 3 Typical On-Resistance vs. Drain Current and Gate Voltage

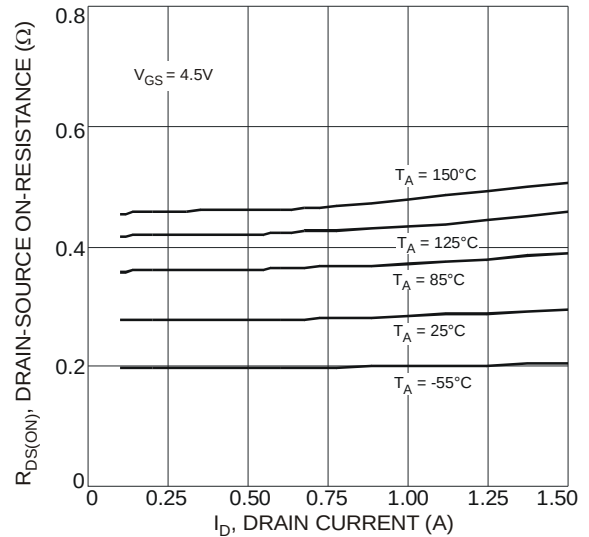


Fig. 4 Typical Drain-Source On-Resistance vs. Drain Current and Temperature

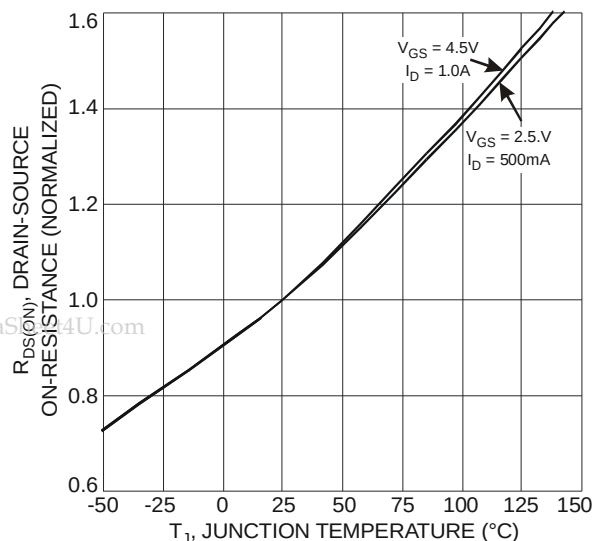


Fig. 5 On-Resistance Variation with Temperature

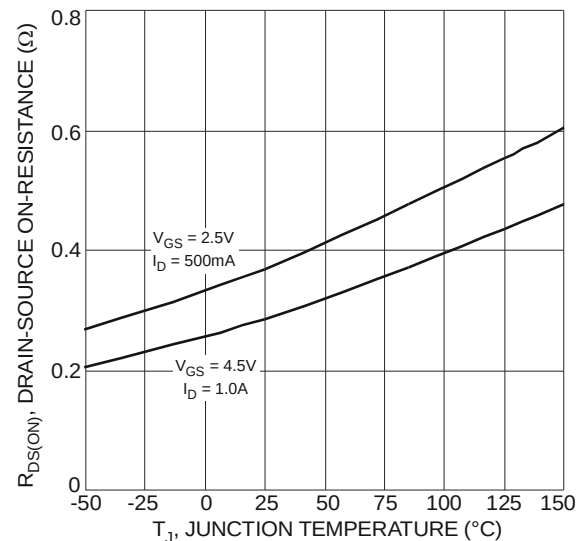


Fig. 6 On-Resistance Variation with Temperature

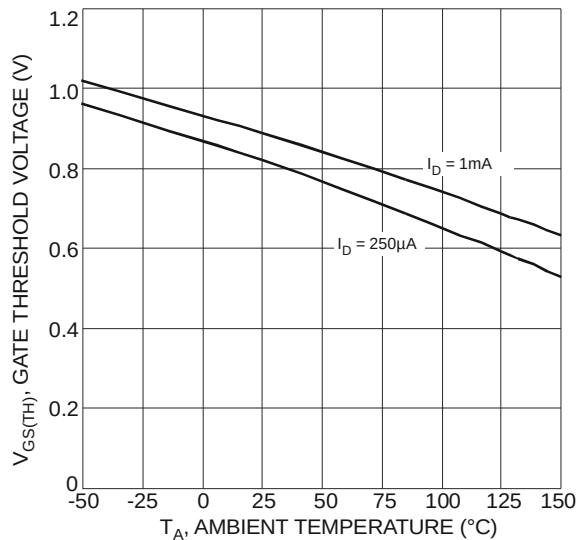


Fig. 7 Gate Threshold Variation vs. Ambient Temperature

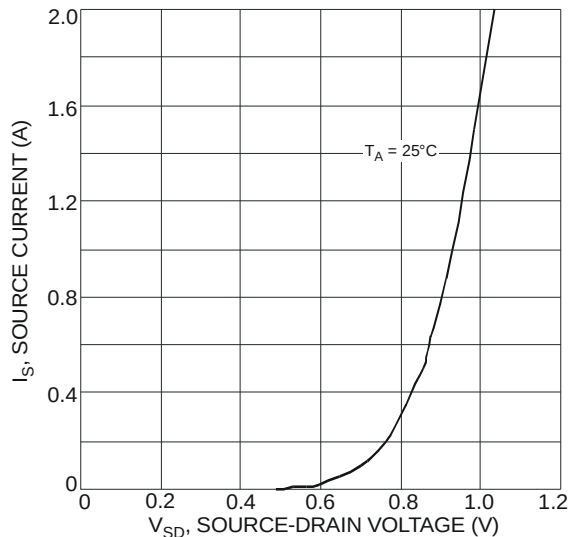


Fig. 8 Diode Forward Voltage vs. Current

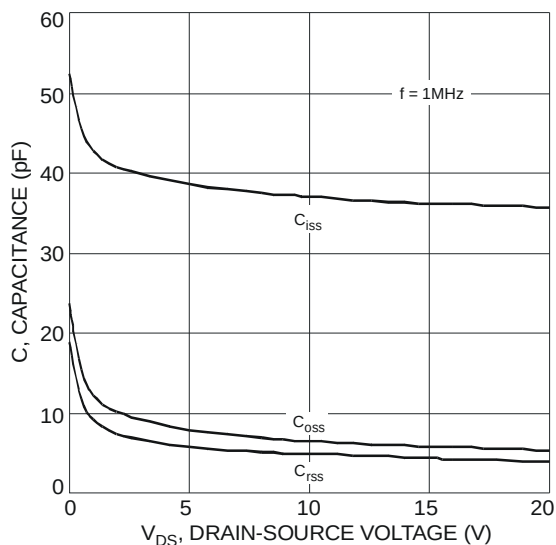


Fig. 9 Typical Capacitance

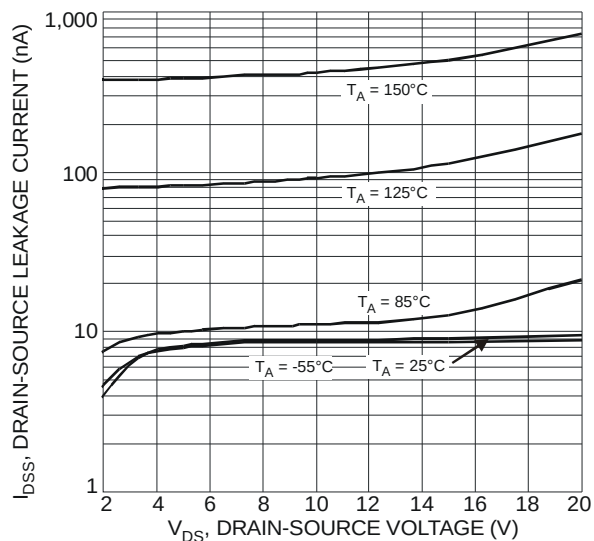


Fig. 10 Typical Drain-Source Leakage Current vs. Drain-Source Voltage

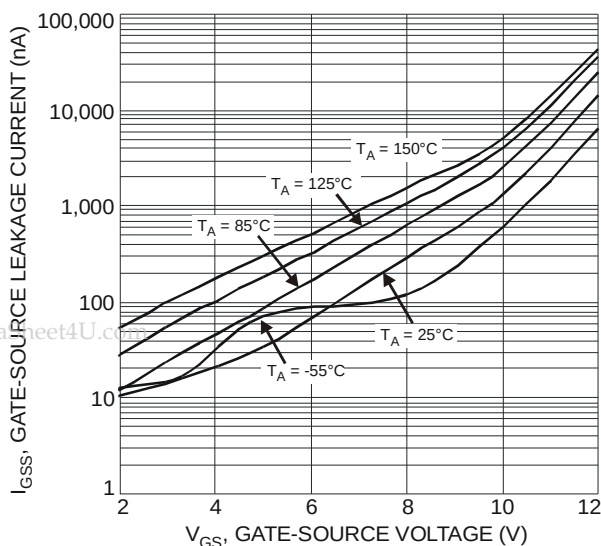


Fig. 11 Typical Gate-Source Leakage Current vs. Gate-Source Voltage

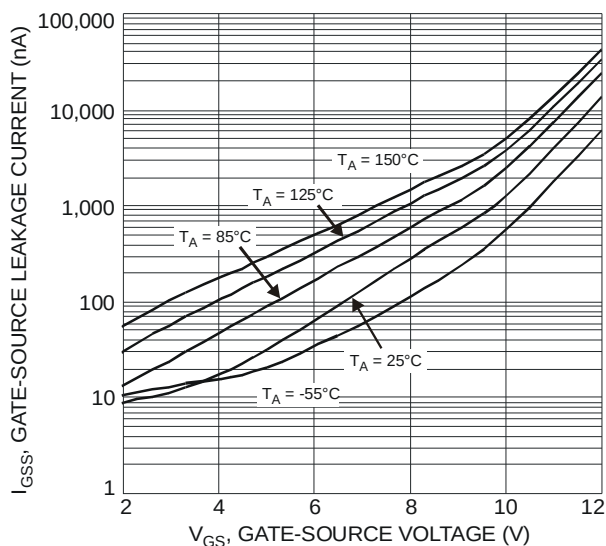


Fig. 12 Typical Gate-Source Leakage Current vs. Gate-Source Voltage

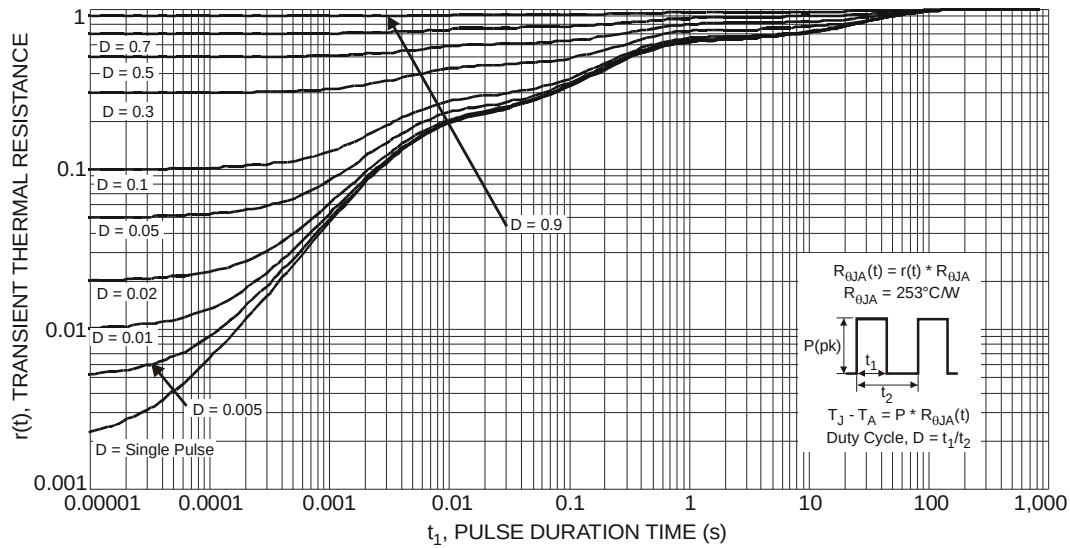
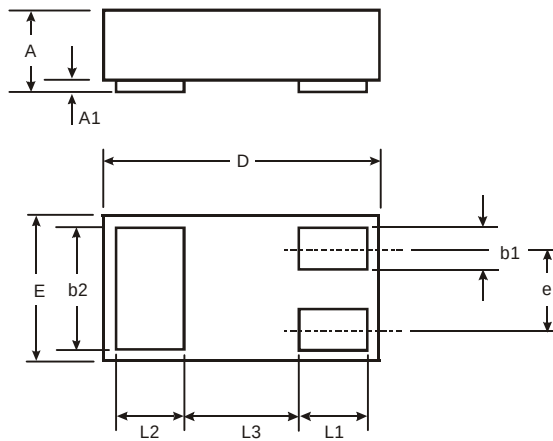


Fig. 13 Transient Thermal Response

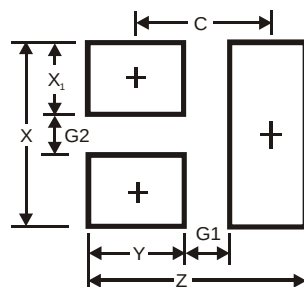
Package Outline Dimensions



DFN1006H4-3			
Dim	Min	Max	Typ
A	—	0.40	—
A1	0	0.05	0.02
b1	0.10	0.20	0.15
b2	0.45	0.55	0.50
D	0.95	1.075	1.00
E	0.55	0.675	0.60
e	—	—	0.35
L1	0.20	0.30	0.25
L2	0.20	0.30	0.25
L3	—	—	0.40

All Dimensions in mm

Suggested Pad Layout



Dimensions	Value (in mm)
Z	1.1
G1	0.3
G2	0.2
X	0.7
X1	0.25
Y	0.4
C	0.7

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