

20V N-CHANNEL ENHANCEMENT MODE MOSFET
Product Summary

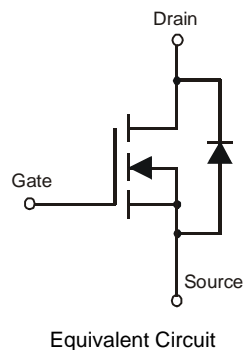
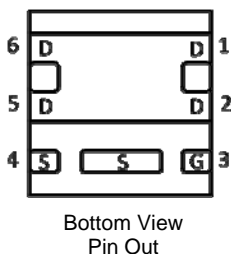
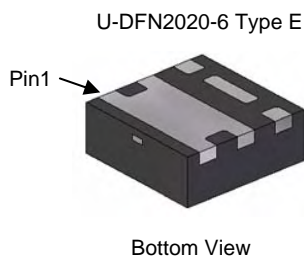
$V_{(BR)DSS}$	$R_{DS(ON) \max}$	Package	$I_{D \max}$ $T_A = +25^\circ\text{C}$
20V	11.6m Ω @ $V_{GS} = 4.5\text{V}$	U-DFN2020-6 Type E	10.5A
	15m Ω @ $V_{GS} = 2.5\text{V}$		9.4A

Description

This new generation MOSFET has been designed to minimize the on-state resistance ($R_{DS(on)}$) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- General Purpose Interfacing Switch
- Power Management Functions


Features

- 0.6mm profile – ideal for low profile applications
- PCB footprint of 4mm²
- Low Gate Threshold Voltage
- Low On-Resistance
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- Halogen and Antimony Free. "Green" Device (Note 3)**
- Qualified to AEC-Q101 Standards for High Reliability**

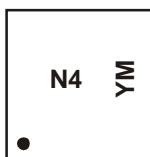
Mechanical Data

- Case: U-DFN2020-6 Type E
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish – NiPdAu over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Weight: 0.0065 grams (approximate)

Ordering Information (Note 4)

Part Number	Marking	Reel size (inches)	Quantity per reel
DMN2015UFDE-7	N4	7	3,000
DMN2015UFDE-13	N4	13	10,000

- Notes:
- No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
 - See <http://www.diodes.com> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 - Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 - For packaging details, go to our website at <http://www.diodes.com>.

Marking Information


N4 = Product Type Marking Code
 YM = Date Code Marking
 Y = Year (ex: Y = 2011)
 M = Month (ex: 9 = September)

Date Code Key

Year	2011	2012	2013	2014	2015	2016	2017
Code	Y	Z	A	B	C	D	E

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V _{DSS}	20	V
Gate-Source Voltage			V _{GSS}	±12	V
Continuous Drain Current (Note 6) V _{GS} = 4.5V	Steady State	T _A = +25°C T _A = +70°C	I _D	10.5 8.5	A
	t < 10s	T _A = +25°C T _A = +70°C	I _D	12.5 10.0	A
Continuous Drain Current (Note 6) V _{GS} = 2.5V	Steady State	T _A = +25°C T _A = +70°C	I _D	9.4 7.5	A
	t < 10s	T _A = +25°C T _A = +70°C	I _D	11.2 8.8	A
Pulsed Drain Current (10μs pulse, duty cycle = 1%)			I _{DM}	80	A
Maximum Body Diode Continuous Current			I _S	2.5	A

Thermal Characteristics

Characteristic		Symbol	Value	Units
Total Power Dissipation (Note 5)	T _A = +25°C	P _D	0.66	W
	T _A = +70°C		0.42	
Thermal Resistance, Junction to Ambient (Note 5)	Steady state	R _{θJA}	189	°C/W
	t < 10s		132	
Total Power Dissipation (Note 6)	T _A = +25°C	P _D	2.03	W
	T _A = +70°C		1.31	
Thermal Resistance, Junction to Ambient (Note 6)	Steady state	R _{θJA}	61	°C/W
	t < 10s		43	
Thermal Resistance, Junction to Case (Note 6)		R _{θJC}	9.3	
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	20	—	—	V	V _{GS} = 0V, I _D = 250μA
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	—	—	1	μA	V _{DS} = 16V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±100	nA	V _{GS} = ±12V, V _{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(th)}	0.5	—	1.1	V	V _{DS} = V _{GS} , I _D = 250μA
Static Drain-Source On-Resistance	R _{DS(on)}	—	9.3	11.6	mΩ	V _{GS} = 4.5V, I _D = 8.5A
			11.4	15		V _{GS} = 2.5V, I _D = 8.5A
			17	30		V _{GS} = 1.8V, I _D = 5A
			24	50		V _{GS} = 1.5V, I _D = 3A
Forward Transfer Admittance	Y _{fs}	—	11.3	—	S	V _{DS} = 10V, I _D = 8.5A
Diode Forward Voltage	V _{SD}	—	—	1.2	V	V _{GS} = 0V, I _S = 8.5A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	—	1779	—	pF	V _{DS} = 10V, V _{GS} = 0V, f = 1.0MHz
Output Capacitance	C _{oss}	—	175	—	pF	
Reverse Transfer Capacitance	C _{rss}	—	154	—	pF	
Gate Resistance	R _g	—	0.94	—	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1MHz
Total Gate Charge (V _{GS} = 4.5V)	Q _g	—	19.7	—	nC	
Total Gate Charge (V _{GS} = 10V)	Q _g	—	45.6	—	nC	
Gate-Source Charge	Q _{gs}	—	2.9	—	nC	
Gate-Drain Charge	Q _{gd}	—	3.8	—	nC	V _{DS} = 10V, I _D = 8.5A
Turn-On Delay Time	t _{D(on)}	—	7.4	—	ns	
Turn-On Rise Time	t _r	—	16.8	—	ns	
Turn-Off Delay Time	t _{D(off)}	—	43.6	—	ns	
Turn-Off Fall Time	t _f	—	10.9	—	ns	V _{DS} = 10V, I _D = 8.5A V _{GS} = 4.5V, R _G = 1.8Ω
Reverse Recovery Time	T _{rr}	—	8.6	—	ns	
Reverse Recovery Charge	Q _{rr}	—	3.7	—	nC	I _F = 8.5A, di/dt = 210A/μs

- Notes:
5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
 6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
 7. Short duration pulse test used to minimize self-heating effect.
 8. Guaranteed by design. Not subject to production testing.

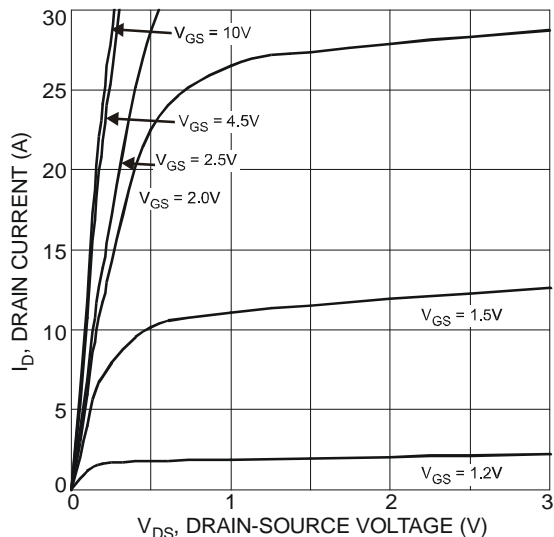


Fig. 1 Typical Output Characteristic

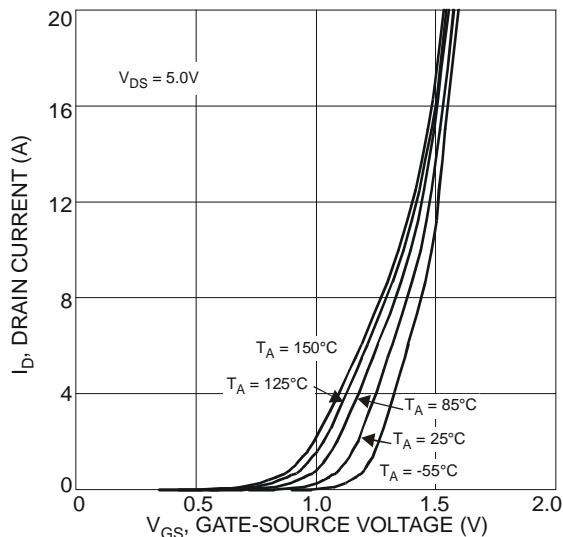


Fig. 2 Typical Transfer Characteristics

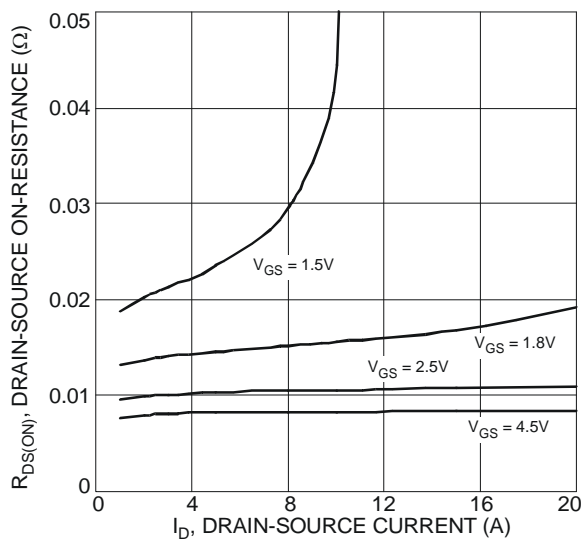


Fig. 3 Typical On-Resistance vs. Drain Current and Gate Voltage

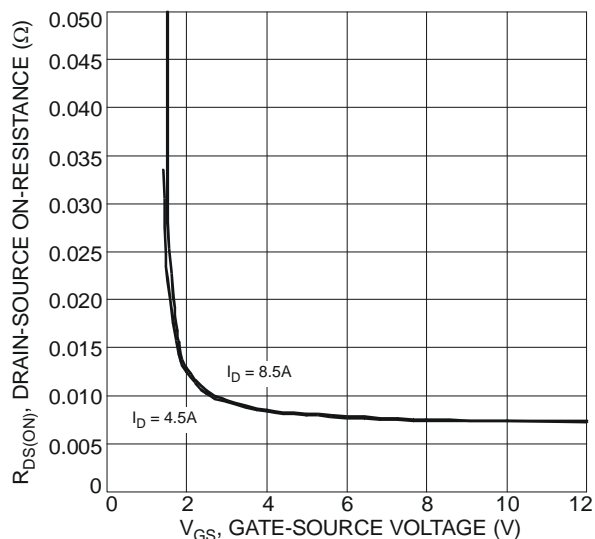


Fig. 4 Typical Drain-Source On-Resistance vs. Gate-Source Voltage

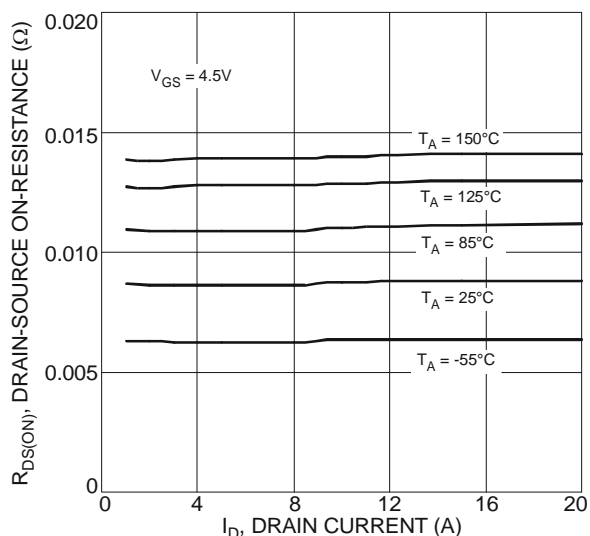


Fig. 5 Typical On-Resistance vs. Drain Current and Temperature

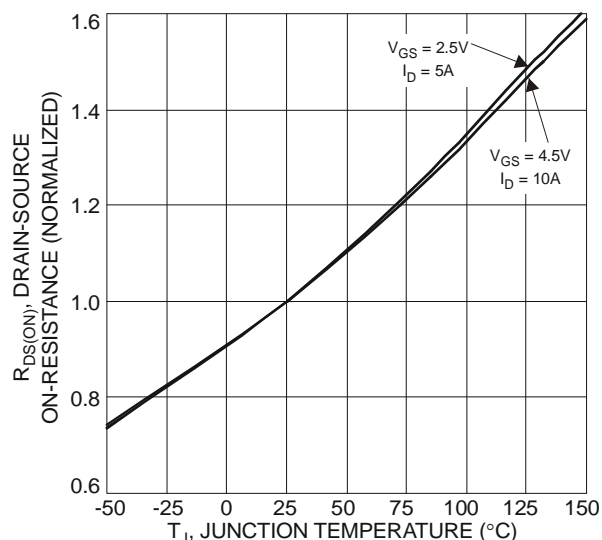


Fig. 6 On-Resistance Variation with Temperature

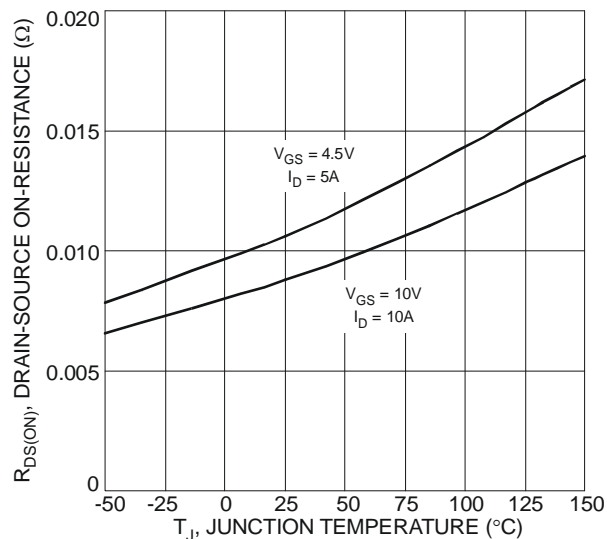


Fig. 7 On-Resistance Variation with Temperature

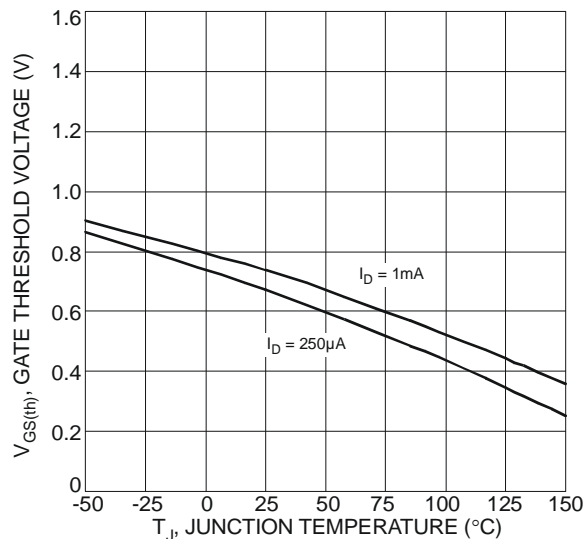


Fig. 8 Gate Threshold Variation vs. Ambient Temperature

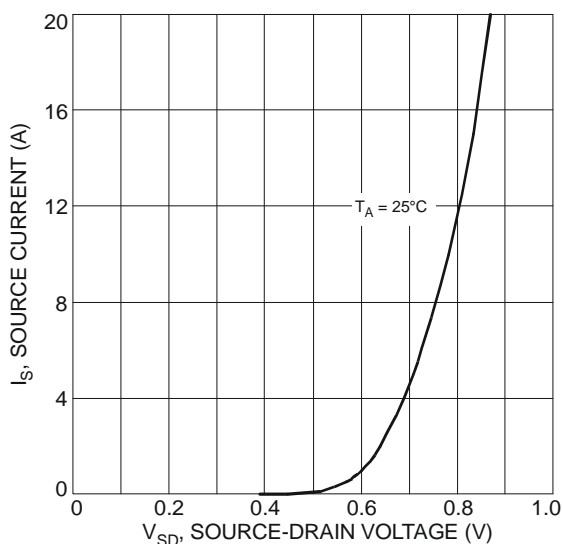


Fig. 9 Diode Forward Voltage vs. Current

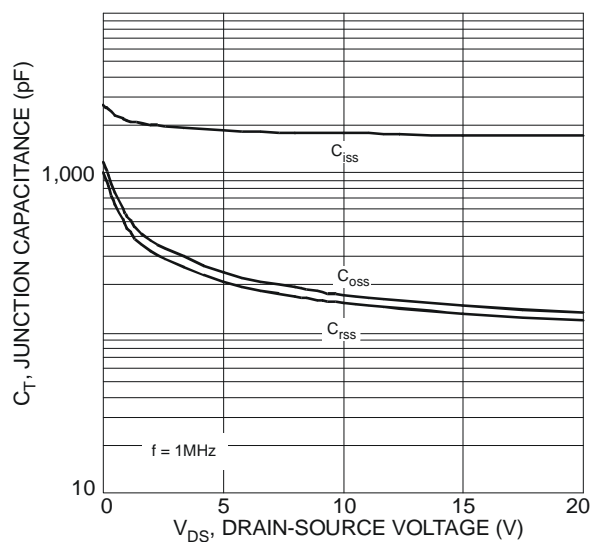


Fig. 10 Typical Junction Capacitance

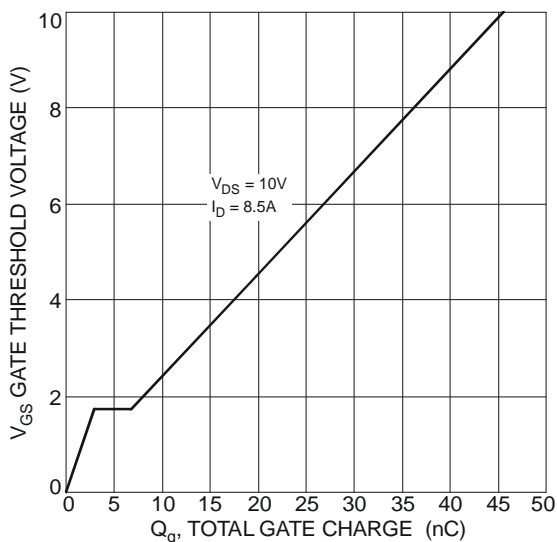


Fig. 11 Gate Charge

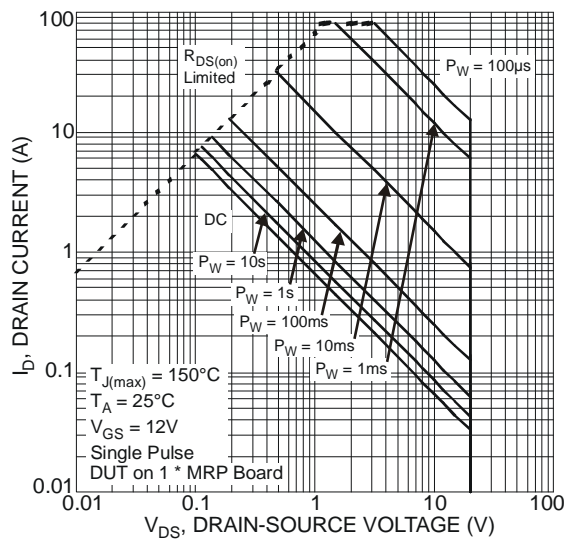
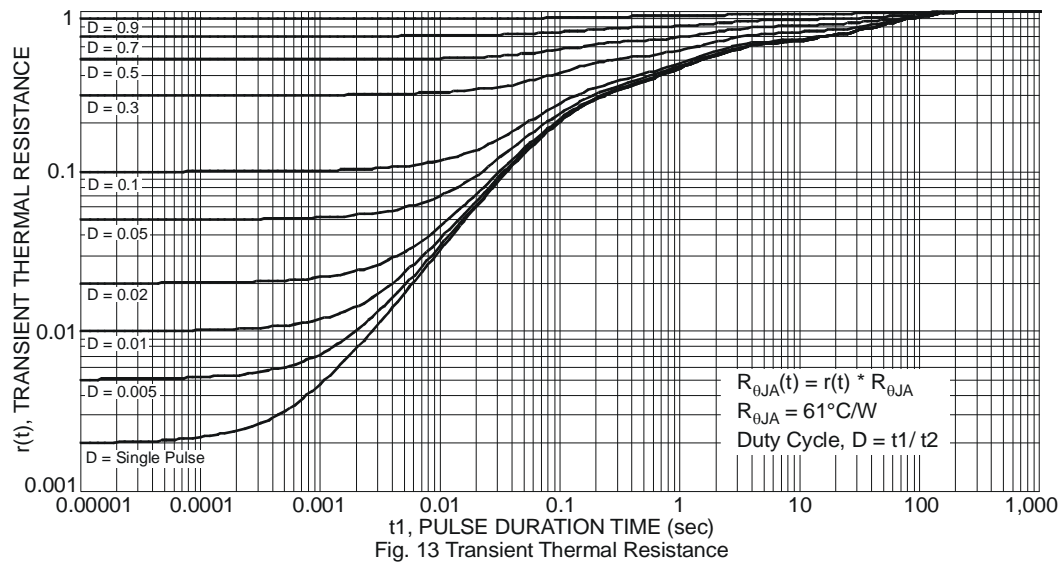
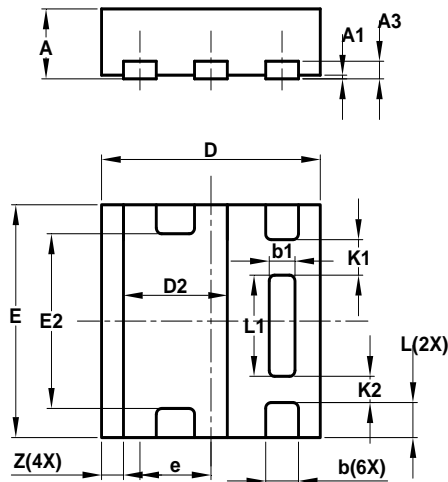


Fig. 12 SOA, Safe Operation Area

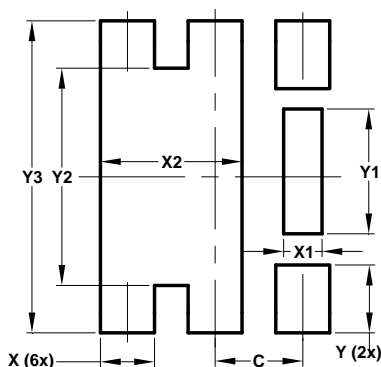


Package Outline Dimensions



U-DFN2020-6 Type E			
Dim	Min	Max	Typ
A	0.57	0.63	0.60
A1	0	0.05	0.03
A3	—	—	0.15
b	0.25	0.35	0.30
b1	0.185	0.285	0.235
D	1.95	2.05	2.00
D2	0.85	1.05	0.95
E	1.95	2.05	2.00
E2	1.40	1.60	1.50
e	—	—	0.65
L	0.25	0.35	0.30
L1	0.82	0.92	0.87
K1	—	—	0.305
K2	—	—	0.225
Z	—	—	0.20
All Dimensions in mm			

Suggested Pad Layout



Dimensions	Value (in mm)
C	0.650
X	0.400
X1	0.285
X2	1.050
Y	0.500
Y1	0.920
Y2	1.600
Y3	2.300

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