# **DMG563H1**

## Silicon NPN epitaxial planar type (Tr1) Silicon PNP epitaxial planar type (Tr2)

For digital circuits

#### ■ Features

- $\bullet$  Low collector-emitter saturation voltage  $V_{\text{CE(sat)}}$
- Halogen-free / RoHS compliant
   (EU RoHS / UL-94 V-0 / MSL: Level 1 compliant)

### ■ Marking Symbol: T1

#### ■ Basic Part Number

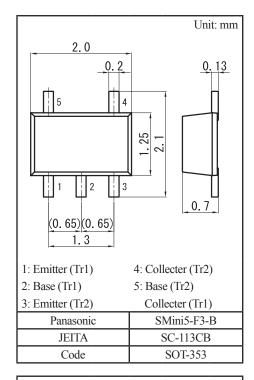
DRC2144E + DRA2143X (Collector-base connection)

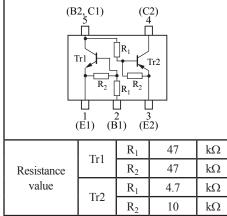
#### ■ Packaging

DMG563H10R Embossed type (Thermo-compression sealing): 3 000 pcs / reel (standard)

#### ■ Absolute Maximum Ratings $T_a = 25$ °C

	Parameter	Symbol	Rating	Unit
Tr1	Collector-base voltage (Emitter open)	V <sub>CBO</sub>	50	V
	Collector-emitter voltage (Base open)	V <sub>CEO</sub>	50	V
	Collector current	$I_{C}$	100	mA
Tr2	Collector-base voltage (Emitter open)	V <sub>CBO</sub>	-50	V
	Collector-emitter voltage (Base open)	V <sub>CEO</sub>	-50	V
	Collector current	$I_{C}$	-100	mA
Overall	Total power dissipation	P <sub>T</sub>	150	mW
	Junction temperature	$T_j$	150	°C
	Storage temperature	T <sub>stg</sub>	-55 to +150	°C





## $\blacksquare$ Electrical Characteristics $T_a = 25^{\circ}C \pm 3^{\circ}C$

### • Tr1

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	$V_{CBO}$	$I_C = 10 \mu A, I_E = 0$	50			V
Collector-emitter voltage (Base open)	V <sub>CEO</sub>	$I_{\rm C} = 2 \text{ mA}, I_{\rm B} = 0$	50			V
Collector-base cutoff current (Emitter open)	$I_{CBO}$	$V_{\rm CB} = 50 \text{ V}, I_{\rm E} = 0$			0.1	μΑ
Collector-emitter cutoff current (Base open)	I <sub>CEO</sub>	$V_{CE} = 50 \text{ V}, I_{B} = 0$			0.5	μΑ
Emitter-base cutoff current (Collector open)	$I_{EBO}$	$V_{EB} = 6 \text{ V}, I_{C} = 0$			0.1	mA
Forward current transfer ratio	$h_{FE}$	$V_{CE} = 10 \text{ V}, I_{C} = 5 \text{ mA}$	80			_
Collector-emitter saturation voltage	V <sub>CE(sat)</sub>	$I_C = 10 \text{ mA}, I_B = 0.5 \text{ mA}$			0.25	V
Input voltage (ON)	V <sub>I(on)</sub>	$V_{CE} = 0.2 \text{ V}, I_{C} = 5 \text{ mA}$	3.6			V
Input voltage (OFF)	V <sub>I(off)</sub>	$V_{CE} = 5 \text{ V}, I_{C} = 100  \mu\text{A}$			0.8	V
Input resistance	$R_1$		-30%	47	+30%	kΩ
Resistance ratio	$R_1/R_2$		0.8	1.0	1.2	_

Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

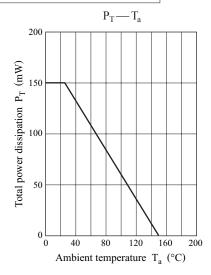
#### • Tr2

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	$V_{CBO}$	$I_{\rm C} = -10 \mu{\rm A}, I_{\rm E} = 0$	-50			V
Collector-emitter voltage (Base open)	V <sub>CEO</sub>	$I_C = -2 \text{ mA}, I_B = 0$	-50			V
Collector-base cutoff current (Emitter open)	$I_{CBO}$	$V_{\rm CB} = -50 \text{ V}, I_{\rm E} = 0$			-0.1	μΑ
Collector-emitter cutoff current (Base open)	I <sub>CEO</sub>	$V_{CE} = -50 \text{ V}, I_{B} = 0$			-0.5	μΑ
Emitter-base cutoff current (Collector open)	$I_{EBO}$	$V_{EB} = -6 \text{ V}, I_C = 0$			-1.0	mA
Forward current transfer ratio	$h_{\mathrm{FE}}$	$V_{CE} = -10 \text{ V}, I_{C} = -5 \text{ mA}$	30			_
Collector-emitter saturation voltage	V <sub>CE(sat)</sub>	$I_C = -10 \text{ mA}, I_B = -0.5 \text{ mA}$			-0.25	V
Input voltage (ON)	V <sub>I(on)</sub>	$V_{CE} = -0.2 \text{ V}, I_{C} = -5 \text{ mA}$	-1.7			V
Input voltage (OFF)	V <sub>I(off)</sub>	$V_{CE} = -5 \text{ V}, I_C = -100 \mu\text{A}$			-0.6	V
Input resistance	$R_1$		-30%	4.7	+30%	kΩ
Resistance ratio	$R_1/R_2$		0.37	0.47	0.57	_

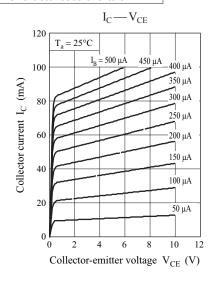
 $Note)\ Measuring\ methods\ are\ based\ on\ JAPANESE\ INDUSTRIAL\ STANDARD\ JIS\ C\ 7030\ measuring\ methods\ for\ transistors.$ 

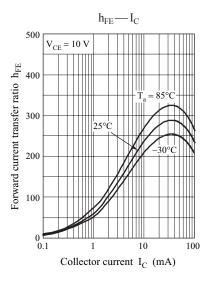
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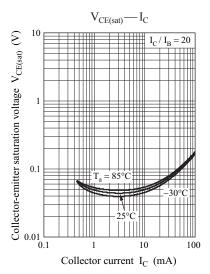
#### Common characteristics chart

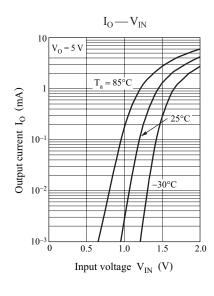


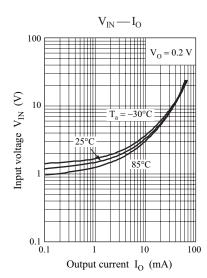
#### Characteristics charts of Tr1





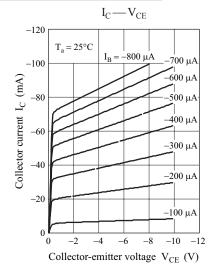


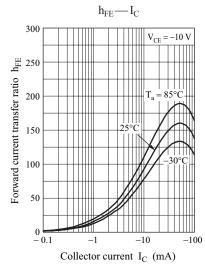


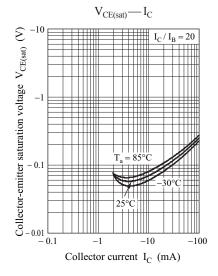


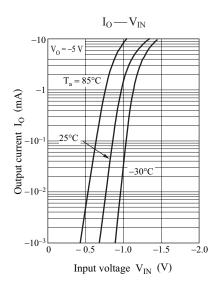
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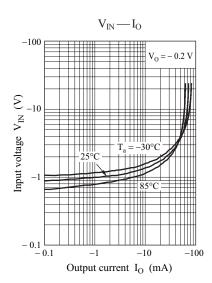
#### Characteristics charts of Tr2







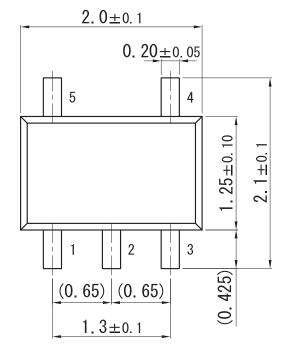


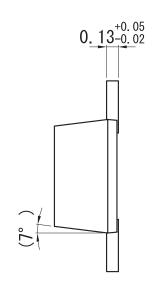


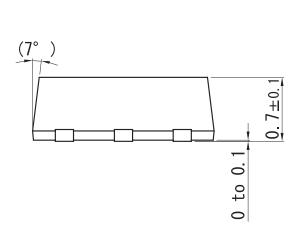
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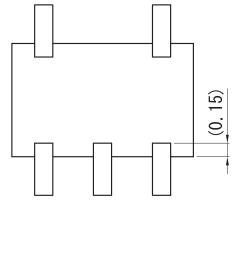
# SMini5-F3-B

Unit: mm

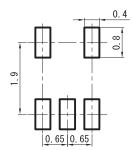








### ■ Land Pattern (Reference) (Unit: mm)



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