



DMG3N60SJ3

Product Summary

BV _{DSS} (@ TJ Max)	Rds(on) Max	l⊳ @Tc = +25°C
650V	3.5Ω @ V _{GS} = 10V	2.8A

Description and Applications

This new generation MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Motor Control
- Backlighting
- DC-DC Converters
- Power Management Functions

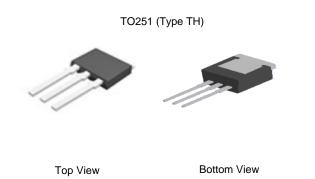
N-CHANNEL ENHANCEMENT MODE MOSFET

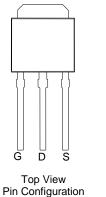
Features and Benefits

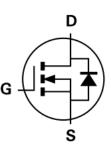
- Low On-Resistance
- High BVDSS Rating for Power Application
- Low Input Capacitance
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative. <u>https://www.diodes.com/quality/product-definitions/</u>

Mechanical Data

- Case: TO251
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (€3)
- Weight: 0.33 grams (Approximate)







Internal Schematic

Ordering Information (Note 4)

Part Number	Case	Packaging
DMG3N60SJ3	TO251 (Type TH)	75 Pieces/Tube

Notes: 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied. 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



D | |= Manufacturer's Marking
 3N60SJ = Product Type Marking Code
 YYWW = Date Code Marking
 YY = Last Two Digits of Year (ex: 20 = 2020)
 <u>WW</u> = Week Code (01 to 53)



Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Characteristic Drain-Source Voltage Gate-Source Voltage			Symbol	Value 600	Unit V
			Vdss		
			Vgss	±30	V
Continuous Drain Current (Note 5) VGs = 10V	Steady State	T _C = +25°C T _C = +100°C	lo	2.8 1.8	А
Continuous Drain Current (Note 5) V _{GS} = 10V	Steady State	T _A = +25°C	lь	0.7	А
Maximum Body Diode Forward Current (Note 5)			ls	2.5	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			IDM	4.2	А
Avalanche Current, L = 60mH (Note 7)			las	1.0	А
Avalanche Energy, L = 60mH (Note 7)			Eas	33	mJ
Peak Diode Recovery dv/dt			dv/dt	5	V/ns

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	Tc = +25°C	Pp	41	W
	Tc = +100°C	PD	16	vv
Total Power Dissipation (Note 5)	T _A = +25°C	PD	2.5	W
Thermal Resistance, Junction to Ambient (Note 6)		Reja	49	°C/W
Thermal Resistance, Junction to Case (Note 5)		Rejc	3.0	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

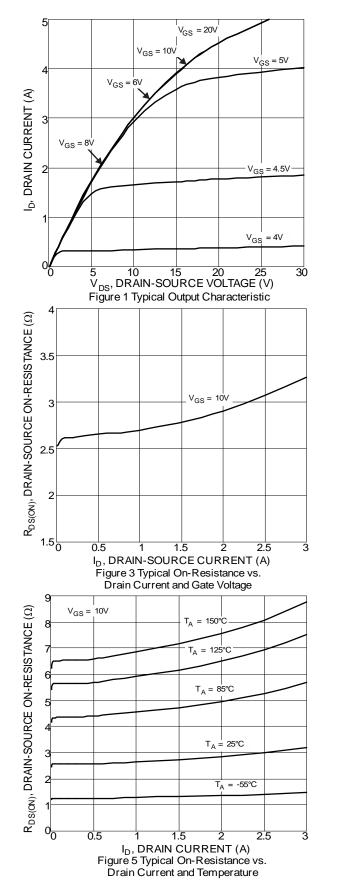
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition		
OFF CHARACTERISTICS (Note 8)								
Drain-Source Breakdown Voltage	BVDSS	600	_	_	V	V _{GS} = 0V, I _D = 250µA		
Zero Gate Voltage Drain Current	IDSS			1	μA	$V_{DS} = 600V, V_{GS} = 0V$		
Gate-Source Leakage	lgss	_	—	100	nA	$V_{GS} = \pm 30V, V_{DS} = 0V$		
ON CHARACTERISTICS (Note 8)	ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	VGS(TH)	2.0	3.0	4.0	V	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$		
Static Drain-Source On-Resistance	RDS(ON)	_	2.9	3.5	Ω	VGS = 10V, ID = 1.5A		
Diode Forward Voltage	Vsd	_	0.87	1.5	V	$V_{GS} = 0V, I_{S} = 3.0A$		
DYNAMIC CHARACTERISTICS (Note 7)	DYNAMIC CHARACTERISTICS (Note 7)							
Input Capacitance	Ciss		354					
Output Capacitance	Coss		41	_	pF	V _{DS} = 25V, f = 1.0MHz, V _{GS} = 0V		
Reverse Transfer Capacitance	Crss	_	4	_				
Gate Resistance	Rg		2.6	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$		
Total Gate Charge	QG	—	12.6	_		V _{DD} = 480V, I _D = 2.5A, V _{GS} = 10V		
Gate-Source Charge	Q _{GS}	_	1.7		nC			
Gate-Drain Charge	QGD	_	7.1	_				
Turn-On Delay Time	tD(ON)	_	10.6	_		$V_{DD} = 300V, R_G = 25\Omega, I_D = 2.5A, V_{GS} = 10V$		
Turn-On Rise Time	tR	_	22	_				
Turn-Off Delay Time	tD(OFF)		34		ns			
Turn-Off Fall Time	tF	_	28	_				
Body Diode Reverse Recovery Time	trr		198		ns	dl/dt = 100A/µs, V _{DS} = 100V,		
Body Diode Reverse Recovery Charge	Q _{RR}		952		nC	IF = 2.5A		

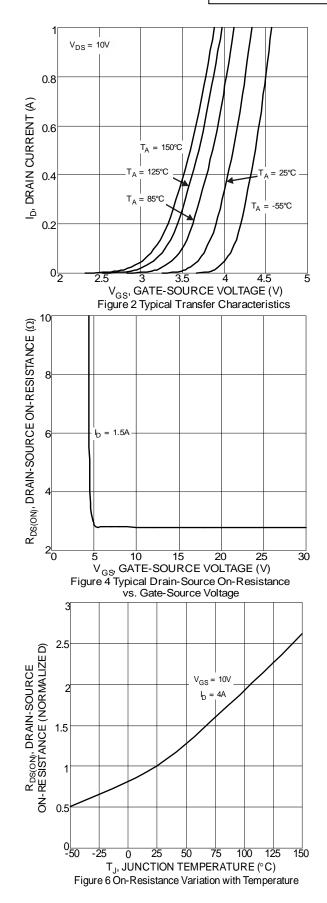
Notes:

Device mounted on infinite heatsink.
 Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad layout.
 Guaranteed by design. Not subject to production testing.
 Short duration pulse test used to minimize self-heating effect.

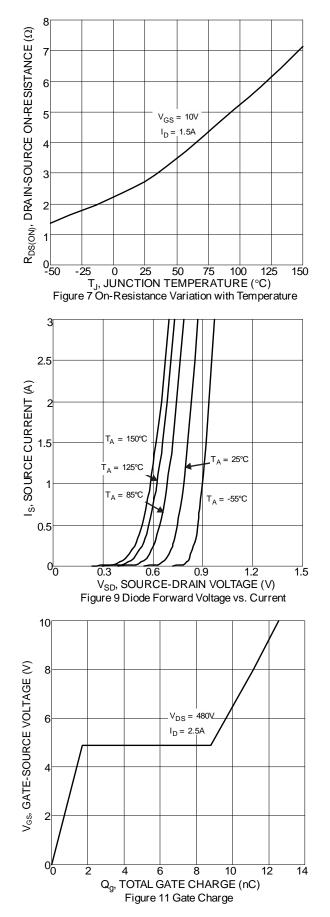


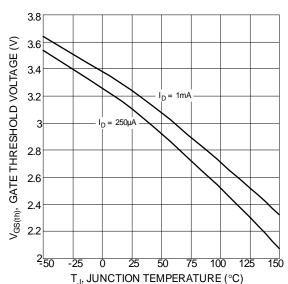
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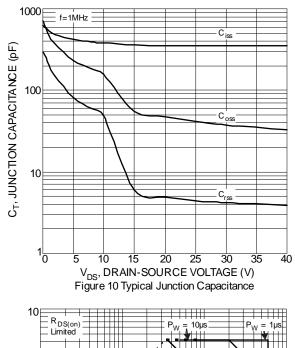


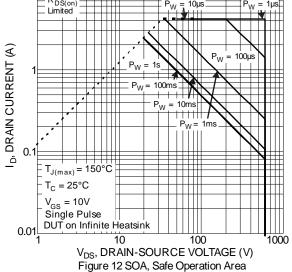




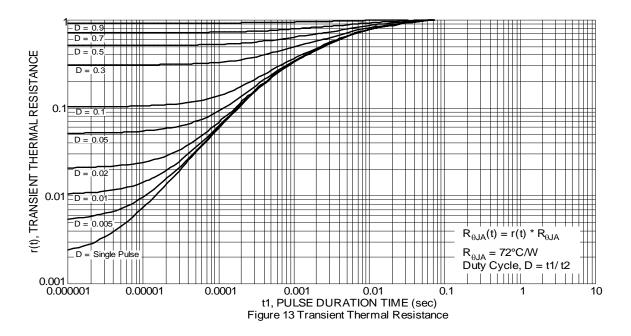








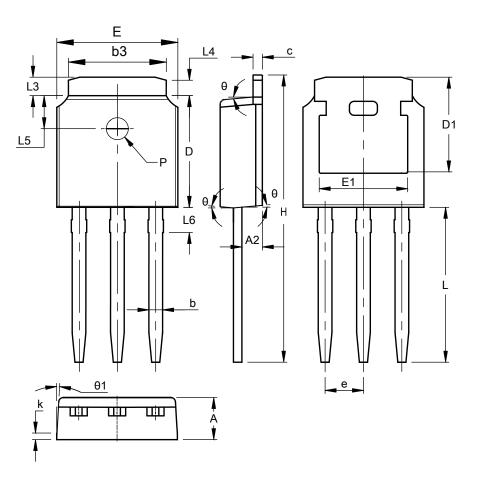






Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.



тс	TO251 (Type TH)						
Dim	Min	Тур					
Α	2.20	2.40	2.30				
A2	0.97	1.17	1.07				
b	0.68	0.90	0.78				
b3	5.20	5.50	5.33				
С	0.43	0.63	0.53				
D	5.98	6.22	6.10				
D1	5	.30 RE	F				
е	2.	286 BS	C				
E	6.40	6.80	6.60				
E1	4.63	5.03	4.83				
н	16.22	16.82	16.52				
k	C	0.40REF					
L	9.15	9.65	9.40				
L3	0.88	1.28	1.02				
L4	0.75 REF						
L5	1.65	1.95	1.80				
L6	0.85	1.25	1.05				
PØ	1.20						
θ	5°	9° 9°	7°				
θ1	5°	7°					
All Dimensions in mm							

TO251 (Type TH)



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