





N-CHANNEL ENHANCEMENT MODE MOSFET

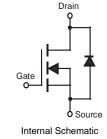
Features

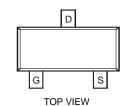
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Lead Free By Design/RoHS Compliant (Note 1)
- "Green" Device (Note 2)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: SOT-23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Terminals Connections: See Diagram Below
- Marking Information: See Page 4
- Ordering Information: See Page 4
- Weight: 0.008 grams (approximate)







Maximum Ratings @T_A = 25°C unless otherwise specified

Charact	eristic		Symbol	Value	Units
Drain-Source Voltage			V _{DSS}	20	V
Gate-Source Voltage		V _{GSS}	±12	V	
Continuous Drain Current (Note 3)	$T_A = 25$ °C $T_A = 85$ °C	I _D	5.47 3.43	А	
Pulsed Drain Current (Note 4)		I _{DM}	20	Α	

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 3)	P _D	0.74	W
Thermal Resistance, Junction to Ambient @T _A = 25°C (Note 3)	$R_{ heta JA}$	167	°C/W
Operating and Storage Temperature Range	T_{J}, T_{STG}	-55 to +150	ů

Notes:

- 1. No purposefully added lead.
- Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.
 Device mounted on FR-4 PCB, with minimum recommended pad layout.
- 4. Repetitive rating, pulse width limited by junction temperature.

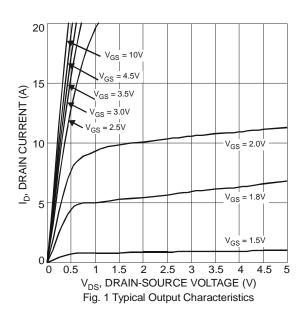


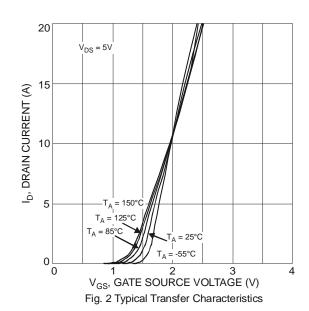
Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition			
OFF CHARACTERISTICS (Note 5)									
Drain-Source Breakdown Voltage	BV _{DSS}	20	-	-	V	$V_{GS} = 0V, I_D = 250\mu A$			
Zero Gate Voltage Drain Current TJ = 25°C	I _{DSS}	-	-	1.0	μΑ	$V_{DS} = 20V$, $V_{GS} = 0V$			
Gate-Source Leakage	I_{GSS}	-	-	±100	nA	$V_{GS} = \pm 12V, V_{DS} = 0V$			
ON CHARACTERISTICS (Note 5)									
Gate Threshold Voltage	V _{GS(th)}	0.5	0.95	1.2	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$			
			21	29		$V_{GS} = 10V, I_D = 6A$			
Static Drain-Source On-Resistance	D		25	35	$m\Omega$	$V_{GS} = 4.5V, I_D = 5A$			
Static Drain-Source On-Resistance	R _{DS (ON)}	-	34	48		$V_{GS} = 2.5V, I_D = 4A$			
			65	91		$V_{GS} = 1.8V, I_D = 2A$			
Forward Transfer Admittance	Y _{fs}	-	9	-	S	$V_{DS} = 5V, I_{D} = 3.8A$			
Diode Forward Voltage	V_{SD}	-	0.75	1.0	V	$V_{GS} = 0V, I_{S} = 1A$			
DYNAMIC CHARACTERISTICS (Note 6)									
Input Capacitance	C _{iss}	-	434.7	-	pF	10/ 10/ 1/			
Output Capacitance	Coss	-	69.1	-	рF	$V_{DS} = 10V, V_{GS} = 0V,$ f = 1.0MHz			
Reverse Transfer Capacitance	C _{rss}	-	61.2	-	pF	1 = 1.000112			
Gate Resistance	R_g	-	1.53	-	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$			
Total Gate Charge	Q_g	-	5.4	-	nC	15/1/1/ 10//			
Gate-Source Charge	Q_{gs}	-	0.9	-	nC	$V_{GS} = 4.5V, V_{DS} = 10V,$			
Gate-Drain Charge	Q_{gd}	-	1.5	-	nC	$I_D = 6A$			
Turn-On Delay Time	t _{D(on)}	-	6.5	-	ns				
Turn-On Rise Time	t _r	-	8.3	-	ns	$V_{DD} = 10V, V_{GS} = 5V,$			
Turn-Off Delay Time	t _{D(off)}	-	21.6	-	ns	$R_L = 1.7\Omega$, $R_G = 6\Omega$			
Turn-Off Fall Time	t _f	-	5.3	-	ns				

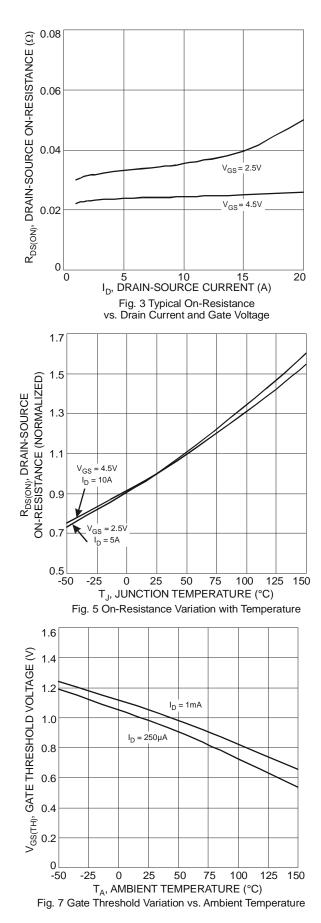
Notes:

- 5. Short duration pulse test used to minimize self-heating effect.
- 6. Guaranteed by design. Not subject to production testing.









0.08 $R_{DS(ON)}$, DRAIN-SOURCE ON-RESISTANCE (Ω) $V_{GS} = 4.5V$ 0.06 $T_A = 150$ °C 0.04 T_A = 125°C T_A = 85°C $T_A = 25^{\circ}C$ 0.02 0 10 19 1_D, DRAIN CURRENT (A) 0 20 15 Fig. 4 Typical Drain-Source On-Resistance

vs. Drain Current and Temperature

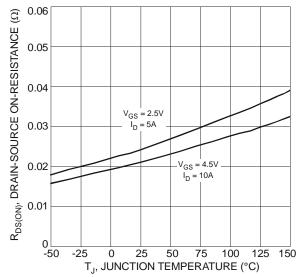
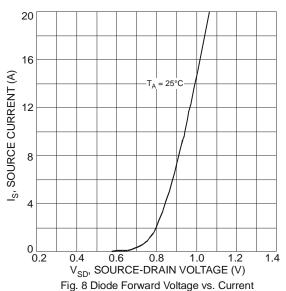
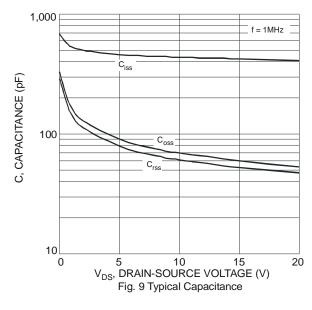


Fig. 6 On-Resistance Variation with Temperature







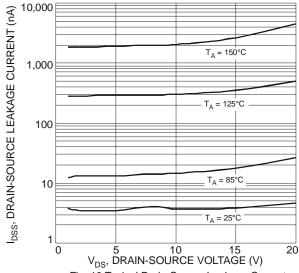


Fig. 10 Typical Drain-Source Leakage Current vs. Drain-Source Voltage

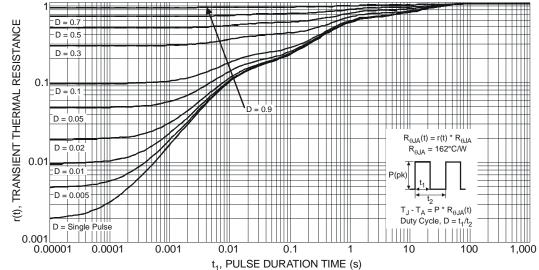


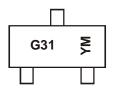
Fig. 11 Transient Thermal Response

Ordering Information (Note 7)

Part Number	Case	Packaging		
DMG3420U-7	SOT-23	3000/Tape & Reel		

Notes: 7. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information



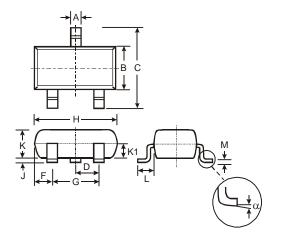
G31 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: W = 2009) M = Month (ex: 9 = September)

Date Code Key

Year	2009	9	2010		2011	20	12	2013		2014	2	2015
Code	W		Χ		Υ		Z	Α		В		С
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

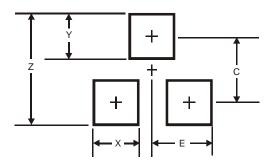


Package Outline Dimensions



SOT-23					
Dim	Min	Max	Тур		
Α	0.37	0.51	0.40		
В	1.20	1.40	1.30		
С	2.30	2.50	2.40		
D	0.89	1.03	0.915		
F	0.45	0.60	0.535		
G	1.78	2.05	1.83		
Н	2.80	3.00	2.90		
7	0.013	0.10	0.05		
K	0.903	1.10	1.00		
K1	-	-	0.400		
L	0.45	0.61	0.55		
M	0.085	0.18	0.11		
α	0°	8°	-		
All Dimensions in mm					

Suggested Pad Layout



Dimensions	Value (in mm)
Z	2.9
Х	0.8
Υ	0.9
С	2.0
E	1.35



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