

COMPLIANT



Low-Voltage, 0.4 Ω R_{ON}, Single SPST Analog Switch

DESCRIPTION

The DG2715, DG2716 are low voltage, single supply, dual SPST analog switches. Designed for high performance switching of analog signals, the DG2715/2716 provide low on-resistance (0.4 Ω at + 2.7 V), fast speed (t_ON, t_OFF at 17 ns and 14 ns) and the ability to handle signals over the entire analog voltage range.

When operated on a + 3 V supply, control pins are compatible with 1.8 V digital logic. Additionally, on-resistance flatness and matching (0.05 Ω and 0.1 $\Omega)$ offer high accuracy between channels.

Built on Vishay Siliconix's low voltage submicron CMOS process, the DG2715, DG2716 were designed to offer solutions that extend beyond audio/video functions, to providing the performance required for today's demanding mixed-signal switching in portable applications.

The DG2715 contains a normally open (NO) switch, and the DG2716 contains a normally closed switch. An epitaxial layer prevents latch-up. All switches conduct equally well in both directions when on, and block up to the power supply level when off.

As a committed partner to the community and the environment, Vishay Siliconix manufactures this product with the lead (Pb)-free device terminations. For analog switching products manufactured with 100 % matte tin device terminations, the lead (Pb)-free "-E3" suffix is being used as a designator.

FEATURES

- Low voltage operation (1.6 V to 3.6 V)
- Low on-resistance $R_{DS(on)}$: 0.4 Ω at 2.7 V
- Off-isolation: 57 dB at 1 MHz
- Fast switching: 25 ns t_{ON}
- Low charge injection Q_{IN.J}: 9 pC
- Low power consumption: < 1 μW
- SC-70 5-lead package

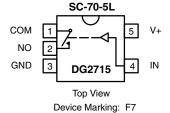
BENEFITS

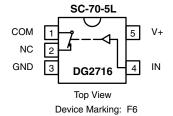
- High accuracy
- · High bandwidth
- TTL and low voltage logic compatibility
- · Low power consumption
- · Reduced PCB space

APPLICATIONS

- · Mixed signal routing
- · Portable and battery operated systems
- · Low voltage data acquisition
- Modems
- PCMCIA cards

FUNCTIONAL BLOCK DIAGRAM AND PIN CONFIGURATION





TRUTH TABLE (DG2715)					
Logic Switch					
0	OFF				
1	ON				

TRUTH TABLE (DG2716)					
Logic Switch					
0	ON				
1	OFF				

ORDERING INFORMATION						
Temp. Range Package Part Number						
- 40 °C to 85 °C	SC70-5	DG2715DL-T1-E3				
		DG2716DL-T1-E3				

DG2715, DG2716

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ABSOLUTE MAXIMUM RATINGS						
Parameter	Limit	Unit				
Reference V+ to GND	- 0.3 to + 4	V				
IN, COM, NC, NO ^a	- 0.3 to (V+ + 0.3 V)	V				
Continuous Current (NO, NC and COM Pir	± 200	mA				
Peak Current (Pulsed at 1 ms, 10 % duty c	± 300	IIIA				
Storage Temperature	(D Suffix)	- 65 to 150	°C			
Power Dissipation (Packages) ^b	5-Pin SC-70 ^c	250	mW			

Notes:

- a. Signals on NC, NO, or COM or IN exceeding V+ will be clamped by internal diodes. Limit forward diode current to maximum current ratings.
- b. All leads welded or soldered to PC board.
- c. Derate 3.1 mW/°C above 70 °C.

SPECIFICATIONS (V+	= 1.8 V)						
		Test Conditions Otherwise Unless Specified			Limits - 40 °C to 85 °C		
Parameter	Symbol	$V+ = 1.8 \text{ V}, \pm 10 \text{ \%}, V_{IN} = 0.4 \text{ V or } 1.0 \text{ V}^e$	Temp.a	Min.b	Typ. ^c	Max. ^b	Unit
Analog Switch							
Analog Signal Range ^d	V_{NO}, V_{NC}, V_{COM}		Full	0		V+	V
On-Resistance	R _{ON}	V+ = 1.8 V, V _{COM} = 0.9 V I _{NO} , I _{NC} = 10 mA	Room Full ^d		0.7	1.5 2.0	Ω
Switch Off Lackage Command	$I_{NO(off)}$ $I_{NC(off)}$ $V+=2.0 V.$		Room Full ^d	- 1 - 10		1 10	
Switch Off Leakage Current ^r	I _{COM(off)}	V_{NO} , $V_{NC} = 0.2 \text{ V}/1.8 \text{ V}$, $V_{COM} = 1.8 \text{ V}/0.2 \text{ V}$	Room Full ^d	- 1 - 10		1 10	nA
Channel-On Leakage Current ^f	I _{COM(on)}	$V+ = 2.0 \text{ V}, V_{NO}, V_{NC} = V_{COM} = 0.2 \text{ V}/1.8 \text{ V}$	Room Full ^d	- 1 - 10		1 10	
Digital Control							
Input High Voltage	V _{INH}		Full	1.0			V
Input Low Voltage	V _{INL}		Full			0.4	•
Input Capacitance ^d	C _{in}		Full		4		pF
Input Current ^f	I _{INL} or I _{INH}	$V_{IN} = 0 \text{ or } V+$	Full	- 1		1	μΑ
Dynamic Characteristics							
Turn-On Time ^d	t _{ON}	V_{NO} or V_{NC} = 1.5 V, R_L = 50 Ω , C_L = 35 pF	Room Full ^d		24	36	ns
Turn-Off Time ^d	t _{OFF}	figures1 and 2	Room Full ^d		21	33	110
Charge Injection ^d	Q _{INJ}	$C_L = 1 \text{ nF}, V_{GEN} = 0 \text{ V}, R_{GEN} = 0 \Omega, \text{ figure } 3$	Room		13		рC
Off-Isolation ^d	OIRR	$R_L = 50 \Omega$, $C_L = 5 pF$, $f = 1 MHz$	Room		- 57		dB
N _O , N _C Off Capacitance ^d	C _{NO(off)} C _{NC(off)}	V _{IN} = 0 or V+, f = 1 MHz	Room		78		pF
Channel-On Capacitance ^d	C _{ON}		Room	_	93		



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SPECIFICATIONS (V+	= 3.0 V)						
		Test Conditions Otherwise Unless Specified		Limits - 40 °C to 85 °C			
Parameter	Symbol	$V+ = 3 V$, $\pm 10 \%$, $V_{IN} = 0.5 V$ or 1.4 V^e	Temp.a	Min.b	Typ.c	Max.b	Unit
Analog Switch							
Analog Signal Range ^d	V_{NO}, V_{NC}, V_{COM}		Full	0		V+	٧
On-Resistance	R _{ON}	$V+ = 2.7 \text{ V}, V_{COM} = 1.5 \text{ V}$ $I_{NO}, I_{NC} = 100 \text{ mA}$	Room Full		0.4	0.6	
R _{ON} Flatness	R _{ON} Flatness	$V+ = 2.7 \text{ V}, V_{COM} = 0.6 \text{ V}, 1.5 \text{ V}, 2.1 \text{ V}$ $I_{NO}, I_{NC} = 100 \text{ mA}$	Room		0.1	0.2	Ω
Switch Off Leakage Current	I _{NO(off)} I _{NC(off)}	V+ = 3.3 V,	Room Full	- 1 - 10		1 10	nA
	I _{COM(off)}	V_{NO} , $V_{NC} = 0.3 \text{ V/3 V}$, $V_{COM} = 3 \text{ V/0.3 V}$	Room Full	- 1 - 10		10	
Channel-On Leakage Current	I _{COM(on)}	$V+ = 3.3 \text{ V}, V_{NO}, V_{NC} = V_{COM} = 0.3 \text{ V/3 V}$	Room Full	- 1 - 10		1 10	
Digital Control							
Input High Voltage	V _{INH}		Full	1.4			V
Input Low Voltage	V _{INL}		Full			0.5	·
Input Capacitance ^d	C _{in}		Full		5		pF
Input Current	I _{INL} or I _{INH}	$V_{IN} = 0$ or $V+$	Full	- 1		1	μΑ
Dynamic Characteristics							
Turn-On Time	t _{ON}	V_{NO} or V_{NC} = 1.5 V, R_{L} = 50 Ω , C_{L} = 35 pF	Room Full		17	29	ns
Turn-Off Time	t _{OFF}	Figure 1	Room Full		14	26	113
Charge Injection ^d	Q_{INJ}	$C_L = 1 \text{ nF}, V_{GEN} = 0 \text{ V}, R_{GEN} = 0 \Omega$, figure 3	Room		9		рC
Off-Isolation ^d	OIRR	$R_L = 50 \Omega$, $C_L = 5 pF$, $f = 1 MHz$	Room		- 57		dB
NO, NC Off Capacitance ^d	C _{NO(off)} C _{NC(off)}	V _{IN} = 0 or V+, f = 1 MHz	Room		72		pF
Channel-On Capacitance ^d	C _{ON}		Room		92]
Power Supply							•
Power Supply Range	V+			1.5		3.6	V
Power Supply Current	I+	$V+ = 3.6 \text{ V}, V_{IN} = 0 \text{ or } V+$			0.01	1.0	μΑ

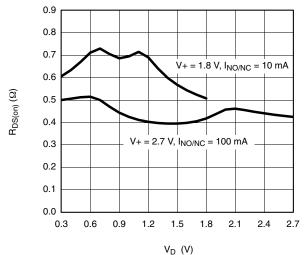
Notes:

- a. Room = 25 $^{\circ}$ C, Full = as determined by the operating suffix.
- b. The algebraic convention whereby the most negative value is a minimum and the most positive a maximum, is used in this data sheet.
- c. Typical values are for design aid only, not guaranteed nor subject to production testing.
- d. Guarantee by design, nor subjected to production test.
- e. V_{IN} = input voltage to perform proper function.
- f. Guaranteed by 3 V leakage testing, not production tested.

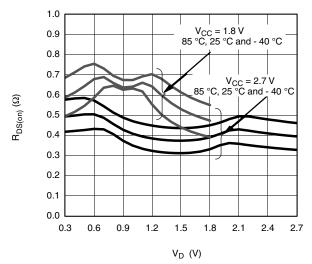
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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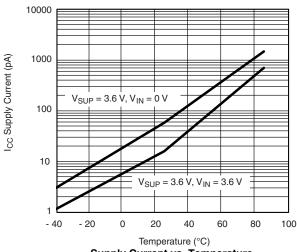
TYPICAL CHARACTERISTICS ($T_A = 25$ °C, unless otherwise noted)



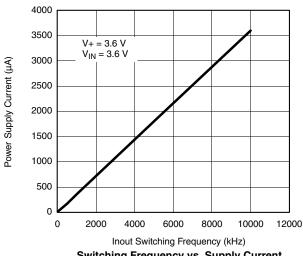
 $R_{DS(on)}$ vs. V_{COM} vs. 1 V_{CC}



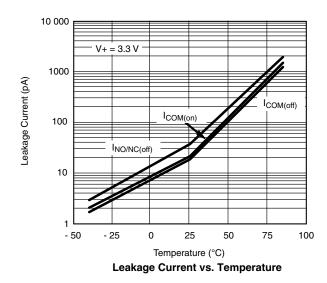
 $R_{DS(on)}$ vs. $V_D,\,V_{CC}$ and Temperature



Supply Current vs. Temperature



Switching Frequency vs. Supply Current



V + = 3.3 V1500 1000 I_{COM(on)} Leakage Current (pA) I_{COM(off)} 500 0 - 500 I_{NO/NC(of} - 1000 - 1500 - 2000 0.0 0.5 1.0 1.5 2.0 2.5 3.0 3.5 V_{COM}, V_{NO}, V_{NC} - Analog Voltage (V)

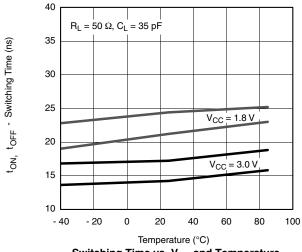
2000

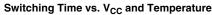
Leakage Current vs. Analog Voltage

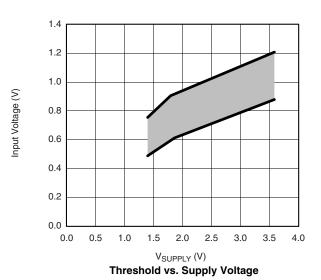


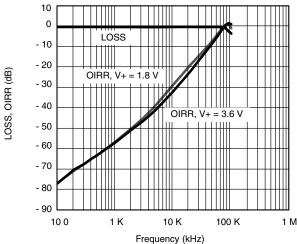


TYPICAL CHARACTERISTICS ($T_A = 25$ °C, unless otherwise noted)

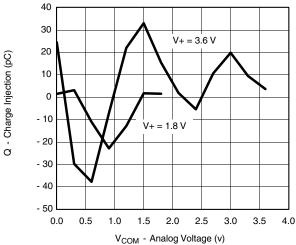








Insertion Loss, Off-Isolation vs. Frequency

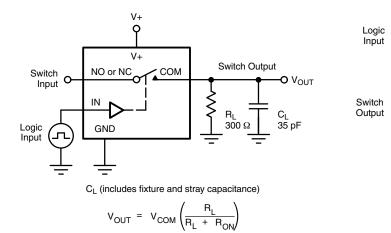


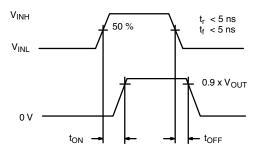
Charge Injection vs. Analog Voltage

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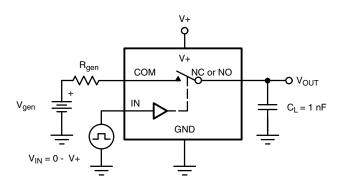
TEST CIRCUITS

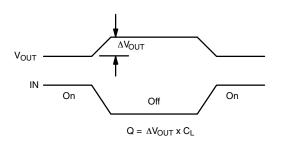




Logic "1" = Switch On Logic input waveforms inverted for switches that have the opposite logic sense.

Figure 1. Switching Time





IN depends on switch configuration: input polarity determined by sense of switch.

Figure 2. Charge Injection



TEST CIRCUITS

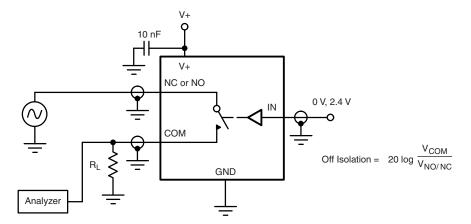


Figure 3. Off-Isolation

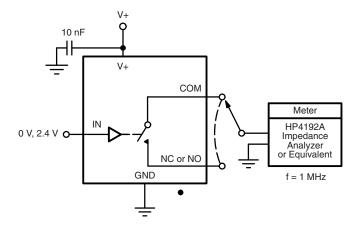


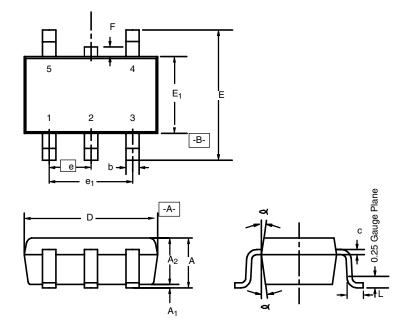
Figure 4. Channel Off/On Capacitance

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?72740.





SC-70: 5-LEAD



	i							
	MILLIMETERS			ı	S			
Dim	Min	Nom	Max	Min	Nom	Max		
Α	0.90	-	1.10	0.035	-	0.043		
A ₁	_	-	0.10	-	_	0.004		
A ₂	0.80	-	1.00	0.031	_	0.039		
b	0.15	-	0.30	0.006	_	0.012		
С	0.10	-	0.25	0.004	_	0.010		
D	1.80	2.00	2.20	0.071	0.079	0.087		
Е	1.80	2.10	2.40	0.071	0.083	0.094		
E ₁	1.15	1.25	1.35	0.045	0.049	0.053		
е	0.65BSC			0.026BSC				
e ₁	1.20	1.30	1.40	0.047	0.051	0.055		
F	0	-	0.15	0	-	0.006		
L	0.10	0.20	0.30	0.004	0.008	0.012		
ø	7°Nom 7°Nom							
ECN: S-60035—Rev. C, 16-Jan-06 DWG: 5861								





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