



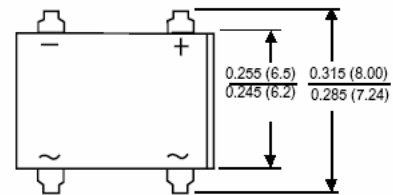
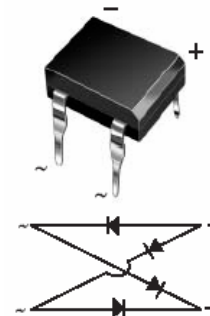
## Features

- ◆ Ideal for printed circuit boards
- ◆ Applicable for automotive insertion
- ◆ High surge current capability
- ◆ Solder Dip 260°C, 40seconds

## Mechanical Data

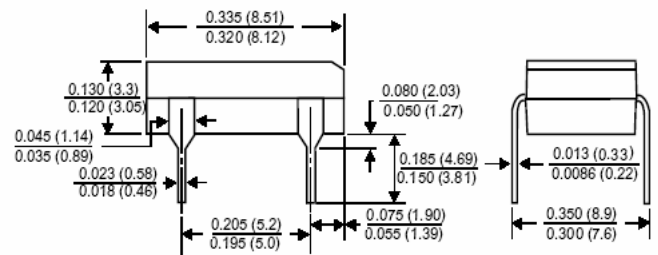
- ◆ Case:DF  
Epoxy meets UL-94V-0 Flammability rating
- ◆ Terminals:Matte tin plated (E3 Suffix) leads, solderable per J-STD-002B and JESD22-B102D
- ◆ Polarity:As marked on body

Case Style DFM



## Typical Applications

General purpose use in ac-to-dc bridge full wave rectification for SMPS, Lighting Ballaster, Adapter, Battery Charger, Home Appliances, Office Equipment, and Telecommunication applications.



## Maximum Ratings & Electrical Characteristics

Ratings at 25°C

ambient temperature unless otherwise specified.

| Parameter                                                                             | Symbol                           | DF005       | DF01 | DF02 | DF04 | DF06 | DF08 | DF10 | Unit               |
|---------------------------------------------------------------------------------------|----------------------------------|-------------|------|------|------|------|------|------|--------------------|
| Maximum repetitive peak reverse voltage                                               | V <sub>RRM</sub>                 | 50          | 100  | 200  | 400  | 600  | 800  | 1000 | V                  |
| Maximum RMS voltage                                                                   | V <sub>RMS</sub>                 | 35          | 70   | 140  | 280  | 420  | 560  | 700  | V                  |
| Maximum DC blocking voltage                                                           | V <sub>DC</sub>                  | 50          | 100  | 200  | 400  | 600  | 800  | 1000 | V                  |
| Maximum Average forward output rectified current at TA=40°C                           | I <sub>F(AV)</sub>               | 1.0         |      |      |      |      |      |      | A                  |
| Peak forward surge current single sine-wave superimposed on rated load (JEDEC Method) | I <sub>FSM</sub>                 | 30          |      |      |      |      |      |      | A                  |
| Rating for fusig (t<8.3ms)                                                            | I <sup>2</sup> t                 | 3.7         |      |      |      |      |      |      | A <sup>2</sup> sec |
| Maximum instantaneous forward voltage drop per leg at 0.5A                            | V <sub>F</sub>                   | 1.10        |      |      |      |      |      |      | V                  |
| Maximum DC reverse current at TA=25°C<br>rated DC blocking voltage per leg TA=125°C   | I <sub>R</sub>                   | 5<br>500    |      |      |      |      |      |      | μA                 |
| Typical junction capacitance per eiement at 4.0V, 1MHz                                | C <sub>j</sub>                   | 25          |      |      |      |      |      |      | pF                 |
| Typical thermal resistance per leg (Note 1)                                           | R <sub>θJA</sub>                 | 40          |      |      |      |      |      |      | °C/W               |
|                                                                                       | R <sub>θJL</sub>                 | 15          |      |      |      |      |      |      |                    |
|                                                                                       | R <sub>θJC</sub>                 | 18          |      |      |      |      |      |      |                    |
| Operating junction and Storage temperature range                                      | T <sub>J</sub> ,T <sub>STG</sub> | -55 to +150 |      |      |      |      |      |      | °C                 |

**Notes:** 1. Device mounted P.C.B with 0.47x0.47"(12mmx12mm) Copper Pads.

2. JEDEC registered values

**Ratings and Characteristics Curves** ( $T_A = 25^\circ\text{C}$  unless otherwise noted)

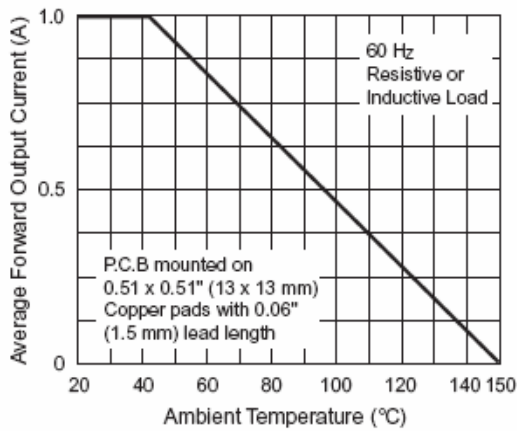


Figure 1. Derating Curve Output Rectified Current

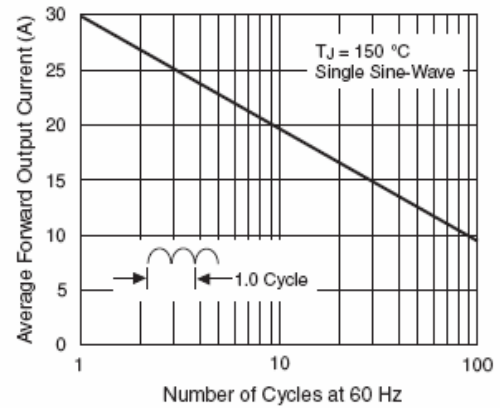


Figure 2. Maximum Non-Repetitive Peak Forward Surge Current Per Leg

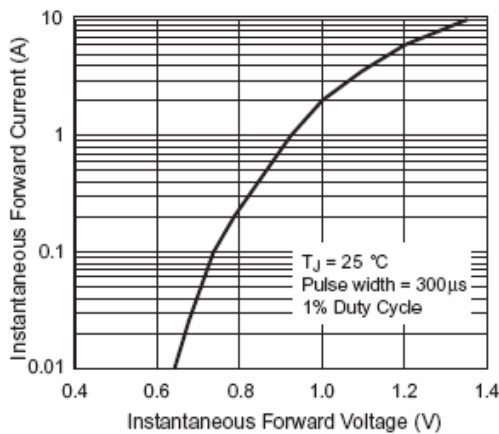


Figure 3. Typical Forward Characteristics Per Leg

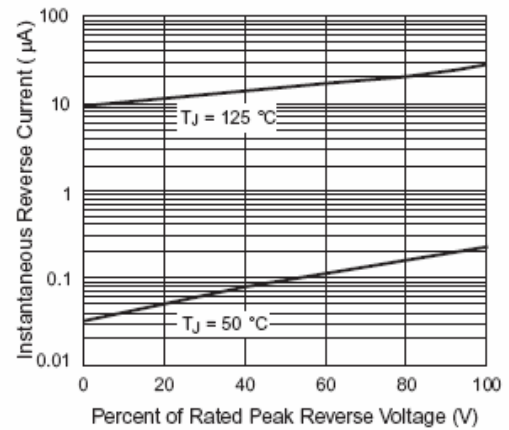


Figure 4. Typical Reverse Leakage Characteristics Per Leg

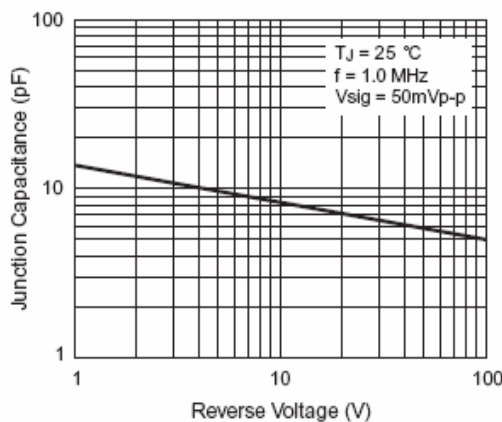


Figure 5. Typical Junction Capacitance Per Leg

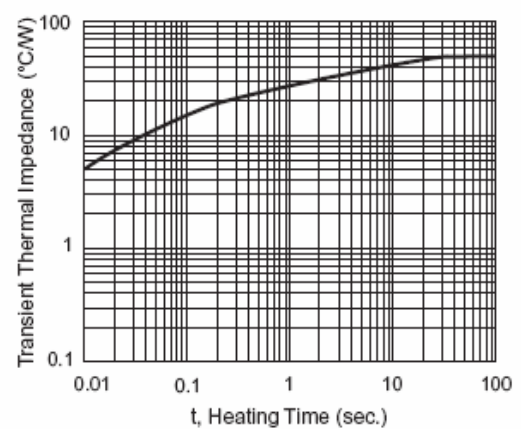
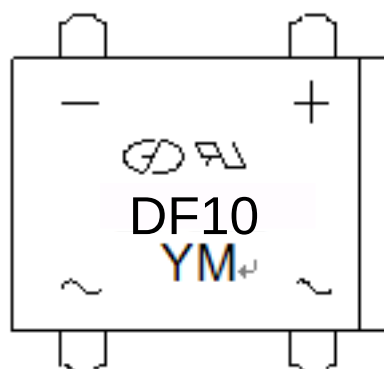


Figure 6. Typical Transient Thermal Impedance Per Leg

# Marking



## DATE CODE

| Year  | 2009 | 2010 | 2011 | 2012 | 2013 | 2014 | 2015 | 2016 | 2017 | 2018 | 2019 | 2020 |
|-------|------|------|------|------|------|------|------|------|------|------|------|------|
| Code  | 9    | A    | B    | C    | D    | E    | F    | G    | H    | J    | K    | 0    |
| Month | 1    | 2    | 3    | 4    | 5    | 6    | 7    | 8    | 9    | 10   | 11   | 12   |
| Code  | 1    | 2    | 3    | 4    | 5    | 6    | 7    | 8    | 9    | O    | N    | D    |