

FEATURES

- Double Side Cooling
- High Surge Capability

APPLICATIONS

- Crowbar
- High Power Drives
- High Voltage Power Supplies
- Static Switches

KEY PARAMETERS

V_{DRM}	5200V
I_{T(AV)}	4310A
I_{TSM}	53300A
dV/dt*	2000V/μs
dI/dt	1000A/μs

* Higher dV/dt selections are available on request

VOLTAGE RATINGS

Part and Ordering Number	Repetitive Peak Voltages V _{DRM} and V _{RRM} (V)	Conditions
DCR4330M52*	5200	T _{vj} = -40°C to 125°C, I _{DRM} = I _{RRM} = 300mA, V _{DRM} , V _{RRM} t _p = 10ms V _{DSM} & V _{RSM} = V _{DRM} & V _{RRM} + 100V respectively
DCR4330M50	5000	
DCR4330M45	4500	

Lower voltage grades available.

*5000V @ -40°C, 5200V @ 0°C

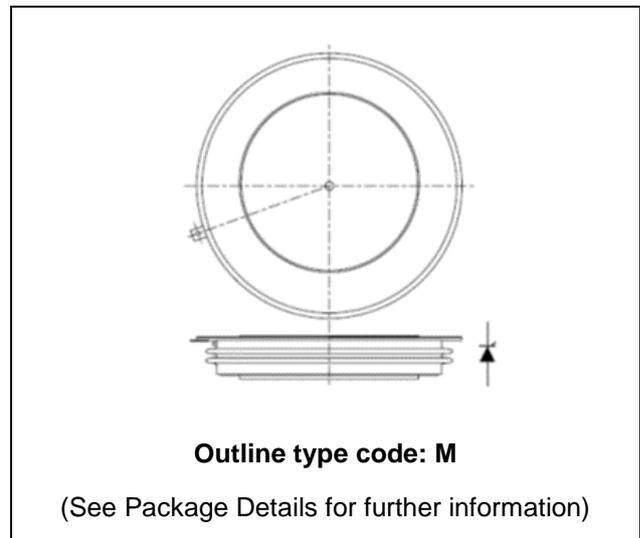


Fig. 1 Package outline

ORDERING INFORMATION

When ordering, select the required part number shown in the Voltage Ratings selection table.

For example:

DCR4330M52

Note: Please use the complete part number when ordering and quote this number in any future correspondence relating to your order.

CURRENT RATINGS

$T_{case} = 60^{\circ}\text{C}$ unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
Double Side Cooled				
$I_{T(AV)}$	Mean on-state current	Half wave resistive load	4310	A
$I_{T(RMS)}$	RMS value	-	6770	A
I_r	Continuous (direct) on-state current	-	6220	A

SURGE RATINGS

Symbol	Parameter	Test Conditions	Max.	Units
I_{TSM}	Surge (non-repetitive) on-state current	10ms half sine, $T_{case} = 125^{\circ}\text{C}$ $V_R = 0$	53.3	kA
I^2t	I^2t for fusing		14.2	MA ² s

THERMAL AND MECHANICAL RATINGS

Symbol	Parameter	Test Conditions	Min.	Max.	Units	
$R_{th(j-c)}$	Thermal resistance - junction to case	Double side cooled	DC	-	5.2	$^{\circ}\text{C}/\text{kW}$
		Single side cooled	Anode DC	-	10.1	$^{\circ}\text{C}/\text{kW}$
			Cathode DC	-	10.8	$^{\circ}\text{C}/\text{kW}$
$R_{th(c-h)}$	Thermal resistance - case to heatsink	Clamping force 83kN (with mounting compound)	Double side	-	1.0	$^{\circ}\text{C}/\text{kW}$
			Single side	-	2.0	$^{\circ}\text{C}/\text{kW}$
T_{vj}	Virtual junction temperature	Blocking V_{DRM} / V_{RRM}	-	125	$^{\circ}\text{C}$	
T_{stg}	Storage temperature range		-55	125	$^{\circ}\text{C}$	
F_m	Clamping force		74	91	kN	

DYNAMIC CHARACTERISTICS

Symbol	Parameter	Test Conditions	Typ.	Max.	Units
I_{RRM}/I_{DRM}	Peak reverse and off-state current	At V_{RRM}/V_{DRM} , $T_{case} = 125^{\circ}C$	-	300	mA
		At 50% V_{RRM}/V_{DRM} , $T_{case} = 125^{\circ}C$	20	-	mA

Symbol	Parameter	Test Conditions	Min.	Max.	Units	
V_{TM}	Instantaneous forward voltage	At 4000A peak, $T_j = 125^{\circ}C$	1.45	1.65	V	
dV/dt	Max. linear rate of rise of off-state voltage	To 67% V_{DRM} , $T_j = 125^{\circ}C$, gate open	-	2000	V/ μ s	
dI/dt	Rate of rise of on-state current	From 67% V_{DRM} to $2x I_{T(AV)}$ Gate source 30V, 10Ω $t_r < 0.5\mu$ s, $T_j = 125^{\circ}C$	Repetitive 50Hz	-	400	A/ μ s
			Non-repetitive	-	1000	A/ μ s
V_{T(ro)}	Threshold voltage - Low level	500A to 3600A at $T_{case} = 125^{\circ}C$	-	0.85	V	
	Threshold voltage - High level	3600A to 9000A at $T_{case} = 125^{\circ}C$	-	1.03	V	
r_T	On-state slope resistance - Low level	500A to 3600A at $T_{case} = 125^{\circ}C$	-	0.21	m Ω	
	On-state slope resistance - High level	3600A to 9000A at $T_{case} = 125^{\circ}C$	-	0.16	m Ω	
t_{gd}	Delay time	$V_D = 67\% V_{DRM}$, gate source 30V, 10Ω $t_r = 0.5\mu$ s, $T_j = 25^{\circ}C$	-	3	μ s	
t_q	Turn-off time	$T_j = 125^{\circ}C$, $V_R = 200V$, $dI/dt = 1A/\mu$ s $dV_{DR}/dt = 20V/\mu$ s linear	-	750	μ s	
Q_s	Stored charge	$I_T = 1500A$, $T_j = 125^{\circ}C$, $dI/dt = 1A/\mu$ s	3870	8340	μ C	
I_{RR}	Reverse recovery current	$V_R \sim 2100V$, $C_s = 1\mu$ F, $R_s = 63\Omega$	51	83	A	
I_L	Latching current	$T_j = 25^{\circ}C$, $V_D = 5V$	-	3	A	
I_H	Holding current	$T_j = 25^{\circ}C$, $R_{G-K} = \infty$, $I_{TM} = 500A$, $I_T = 5A$	-	300	mA	

GATE TRIGGER CHARACTERISTICS AND RATINGS

Symbol	Parameter	Test Conditions	Max.	Units
V_{GT}	Gate trigger voltage	$V_{DRM} = 5V, T_{case} = 25^{\circ}C$	1.5	V
V_{GD}	Gate non-trigger voltage	At 50% $V_{DRM}, T_{case} = 125^{\circ}C$	0.4	V
I_{GT}	Gate trigger current	$V_{DRM} = 5V, T_{case} = 25^{\circ}C$	400	mA
I_{GD}	Gate non-trigger current	At 50% $V_{DRM}, T_{case} = 125^{\circ}C$	10	mA

CURVES

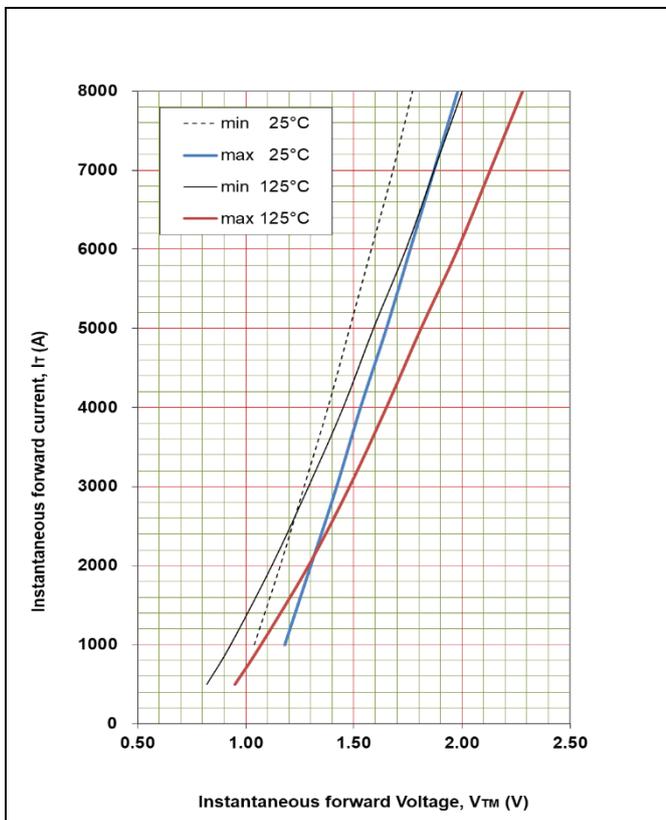


Fig. 2 Maximum & minimum on-state characteristics

V_{TM} EQUATION

$$V_{TM} = A + B \cdot \ln(I_T) + C \cdot I_T + D \cdot \sqrt{I_T}$$

Where $A = 0.061663$

$B = 0.116682$

$C = 0.000119$

$D = 0.002396$

These values are valid for $T_j = 125^{\circ}C$ for I_T 500A to 9000A

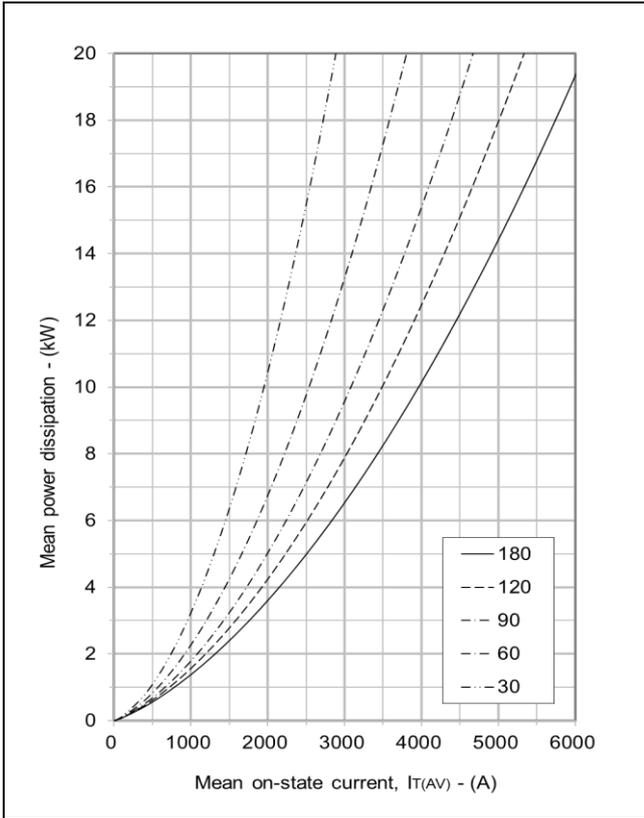


Fig. 3 On-state power dissipation - sine wave

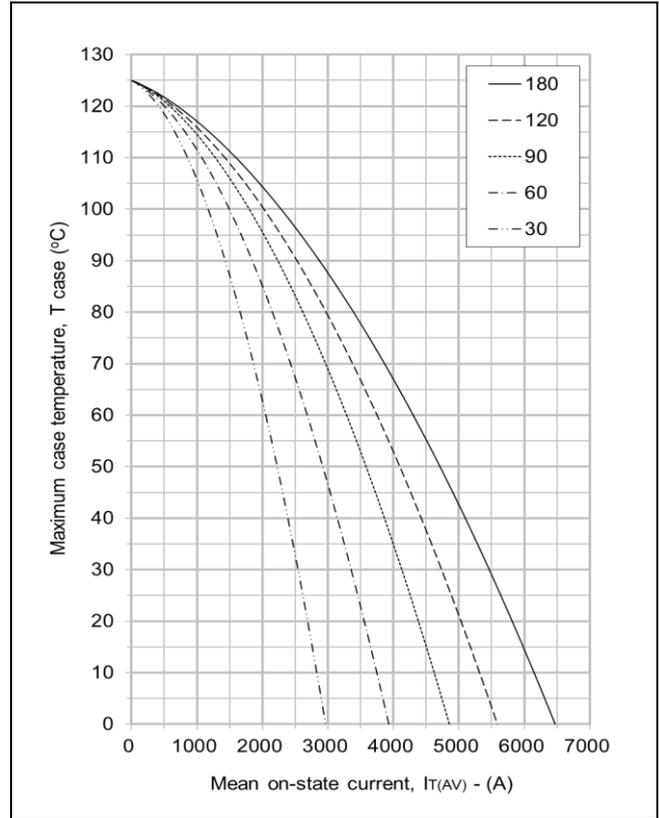


Fig. 4 Maximum permissible case temperature, double side cooled - sine wave

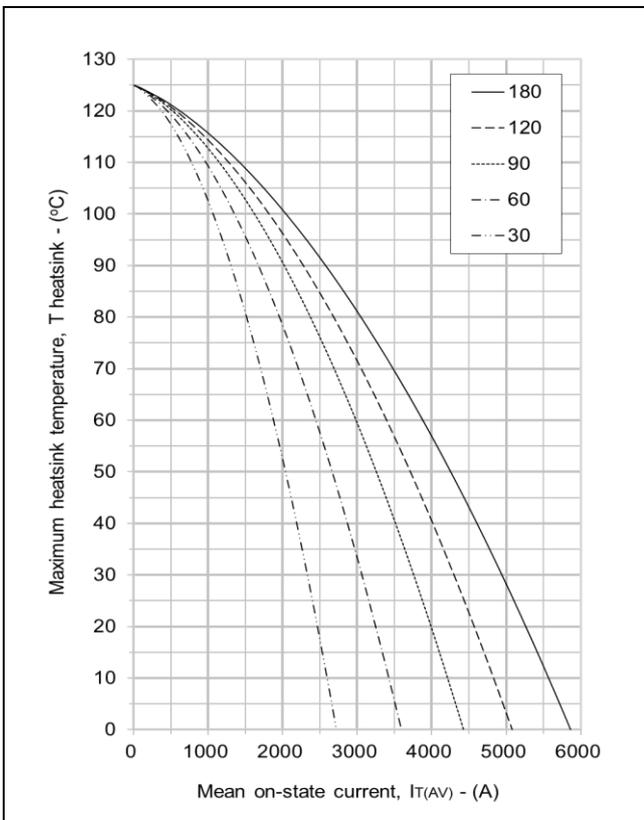


Fig. 5 Maximum permissible heatsink temperature, double side cooled - sine wave

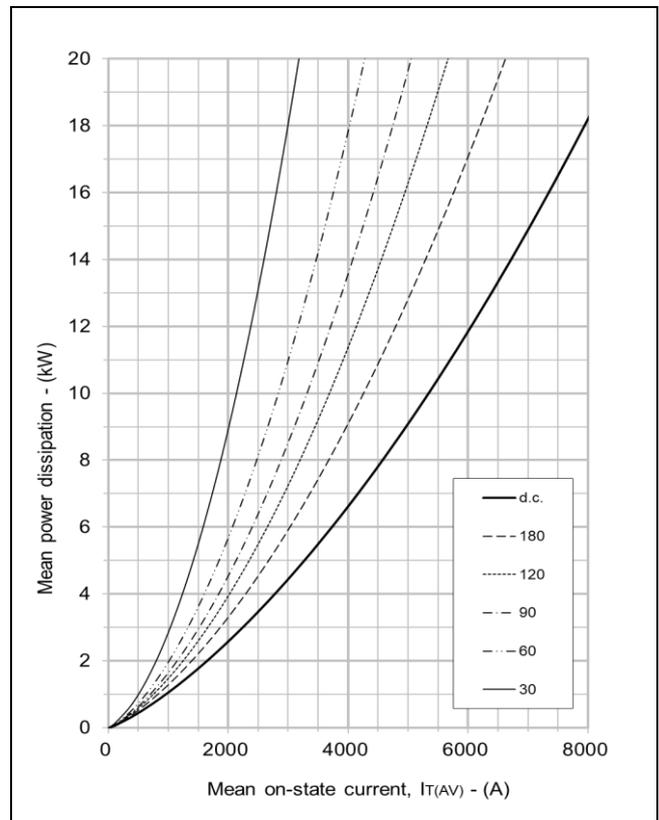


Fig. 6 On-state power dissipation - rectangular wave

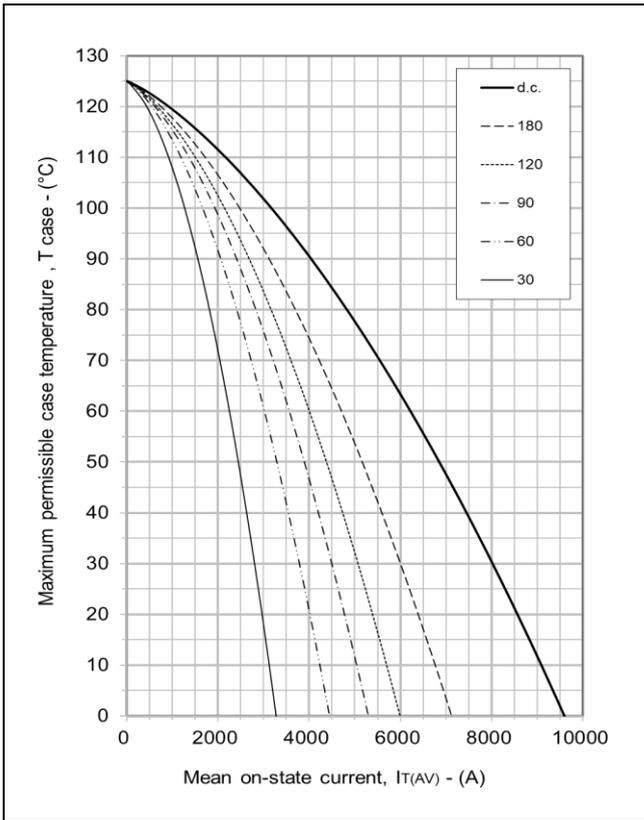


Fig. 7 Maximum permissible case temperature, double side cooled - rectangular wave

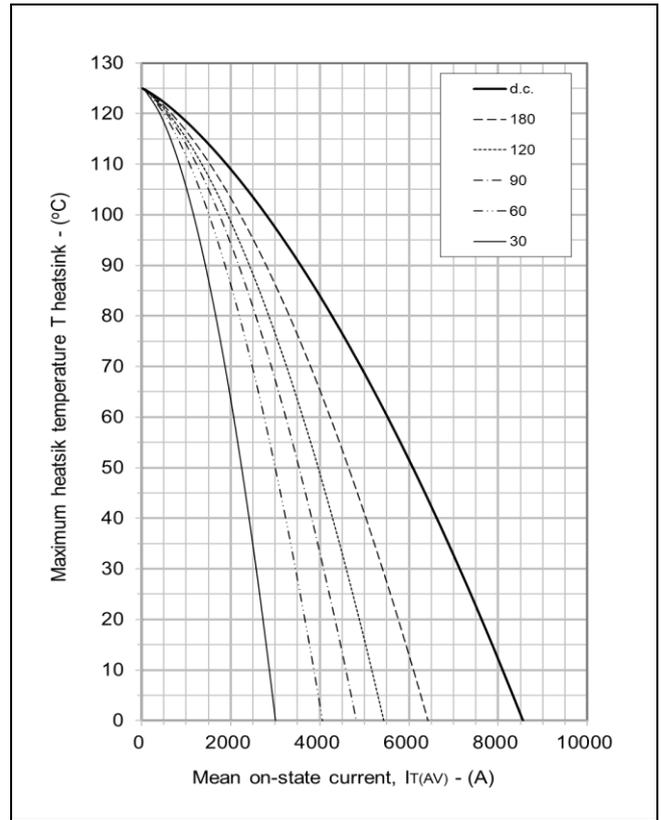


Fig. 8 Maximum permissible heatsink temperature, double side cooled - rectangular wave

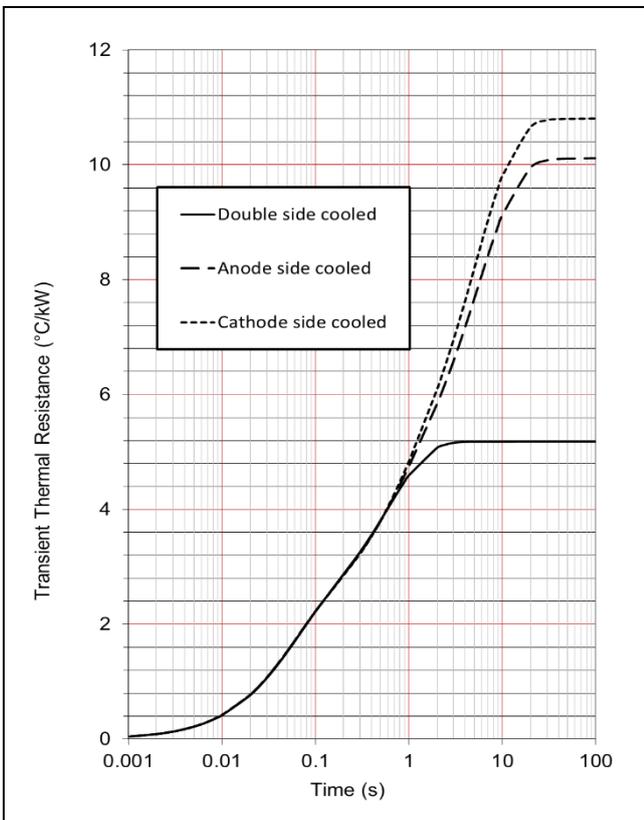


Fig.9 Maximum (limit) transient thermal impedance – junction to case (degC/kW)

		1	2	3	4
Double side cooled	Ri(°C/kW)	1.995	1.243	1.945	0.005
	Ti(s)	0.050	0.593	0.592	110.511
Anode side cooled	Ri(°C/kW)	6.093	1.957	2.042	0.036
	Ti(s)	5.460	0.511	0.050	110.174
Cathode side cooled	Ri(°C/kW)	6.857	1.876	2.063	0.025
	Ti(s)	5.181	0.557	0.050	110.155

$$Z_{th} = \sum_{i=1}^{i=4} R_i \cdot \left(1 - \exp\left(-\frac{T}{T_i}\right)\right)$$

$\Delta R_{th(j-c)}$ Conduction

Tables show the increments of thermal resistance $R_{th(j-c)}$ when the device operates at conduction angles other than d.c.

Double side cooling			Anode Side Cooling			Cathode Sided Cooling		
θ^*	$\Delta Z_{th}(z)$		θ^*	$\Delta Z_{th}(z)$		θ^*	$\Delta Z_{th}(z)$	
	sine.	rect.		sine.	rect.		sine.	rect.
180	0.51	0.36	180	0.51	0.36	180	0.51	0.36
120	0.57	0.49	120	0.58	0.50	120	0.58	0.50
90	0.64	0.56	90	0.65	0.57	90	0.65	0.57
60	0.70	0.63	60	0.71	0.64	60	0.71	0.64
30	0.74	0.71	30	0.75	0.71	30	0.75	0.71
15	0.76	0.74	15	0.77	0.75	15	0.77	0.75

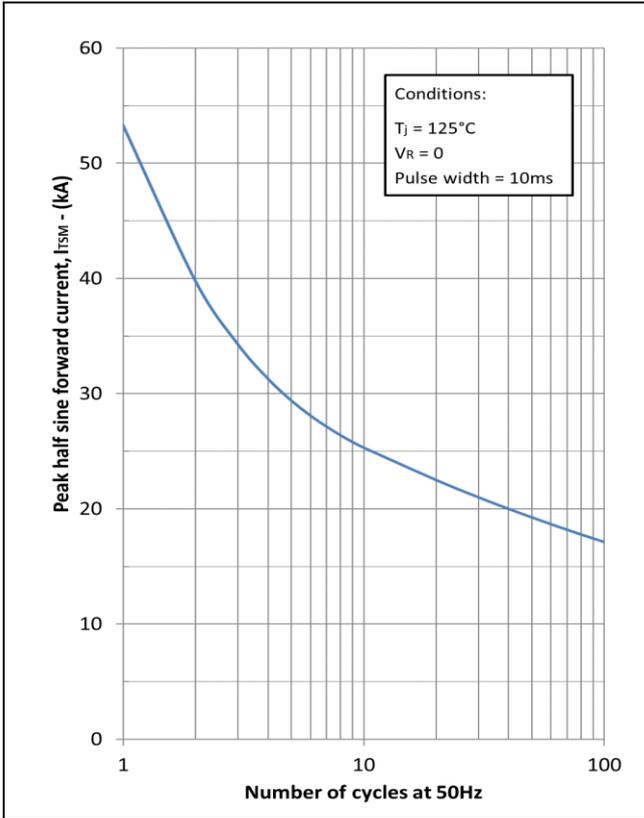


Fig. 10 Multi-cycle surge current

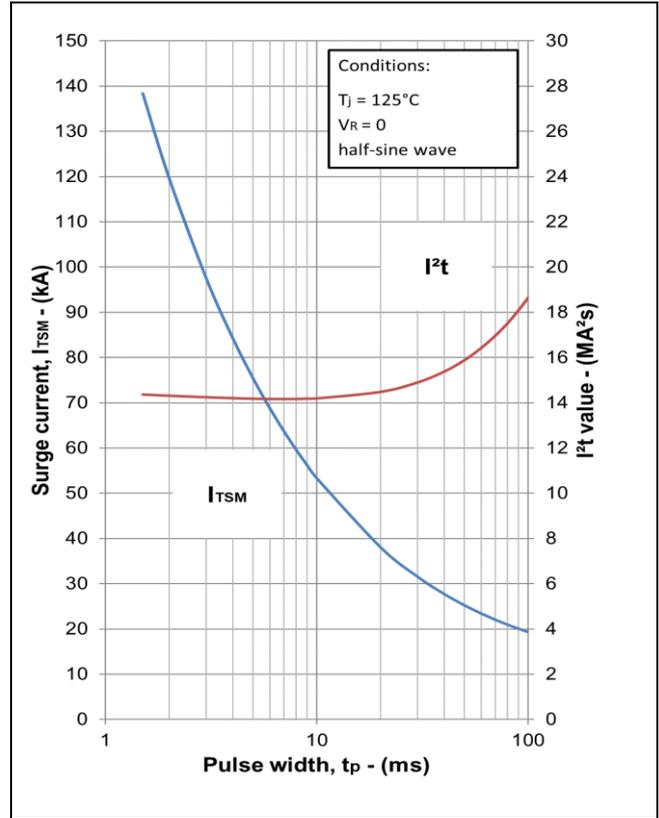


Fig. 11 Single-cycle surge current

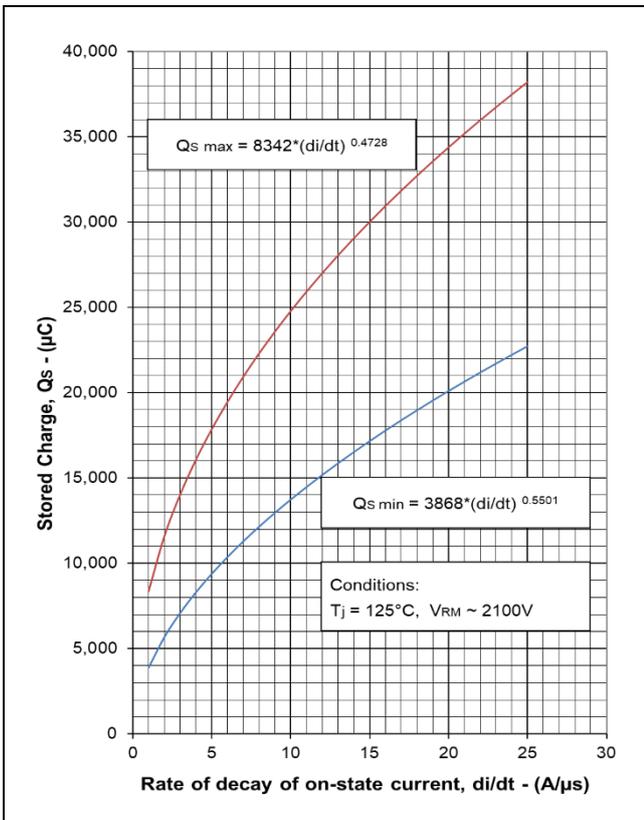


Fig. 12 Stored charge

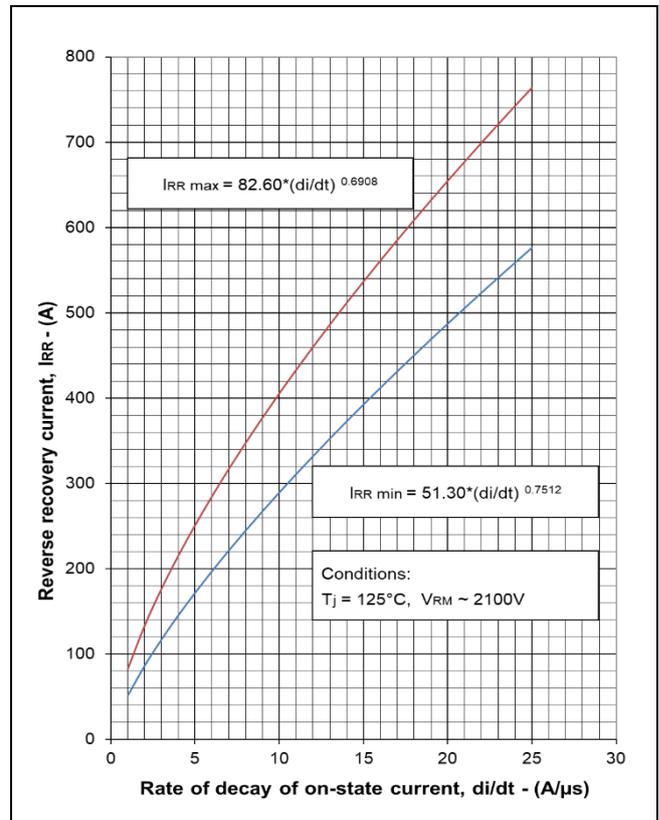


Fig. 13 Reverse recovery current

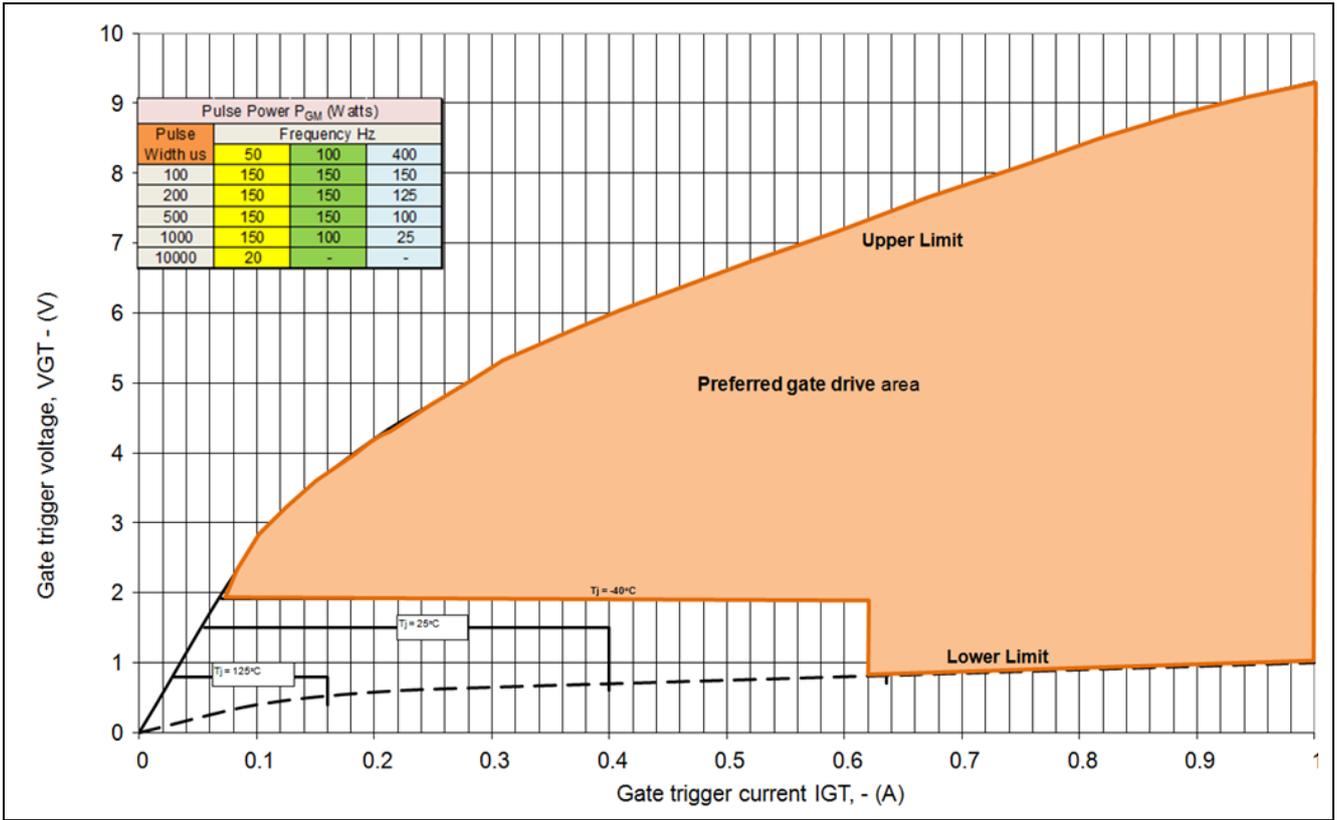


Fig.14 Gate characteristics

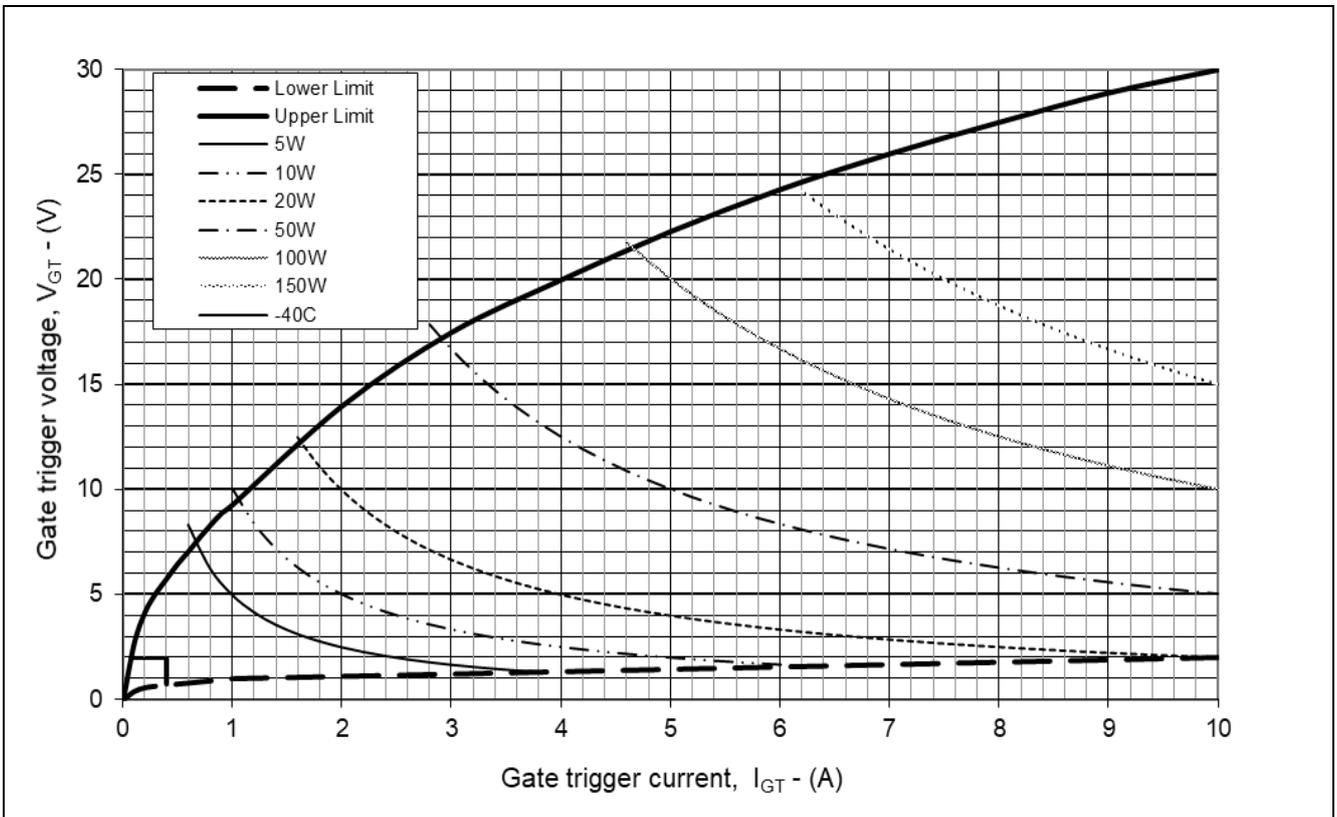


Fig. 15 Gate characteristics

PACKAGE DETAILS

For further package information, please contact Customer services.

All dimensions in mm, unless stated otherwise.

DO NOT SCALE

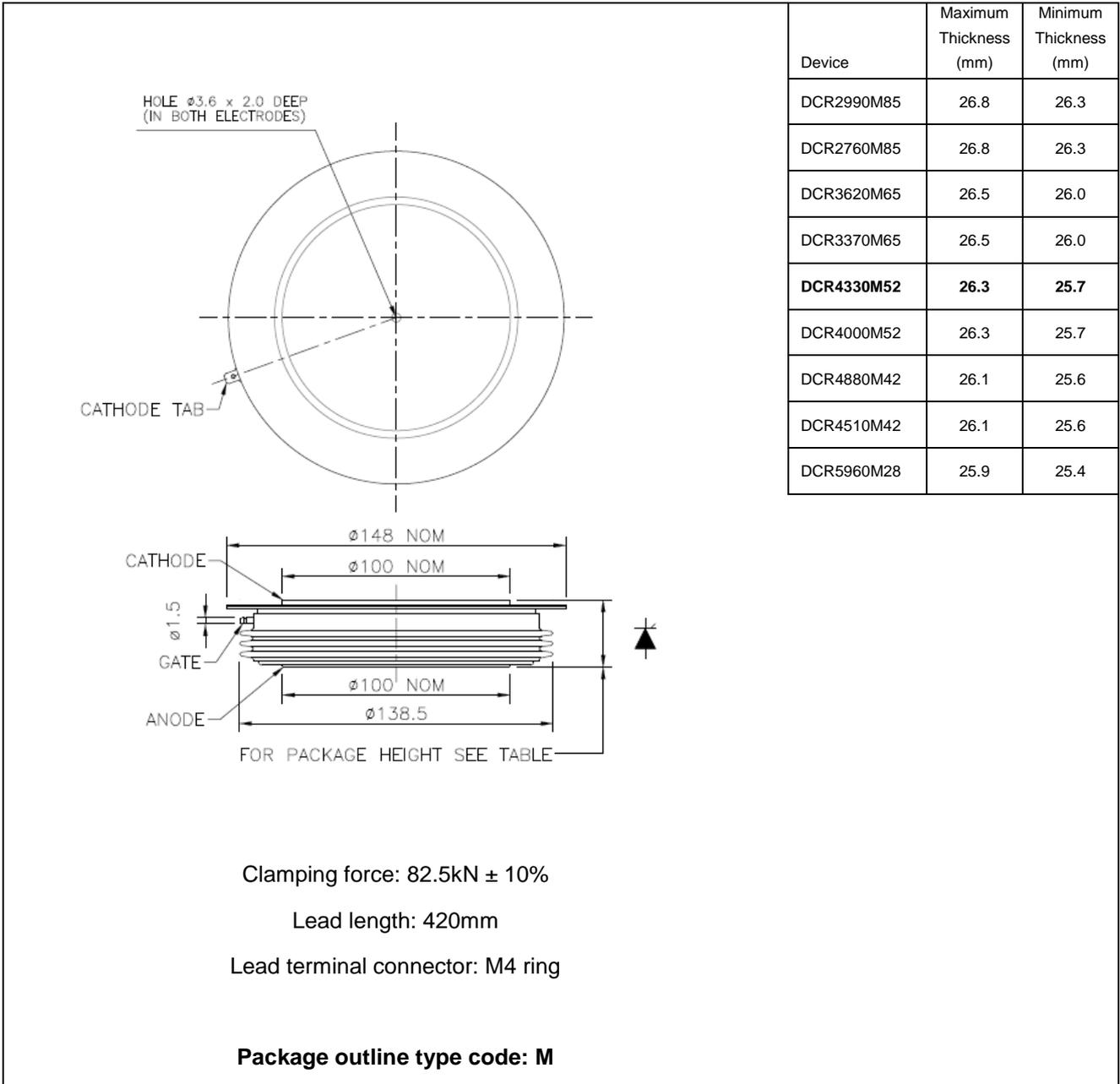


Fig. 16 Package outline

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HEADQUARTERS OPERATIONS

DYNEX SEMICONDUCTOR LIMITED
 Doddington Road, Lincoln, Lincolnshire. LN6 3LF
 United Kingdom.
 Phone: +44 (0) 1522 500500
 Fax: +44 (0) 1522 500550
 Web: <http://www.dynexsemi.com>

CUSTOMER SERVICE

Phone: +44 (0) 1522 502753 / 502901
 e-mail: powersolutions@dynexsemi.com