onsemi

Common Anode Silicon Dual Switching Diodes

DAP222M3T5G

These Common Anode Silicon Epitaxial Planar Dual Diodes are designed for use in ultra high speed switching applications. The DAP222 device is housed in the SOT-723 package which is designed for low power surface mount applications, where board space is at a premium.

Features

- Fast t_{rr}
- Low C_D
- Available in 4 mm Tape and Reel
- This is a Pb-Free Device

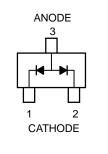
MAXIMUM RATINGS (T_A = 25 °C)

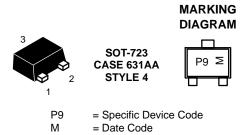
Rating	Symbol	Value	Unit
Reverse Voltage	V _R	80	V
Peak Reverse Voltage	V _{RM}	80	V
Forward Current	١ _F	100	mA

THERMAL CHARACTERISTICS

Rating	Symbol	Max	Unit
Power Dissipation	PD	260	mW
Junction Temperature	Т _Ј	150	°C
Storage Temperature	T _{stg}	-55 ~ +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected. 1. $t = 1.0 \ \mu$ S.





ORDERING INFORMATION

Device	Package	Shipping [†]
DAP222M3T5G	SOT-723 (Pb-Free)	8000/Tape & Reel

⁺For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

DAP222M3T5G

Characteristic	Symbol	Condition	Min	Max	Unit	
Reverse Voltage Leakage Current	I _R	V _R = 70 V	-	0.1	μΑ	
Forward Voltage	V _F	I _F = 100 mA	-	1.2	V	
Reverse Breakdown Voltage	V _R	I _R = 100 μA	80	-	V	
Diode Capacitance	CD	V _R = 6.0 V, f = 1.0 MHz	-	3.5	pF	
Reverse Recovery Time	t _{rr} (Note 2)	I_F = 5.0 mA, V_R = 6.0 V, R_L = 100 Ω , I_{rr} = 0.1 I_R	-	4.0	ns	

ELECTRICAL CHARACTERISTICS (T_A = 25 °C)

2. t_{rr} Test Circuit for DAP222 in Figure 4.

TYPICAL ELECTRICAL CHARACTERISTICS

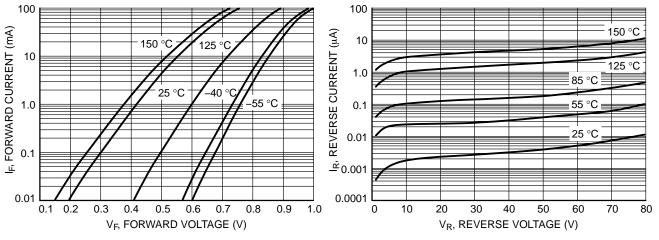


Figure 1. Forward Voltage

Figure 2. Reverse Current

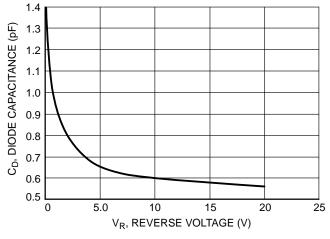
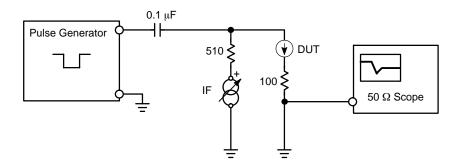
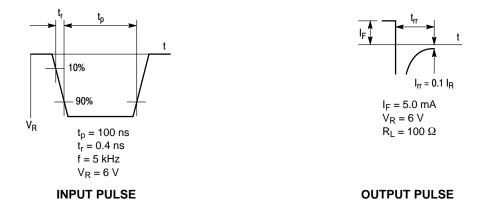


Figure 3. Diode Capacitance

DAP222M3T5G











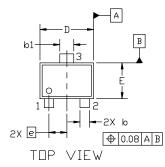


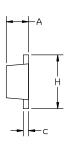
SOT-723 1.20x0.80x0.50, 0.40P CASE 631AA ISSUE E

DATE 24 JAN 2024

NDTES:

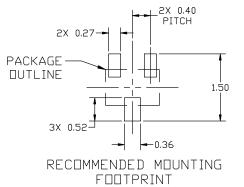
- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2018. CONTROLLING DIMENSION: MILLIMETERS. 1.
- 2.
- MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM З. LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
- DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, 4. PROTRUSIONS OR GATE BURRS.



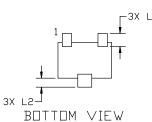


SIDE VIEW

		MILLIMETERS			
	DIM	MIN.	NDM.	MAX.	
1	А	0.45	0.50	0.55	
	b	0.15	0.21	0.27	
	b1	0.25	0.31	0.37	
	С	0.07	0.12	0.17	
	D	1.15	1.20	1.25	
	E	0.75	0.80	0.85	
	e	0.40 BSC			
	Н	1.15	1.20	1.25	
	L	0.29 REF			
	L2	0.15	0.20	0.25	



*For additional information on our Pb-Free strategy and soldering details, please download the DN Semiconductor Soldering and Mounting Techniques Reference Manual, SDLDERRM/D.



GENERIC **MARKING DIAGRAM***



XX = Specific Device Code Μ = Date Code

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

2. EMITTER 2.	II: STYLE 3: ANODE PIN 1. ANODE N/C 2. ANODE CATHODE 3. CATHODE	STYLE 4: PIN 1. CATHODE 2. CATHODE 3. ANODE	STYLE 5: PIN 1. GATE 2. SOURCE 3. DRAIN		
DOCUMENT NUMBER:	98AON12989D		Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.		
DESCRIPTION:	SOT-723 1.20x0.80x	0.50, 0.40P			PAGE 1 OF 1

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