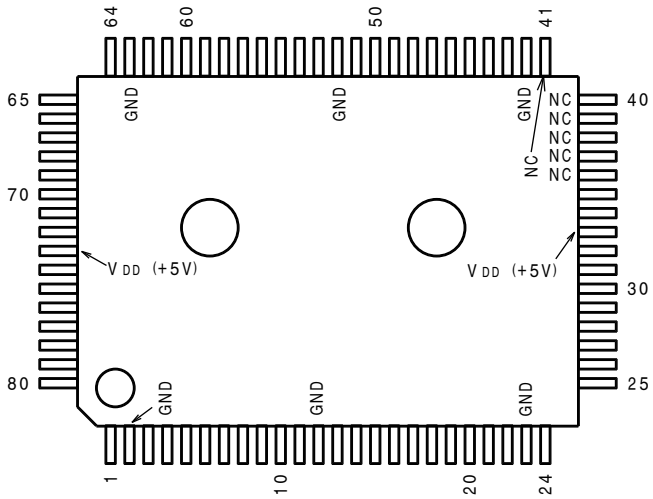
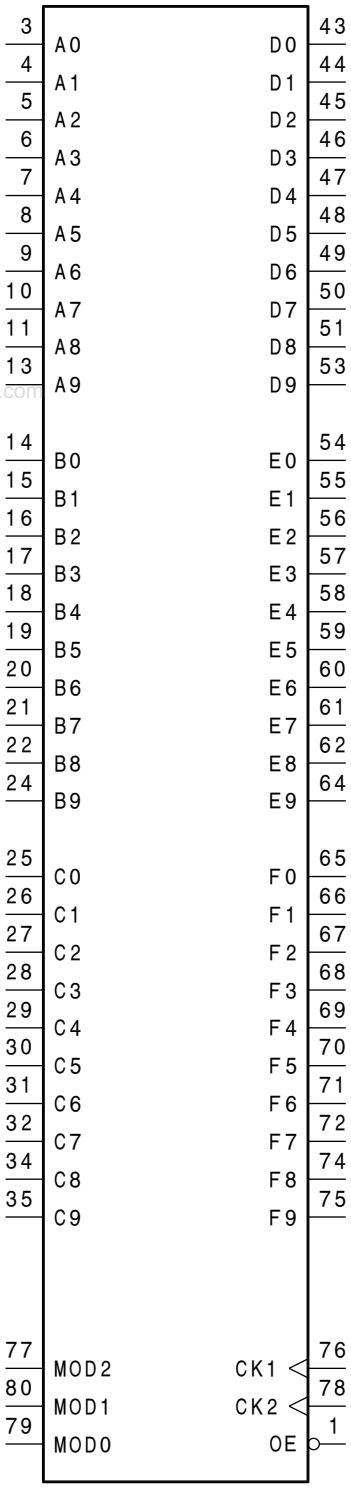
C-MOS 10BIT 2CHANNEL ADDER/MULTIPLEXER

-TOP VIEW-



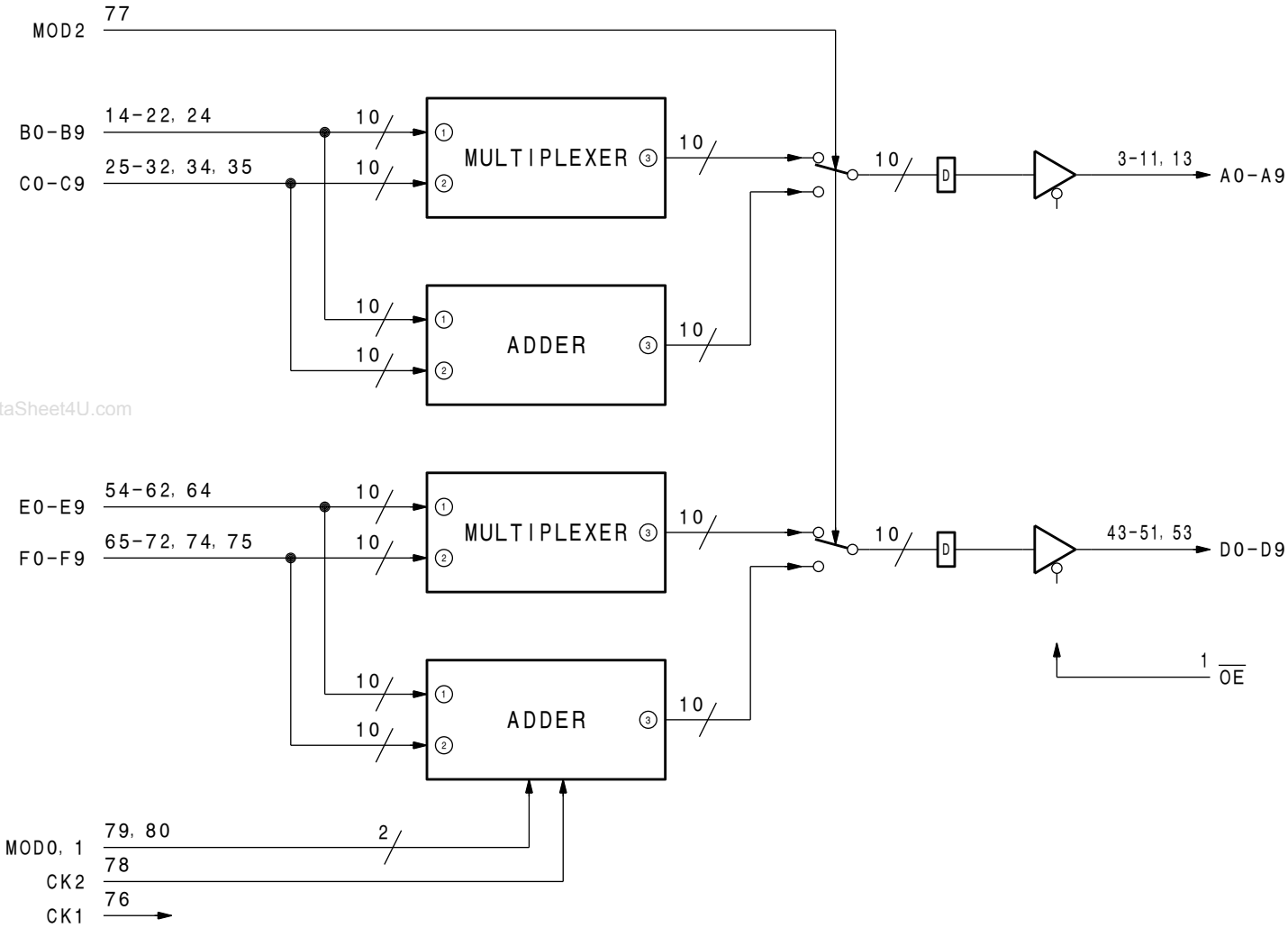
(VDD=+5V)

PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I	$\overline{OE}$	21	I	B7	41	–	NC	61	I	E7
2	–	GND	22	I	B8	42	–	GND	62	I	E8
3	O	A0	23	–	GND	43	O	D0	63	–	GND
4	O	A1	24	I	B9	44	O	D1	64	I	E9
5	O	A2	25	I	C0	45	O	D2	65	I	F0
6	O	A3	26	I	C1	46	O	D3	66	I	F1
7	O	A4	27	I	C2	47	O	D4	67	I	F2
8	O	A5	28	I	C3	48	O	D5	68	I	F3
9	O	A6	29	I	C4	49	O	D6	69	I	F4
10	O	A7	30	I	C5	50	O	D7	70	I	F5
11	O	A8	31	I	C6	51	O	D8	71	I	F6
12	–	GND	32	I	C7	52	–	GND	72	I	F7
13	O	A9	33	–	VDD	53	O	D9	73	–	VDD
14	I	B0	34	I	C8	54	I	E0	74	I	F8
15	I	B1	35	I	C9	55	I	E1	75	I	F9
16	I	B2	36	–	NC	56	I	E2	76	I	CK1
17	I	B3	37	–	NC	57	I	E3	77	I	MOD2
18	I	B4	38	–	NC	58	I	E4	78	I	CK2
19	I	B5	39	–	NC	59	I	E5	79	I	MOD0
20	I	B6	40	–	NC	60	I	E6	80	I	MOD1



TERMINALS

A0-A9	;CHA 10 BIT DIGITAL OUTPUT
B0-B9	;CHB 10 BIT DIGITAL INPUT
C0-C9	;CHC 10 BIT DIGITAL INPUT
CK1	;SYSTEM CLOCK INPUT
CK2	;SUB CLOCK INPUT
D0-D9	;CHD 10 BIT DIGITAL OUTPUT
E0-E9	;CHE 10 BIT DIGITAL INPUT
F0-F9	;CHF 10 BIT DIGITAL INPUT
MOD0-2	;MODE SELECT
$\overline{OE}$	;OUTPUT ENABLE INPUT (LOW:ENABLE)



MOD2	MOD1	MOD0	FUNCTION
1	1	1	ADDER ① BINARY + ② BINARY = ③ BINARY
1	1	0	ADDER ① BINARY + ② 2's COMPLEMENT = ③ BINARY
1	0	1	ADDER ① 2's COMPLEMENT + ② BINARY = ③ BINARY
1	0	0	ADDER ① 2's COMPLEMENT + ② 2's COMPLEMENT = ③ 2's COMPLEMENT
0	1	1	SELECTOR ③ = ①
0	1	0	SELECTOR ③ = ②
0	0	–	MULTIPLEXER *1

*1 MULTIPLEXER