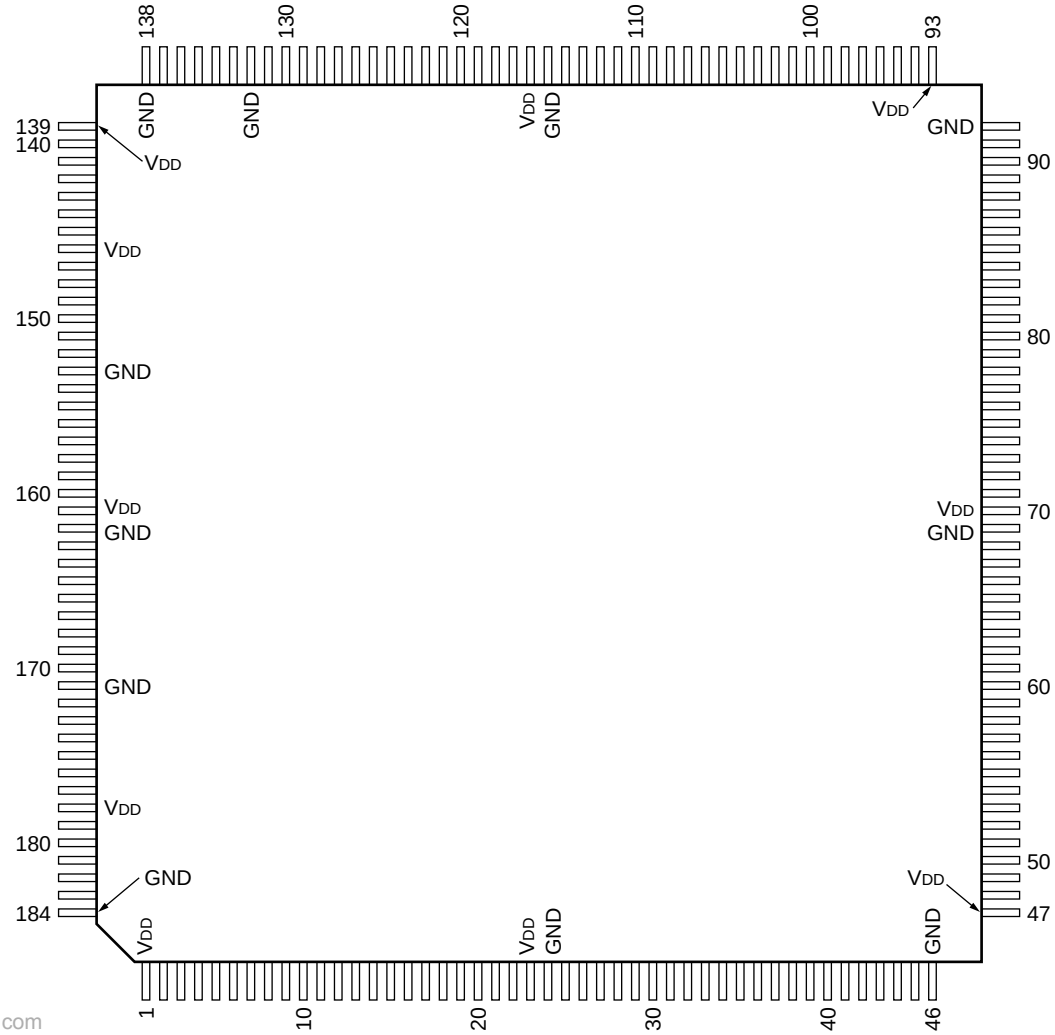
C-MOS MIXER

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	VDD	38	I	KYSR1.12	75	I	BGA.0	112	I	KF1.11	149	O	PGM.4
2	I	TRNS1.5	39	I	KYSR2.0	76	I	BGA.1	113	I	KF2.0	150	O	PGM.5
3	I	TRNS1.6	40	I	KYSR2.1	77	I	BGA.2	114	I	KF2.1	151	O	PGM.6
4	I	TRNS1.7	41	I	KYSR2.2	78	I	BGA.3	115	—	GND	152	O	PGM.7
5	I	TRNS1.8	42	I	KYSR2.3	79	I	BGA.4	116	—	VDD	153	—	GND
6	I	TRNS1.9	43	I	KYSR2.4	80	I	BGA.5	117	I	KF2.2	154	O	PGM.8
7	I	TRNS1.10	44	I	KYSR2.5	81	I	BGA.6	118	I	KF2.3	155	O	PGM.9
8	I	TRNS1.11	45	I	KYSR2.6	82	I	BGA.7	119	I	KF2.4	156	O	PGM.10
9	I	TRNS1.12	46	—	VDD	83	I	BGA.8	120	I	KF2.5	157	O	PGM.11
10	I	TRNS2.0	47	—	GND	84	I	BGA.9	121	I	KF2.6	158	O	NAMO
11	I	TRNS2.1	48	I	KYSR2.7	85	I	BGA.10	122	I	KF2.7	159	O	BLANKO
12	I	TRNS2.2	49	I	KYSR2.8	86	I	BGA.11	123	I	KF2.8	160		TNEN
13	I	TRNS2.3	50	I	KYSR2.9	87	I	BGB.0	124	I	KF2.9	161	—	VDD
14	I	TRNS2.4	51	I	KYSR2.10	88	I	BGB.1	125	I	KF2.10	162	—	GND
15	I	TRNS2.5	52	I	KYSR2.11	89	I	BGB.2	126	I	KF2.11	163	I	BLANKI
16	I	TRNS2.6	53	I	KYSR2.12	90	I	BGB.3	127	I	NAMI	164	I	HD
17	I	TRNS2.7	54	I	SMPL	91	I	BGB.4	128	O	PRK.0	165	O	PVW.0
18	I	TRNS2.8	55	I	CKD	92	—	GND	129	O	PRK.1	166	O	PVW.1
19	I	TRNS2.9	56	I	CS	93	—	VDD	130	O	PRK.2	167	O	PVW.2
20	I	TRNS2.10	57	I/O	SDIO	94	I	BGB.5	131	O	PRK.3	168	O	PVW.3
21	I	TRNS2.11	58	I	CKX	95	I	BGB.6	132	—	GND	169	O	PVW.4
22	I	TRNS2.12	59	I	SADD	96	I	BGB.7	133	O	PRK.4	170	O	PVW.5
23	—	VDD	60	I	RSTL	97	I	BGB.8	134	O	PRK.5	171	—	GND
24	—	GND	61	I	BDR.0	98	I	BGB.9	135	O	PRK.6	172	O	PVW.6
25	I	CK	62	I	BDR.1	99	I	BGB.10	136	O	PRK.7	173	O	PVW.7
26	I	KYSR1.0	63	I	BDR.2	100	I	BGB.11	137	O	TSTOUT	174	O	PVW.8
27	I	KYSR1.1	64	I	BDR.3	101	I	KF1.0	138	—	GND	175	O	PVW.9
28	I	KYSR1.2	65	I	BDR.4	102	I	KF1.1	139	—	VDD	176	O	PVW.10
29	I	KYSR1.3	66	I	BDR.5	103	I	KF1.2	140	O	PRK.8	177	O	PVW.11
30	I	KYSR1.4	67	I	BDR.6	104	I	KF1.3	141	O	PRK.9	178	—	VDD
31	I	KYSR1.5	68	I	BDR.7	105	I	KF1.4	142	O	PRK.10	179	I	TRNS1.0
32	I	KYSR1.6	69	—	GND	106	I	KF1.5	143	O	PRK.11	180	I	TRNS1.1
33	I	KYSR1.7	70	—	VDD	107	I	KF1.6	144	O	PGM.0	181	I	TRNS1.2
34	I	KYSR1.8	71	I	BDR.8	108	I	KF1.7	145	O	PGM.1	182	I	TRNS1.3
35	I	KYSR1.9	72	I	BDR.9	109	I	KF1.8	146	—	VDD	183	I	TRNS1.4
36	I	KYSR1.10	73	I	BDR.10	110	I	KF1.9	147	O	PGM.2	184	—	GND
37	I	KYSR1.11	74	I	BDR.11	111	I	KF1.10	148	O	PGM.3			

INPUT

BKGDA(0 - 11)	:	12-BIT UNIPOLAR BACKGROUND A VIDEO
BKGDB(0 - 11)	:	12-BIT UNIPOLAR BACKGROUND B VIDEO
BLANKI	:	1-BIT ACTIVE HIGH BLANKING CONTROL
$\overline{\text{BO}}$RD(0 - 11)	:	12-BIT UNIPOLAR WIPE BORDER FILL VIDEO
CK	:	1-BIT TTL-COMPATIBLE, C-MOS LEVEL INPUT CLOCK
KYFL1(0 - 11)	:	12-BIT UNIPOLAR KEY1 FILL VIDEO
KYFL2(0 - 11)	:	12-BIT UNIPOLAR KEY2 FILL VIDEO
KYSR1(0 - 12)	:	13-BIT UNIPOLAR KEY1 SOURCERANGE
KYSR2(0 - 12)	:	13-BIT UNIPOLAR KEY2 SOURCERANGE
NAMI	:	1-BIT NAM CONTROL
TRNS1(0 - 12)	:	13-BIT UNIPOLAR OUTER WIPE TRANSISTIONRANGE
TRNS2(0 - 12)	:	13-BIT UNIPOLAR INNER WIPE TRANSISTIONRANGE

OUTPUT

BLANKO	:	1-BIT DELAYED BLANKING CONTROL
NAMO	:	1-BIT COLOUR DIFFERENCE NAM CONTROL
PGM(0 - 11)	:	12-BIT UNIPOLAR PROGRAMME VIDEO
PR$\overline{\text{O}}$C(0 - 11)	:	12-BIT UNIPOLAR PROCESSED KEY
PVW(0 - 11)	:	12-BIT UNIPOLAR PREVIEW VIDEO

OTHER

CKD	:	1-BIT SIF CLOCK
CKX	:	1-BIT ACTIVE HIGH SIF REGISTER UPDATE PULSE
$\overline{\text{CS}}$	:	1-BIT ACTIVE LOW SIF CHIP SELECT INPUT
SADD	:	1-BIT SIF ADDRESS INPUT
SDIO	:	1-BIT BI-DIRECTIONAL DATA INPUT/OUTPUT
SMPL	:	1-BIT ACTIVE LOW OUTPUT CAPTURE TO SERIAL INTERFACE