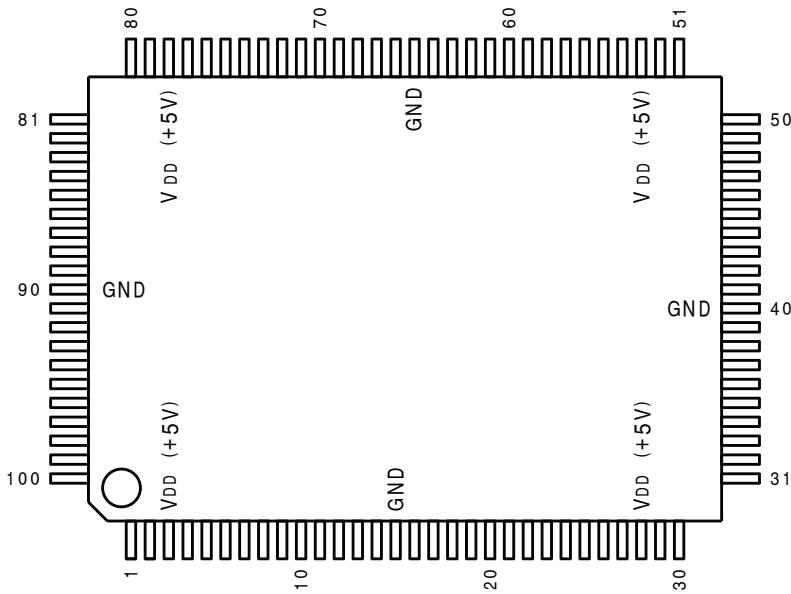

TRIPLE DIGITAL MIX EFFECT
-TOP VIEW-(V_{DD}=+5V)

PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	I	CP0	26	I	C16/A16	51	I	C21/A21	76	I	B24
2	I	B00	27	I	C17/A17	52	I	C22/A22	77	I	B25
3	-	V _{DD}	28	-	V _{DD}	53	-	V _{DD}	78	-	V _{DD}
4	I	B01	29	I	CS	54	I	C23/A23	79	I	B26
5	I	B02	30	I	M1	55	I	C24/A24	80	I	B27
6	I	B03	31	I	CE	56	I	C25/A25	81	I	A00
7	I	B04	32	I	CP1	57	I	C26/A26	82	I	A01
8	I	B05	33	I	B10	58	I	C27/A27	83	I	A02
9	I	B06	34	I	B11	59	I	DL	84	I	A03
10	I	B07	35	I	B12	60	I	M2	85	I	A04
11	O	R00	36	O	R10	61	O	R20	86	I	A05
12	O	R01	37	O	R11	62	O	R21	87	I	A06
13	O	R02	38	O	R12	63	O	R22	88	I	A07
14	O	R03	39	O	R13	64	O	R23	89	I	AE
15	-	GND	40	-	GND	65	-	GND	90	-	GND
16	O	R04	41	O	R14	66	O	R24	91	I	CLK
17	O	R05	42	O	R15	67	O	R25	92	I	C00
18	O	R06	43	O	R16	68	O	R26	93	I	C01
19	O	R07	44	O	R17	69	O	R27	94	I	C02
20	I	C10/A10	45	I	B13	70	I	BG2	95	I	C03
21	I	C11/A11	46	I	B14	71	I	CP2	96	I	C04
22	I	C12/A12	47	I	B15	72	I	B20	97	I	C05
23	I	C13/A13	48	I	B16	73	I	B21	98	I	C06
24	I	C14/A14	49	I	B17	74	I	B22	99	I	C07
25	I	C15/A15	50	I	C20/A20	75	I	B23	100	I	BG01

58	C27/A27	99	C07	98	C06	97	C05	96	C04	95	C03	94	C02	93	C01	92	C00	R27	69
57	C26/A26																	R26	68
56	C25/A25																	R25	67
55	C24/A24																	R24	66
54	C23/A23																	R23	64
52	C22/A22																	R22	63
51	C21/A21																	R21	62
50	C20/A20																	R20	61
27	C17/A17																	R17	44
26	C16/A16																	R16	43
25	C15/A15																	R15	42
24	C14/A14																	R14	41
23	C13/A13																	R13	39
22	C12/A12																	R12	38
21	C11/A11																	R11	37
20	C10/A10																	R10	36
88	A07																	R07	19
87	A06																	R06	18
86	A05																	R05	17
85	A04																	R04	16
84	A03																	R03	14
83	A02																	R02	13
82	A01																	R01	12
81	A00																	R00	11
80	B27																		
79	B26																		
77	B25																		
76	B24																		
75	B23																		
74	B22																		
73	B21																		
72	B20																		
49	B17																		
48	B16																		
47	B15																		
46	B14																		
45	B13																		
35	B12																		
34	B11																		
33	B10																		
10	B07																		
9	B06																		
8	B05																		
7	B04																		
6	B03																		
5	B02																		
4	B01																		
2	B00																		
89	AE																		
31	CE																		
30	M1																		
60	M2																		
59	DL																		
29	CS																		
100	BG01																		
70	BG2																		
71	CP2																		
32	CP1																		
1	CP0																		
91																			

INPUT

- A00-A07 : A0 DATA (8 BIT)
- A10-A17 : A1 DATA (8 BIT)
- A20-A27 : A2 DATA (8 BIT)
- AE : ENABLE FOR A DATA
- B00-B07 : B0 DATA (8 BIT)
- B10-B17 : B1 DATA (8 BIT)
- B20-B27 : B2 DATA (8 BIT)
- BG01 : INPUT HOLD FOR B0 and B1
- BG2 : INPUT HOLD SIGNAL FOR B2
- C00-C07 : C0 DATA (8 BIT)
- CE : ENABLE FOR C DATA
- CLK : CLOCK
- CS : CHIP SELECT
- CP0 : CONTROL POLE FOR C DATA
(at *MIXER0)
- CP1 : CONTROL POLE FOR C DATA
(at *MIXER1)
- CP2 : CONTROL POLE FOR C DATA
(at *MIXER2)
- DL : DELAY CONTROL
- M1 : MIXER MODE FOR *MIXER1
- M2 : MIXER MODE FOR *MIXER2

OUTPUT

- R00-R07 : R0 DATA (8 BIT)
- R10-R17 : R1 DATA (8 BIT)

Notice *

- MIXER0: $R0 = C0 * A0 + (1 - C0) * B0$
- MIXER1: $R1 = C1 * A1 + (1 - C1) * B1$
- MIXER2: $R0 = C2 * A2 + (1 - C2) * B2$

