SONY

CXD3536R

Digital Signal Driver/Timing Generator

Description

The CXD3536R incorporates digital signal processor type RGB driver, color shading correction and timing generator functions onto a single IC. Operation is possible with a system clock up to 100 [MHz] (max.). This IC can process video signals in bands up to 100MHz standard, and can output the timing signals for driving various Sony LCD panels.

Features

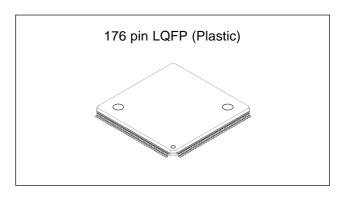
- Various picture quality adjustment functions such as user adjustment, white balance adjustment and gamma correction
- OSD MIX, black frame processing, mute and limiter functions
- LCD panel color shading correction function
- LCD panel vertical stripe correction function
- Drives various Sony data projector LCD panels
- Drives Sony dot/line inversion drive panel
- Controls the CXA3562AR and CXA7005R sampleand-hold drivers
- Line inversion and field inversion signal generation
- Supports AC drive of LCD panels during no signal
- On-chip serial interface
- The data of gamma correction and color shading correction can be downloaded automatically from the external EEPROM.

Applications

LCD projectors and other video equipment

Structure

Silicon gate CMOS IC



Absolute Maximum Ratings (Vss = 0V)

 Supply voltage 	V_{DD}	Vss - 0.5 to +2.5	V		
	VDE	Vss - 0.5 to +4.5	V		
	V_{DDA}	Vss - 0.5 to +4.5	V		
 Input voltage 	Vı	Vss - 0.3 to $Vde + 0.3$	V		
 Output voltage 	Vo	Vss - 0.3 to $Vde + 0.3$	V		
Storage temperature					
	Tstg	-55 to +125	°С		
Junction temperature					
	Tj	125	°C		

Recommended Operating Conditions

 Supply voltage 	VDD	1.65 to 1.95	V
	VDE	3.0 to 3.6	V
	Vdda	3.0 to 3.6	V
Operating temperature	erature		
	Topr	-20 to +75	°C

Note) Company names and product names in this data sheet are trademarks or registered trademarks of the respective company.

Sony reserves the right to change products and specifications without prior notice. This information does not convey any license by any implication or otherwise under any patents or other right. Application circuits shown, if any, are typical examples illustrating the operation of the devices. Sony cannot assume responsibility for any problems arising out of the use of these circuits.

Block Diagram

