

# 2 Semiconductor Cor

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UNITS

W

А

kV

kV °C

°C

# **DESCRIPTION:**

The CENTRAL SEMICONDUCTOR CTLTVS5V0B is a low capacitance, ultra low leakage, fast response, bi-directional TVS in the space saving TLM2D3D6 surface mount package. This device is designed to protect sensitive equipment connected to high speed data lines against ESD damage.

Ultra space saving TLM2D3D6 package

30

40

16

16

-55 to +125

-55 to +150

## **MARKING CODE: 5**

FEATURES:

SYMBOL

Low capacitance

• Ultra low leakage current

## **APPLICATIONS:**

- High speed data line protection
- User interface protection
- Charging/power port protection
- · Serial/parallel port protection

## MAXIMUM RATINGS: (T<sub>A</sub>=25°C)

Peak Power Dissipation (8x20µs)	P <sub>PK</sub>		
Electrical Fast Transient (IEC 61000-4-4) (5x50ns)	EFT		
ESD Voltage (IEC 61000-4-2, Air)			
ESD Voltage (IEC 61000-4-2, Contact)			
Operating Junction Temperature	Тј		
Storage Temperature	T <sub>stg</sub>		

#### ELECTRICAL CHARACTERISTICS: (T<sub>A</sub>=25°C)

Maximum Reverse Stand-off Voltage	Breakdown		Test Current	Current	Clam Volt (8x2	•	TLP Cla Volt (Not	bical amping tage te 1)	Typical Dynamic Resistance (Note 1)	Maximum Junction Capacitance @ 0V Bias
V <sub>RWM</sub>	VBR	<u></u> T	Γ	I <sub>R</sub> @V <sub>RWM</sub>	V <sub>C</sub> @	0 I <sub>PP</sub>	V <sub>CL</sub> @ I <sub>PP</sub>		R <sub>DYN</sub>	CJ
v	MIN V	MAX V	mA	μΑ	v	А	v	Α	Ω	pF
5.0	5.5	10	1.0	0.1	12	1.0	11	8.0	0.5	3.5
5.0	0.5	10	1.0	0.1	15	2.0	15	16	0.5	5.5

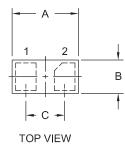
Notes: (1) Transmission Line Pulse (TLP) conditions:  $Z_0=50\Omega$ , tp=100ns

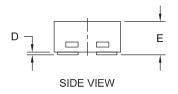
R5 (22-June 2015)

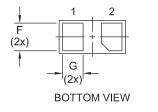




## TLM2D3D6 CASE - MECHANICAL OUTLINE







R2

DIMENSIONS				
	INCHES		MILLIM	ETERS
SYMBOL	MIN	MAX	MIN	MAX
А	0.022	0.026	0.55	0.65
В	0.010	0.014	0.25	0.35
С	0.014		0.35	
D	0.000	0.002	0.00	0.05
E	0.011	0.013	0.28	0.32
F	0.008	0.012	0.20	0.30
G	0.005	0.009	0.13	0.24
TLM2D3D6 (REV: R2)				

PIN CONFIGURATION



LEAD CODE: 1) Anode 1 2) Anode 2

MARKING CODE: 5

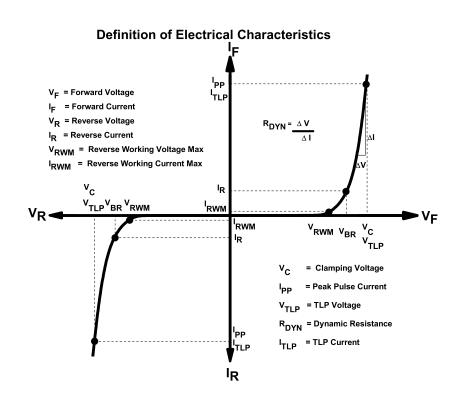
R5 (22-June 2015)



CTLTVS5V0B

-0/4

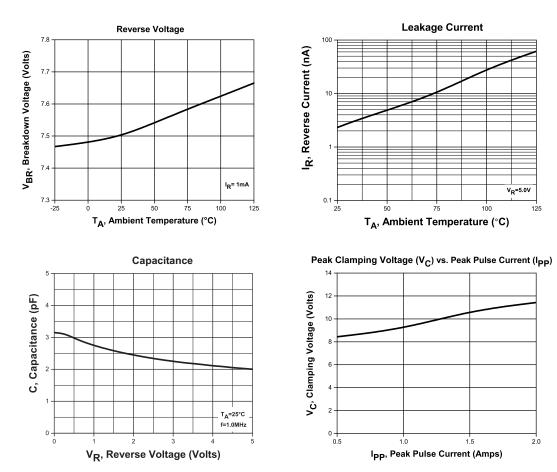
SURFACE MOUNT SILICON BI-DIRECTIONAL LOW CAPACITANCE TRANSIENT VOLTAGE SUPPRESSOR



R5 (22-June 2015)







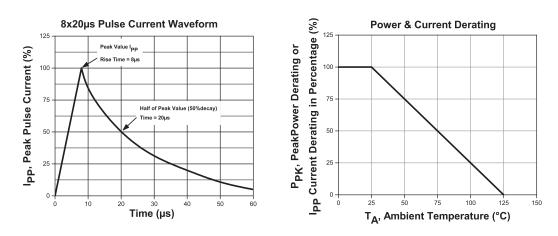
# TYPICAL ELECTRICAL CHARACTERISTICS

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R5 (22-June 2015)







## TYPICAL ELECTRICAL CHARACTERISTICS

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CTI	TVS5V0B	
CIL	.1 0 3 3 0 0 0	



## SERVICES

- · Bonded Inventory
- Custom Electrical Screening
- Custom Electrical Characteristic Curves
- SPICE Models
- Custom Packaging
- Package Base Options
- Custom Device Development/Multi Discrete Modules (MDM™)
- Bare Die Available for Hybrid Applications

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