



CTL0502NS

N-Channel Enhancement MOSFET

Features

- Drain-Source Breakdown Voltage V_{DS} 20 V
- Drain-Source On-Resistance
 $R_{DS(ON)}$ 21m Ω , at V_{GS} = 4.5V, I_D = 5.0A
 $R_{DS(ON)}$ 24m Ω , at V_{GS} = 2.5V, I_D = 3.5A
 $R_{DS(ON)}$ 31m Ω , at V_{GS} = 1.8V, I_D = 2.8A
- Continuous Drain Current at T_C =25°C I_D = 5.0A
- Advanced high cell density Trench Technology
- RoHS Compliance & Halogen Free

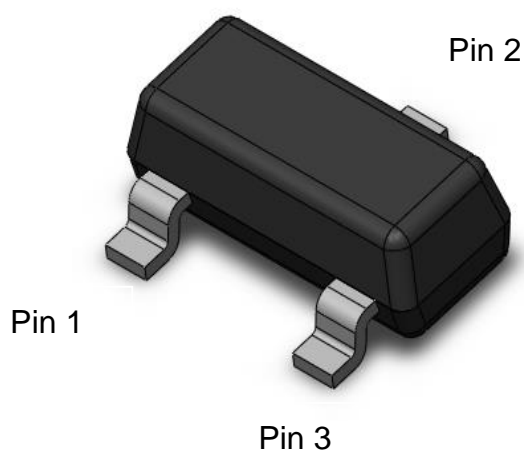
Description

The CTL0502NS uses high performance Trench Technology to provide excellent $R_{DS(ON)}$ and low gate charge which is suitable for most of the synchronous buck converter applications.

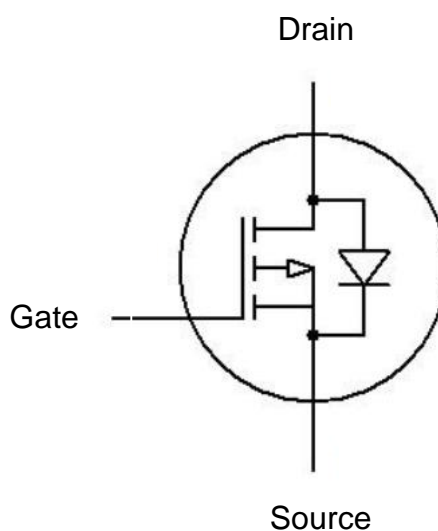
Applications

- Power Management
- Lithium Ion Battery

Package Outline



Schematic



Gate:	Pin 1
Drain:	Pin 2
Source:	Pin 3

**CTL0502NS****N-Channel Enhancement MOSFET****Absolute Maximum Rating at 25°C**

Symbol	Parameters	Test Conditions	Min	Notes
V _{DS}	Drain-Source Voltage	20	V	
V _{GS}	Gate-Source Voltage	±12	V	
I _D	Continuous Drain Current	5.0	A	1
I _{DM}	Pulsed Drain Current	25	A	1
P _D	Total Power Dissipation	1.4	W	2
T _{STG}	Storage Temperature Range	-55 to 150	°C	
T _J	Operating Junction Temperature Range	-55 to 150	°C	

Thermal Characteristics

Symbol	Parameters	Test Conditions	Min	Typ	Max	Units	Notes
R _{ΘJA4}	Thermal Resistance Junction-Ambient (t=10s)		--	175	--	°C /W	1,4

**CTL0502NS****N-Channel Enhancement MOSFET****Electrical Characteristics** $T_A = 25^\circ\text{C}$ (unless otherwise specified)**Static Characteristics**

Symbol	Parameters	Test Conditions	Min	Typ	Max	Units	Notes
B _{VDSS}	Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D = 250μA	20	-	-	V	
I _{DSS}	Drain-Source Leakage Current	V _{DS} = 20V, V _{GS} = 0V	-	-	1	μA	
I _{GSS}	Gate-Source Leakage Current	V _{GS} = ±12V, V _{DS} = 0V	-	-	±100	nA	

On Characteristics

Symbol	Parameters	Test Conditions	Min	Typ	Max	Units	Notes
R _{DS(ON)}	Drain-Source On-Resistance	V _{GS} = 4.5V, I _D = 3.0A	-	21	31	mΩ	3
		V _{GS} = 2.5V, I _D = 2.6A	-	24	37	mΩ	
		V _{GS} = 1.8V, I _D = 1.0A	-	31	47	mΩ	
V _{GS(th)}	Gate-Source Threshold Voltage	V _{GS} = V _{DS} , I _D = 250μA	0.7	---	1.4	V	3

Dynamic Characteristics

Symbol	Parameters	Test Conditions	Min	Typ	Max	Units	Notes
C _{ISS}	Input Capacitance	V _{GS} = 0V, V _{DS} = 10V f = 1MHz	-	668	-	pF	
C _{OSS}	Output Capacitance		-	118	-		
C _{RSS}	Reverse Transfer Capacitance		-	86	-		

Switching Characteristics

Symbol	Parameters	Test Conditions	Min	Typ	Max	Units	Notes
T _{D(ON)}	Turn-On Delay Time	V _{DS} = 5V , V _{GS} = 4.5V, R _G = 6Ω, I _D = 3.6A	-	10.5	21	ns	
T _R	Rise Time		-	5	10		
T _{D(OFF)}	Turn-Off Delay Time		-	29.5	59		
T _F	Fall Time		-	4.5	9		
Q _G	Total Gate Charge	V _{DS} = 10V , V _{GS} = 4.5V, I _D = 3.6A	-	7.0	-	nC	
Q _{GS}	Gate-Source Charge		-	1.0	-		
Q _{GD}	Gate-Drain Charge		-	1.5	-		



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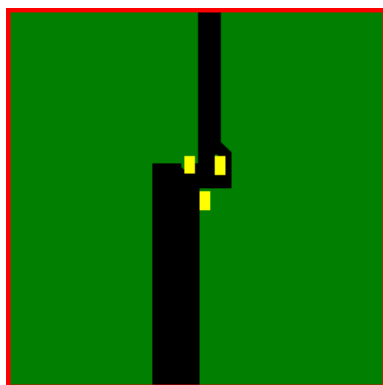
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Drain-Source Diode Characteristics

Symbol	Parameters	Test Conditions	Min	Typ	Max	Units	Notes
V _{SD}	Body Diode Forward Voltage	V _{GS} = 0V, I _D = 5.0	-	-	1.2	V	
I _{SD}	Body Diode Continuous Current		-	-	5.0	A	1

Note:

1. The power dissipation is limited by 150°C junction temperature.
2. Device mounted on a glass-epoxy board



FR-4

25.4 × 25.4 mm .

2 Oz Copper

Test Board

3. The data tested by pulsed , pulse width ≤ 300μs , duty cycle ≤ 2%
4. Thermal Resistance follow JESD51-3.



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Typical Characteristic Curves

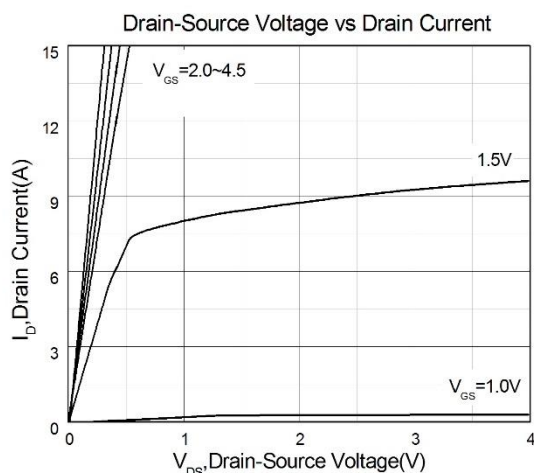


Figure 1

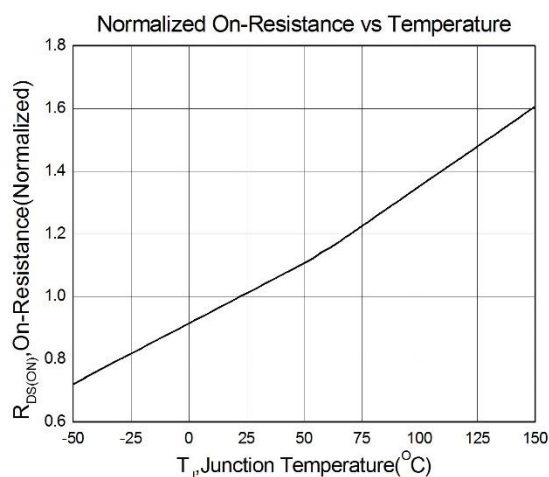


Figure 2

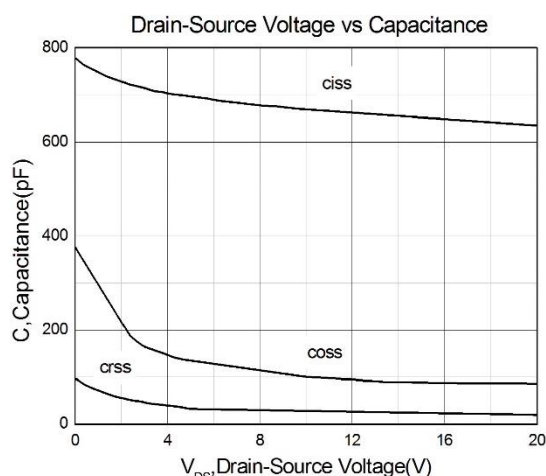


Figure 3

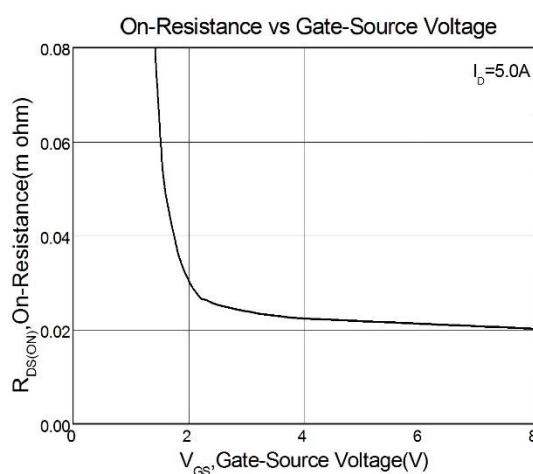


Figure 4

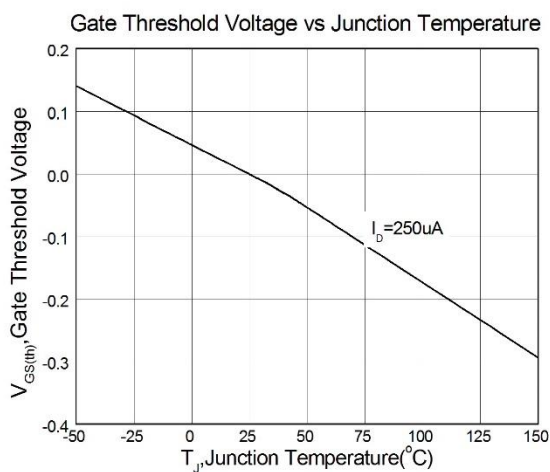


Figure 5

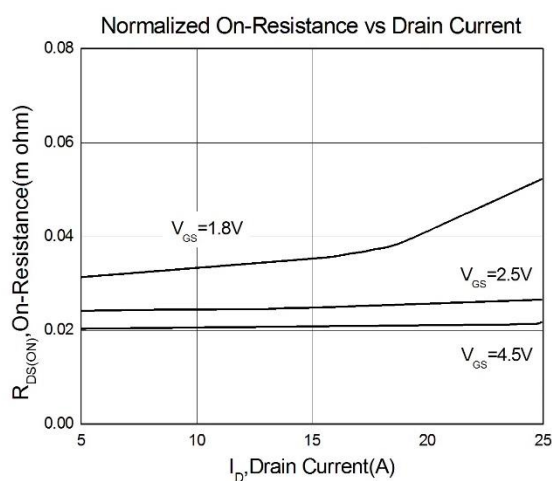
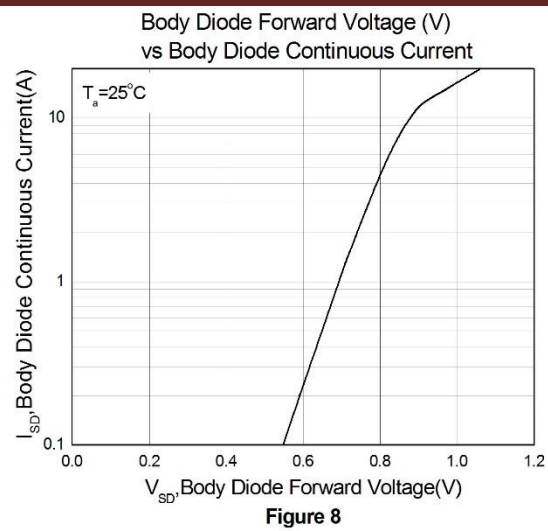
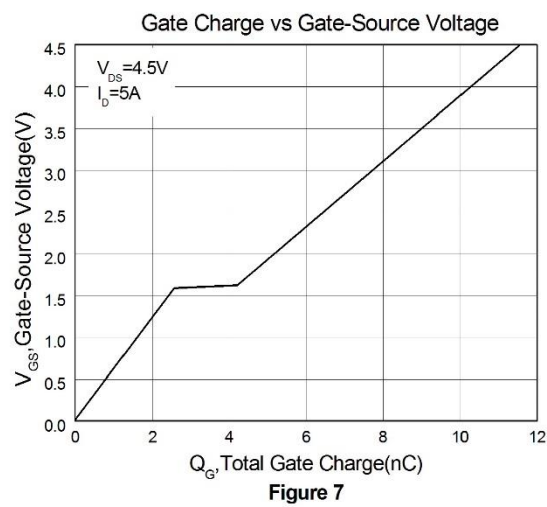


Figure 2



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Test Circuits & Waveforms

Figure 9: Gate Charge Test Circuit

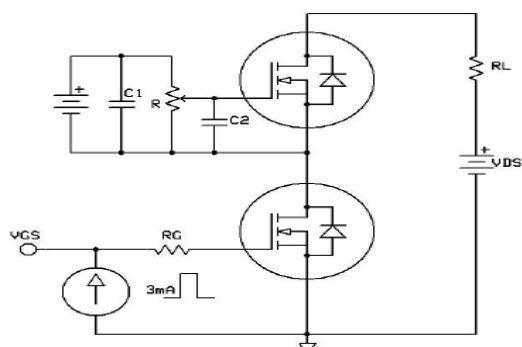


Figure 10: Gate Charge Waveform

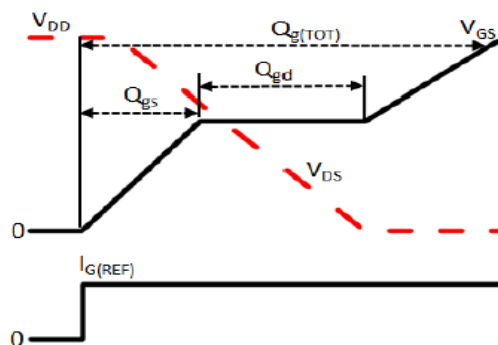


Figure 11: Switching Time Test Circuit

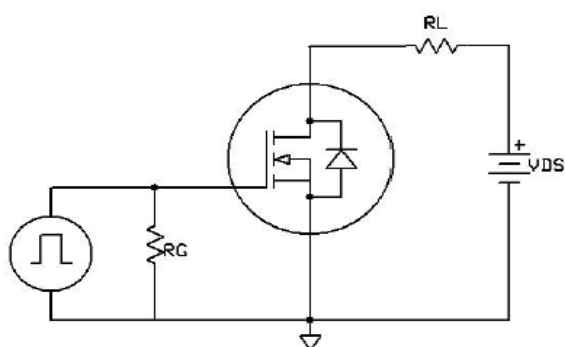
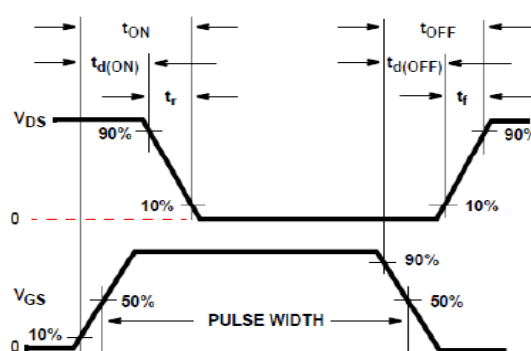


Figure 12: Switching Time Waveform

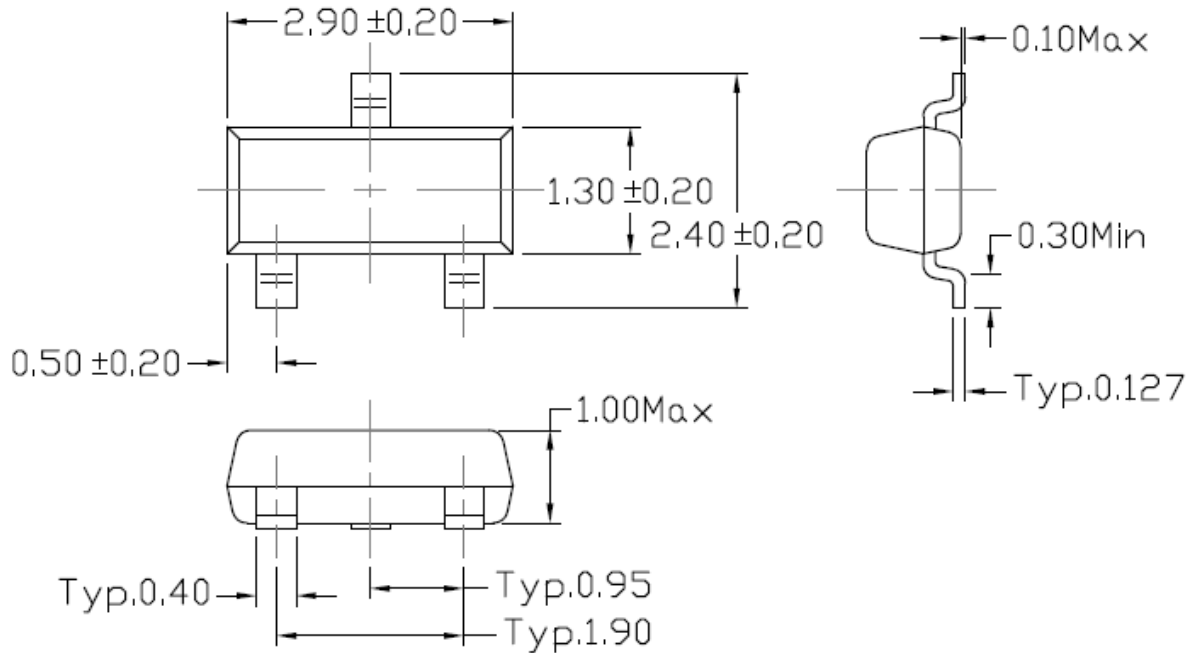




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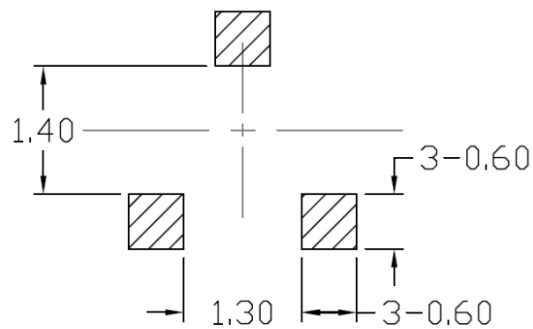
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Package Dimension *Dimensions in mm unless otherwise stated*



Note: Dimensions in mm

Recommended pad layout for surface mount leadform



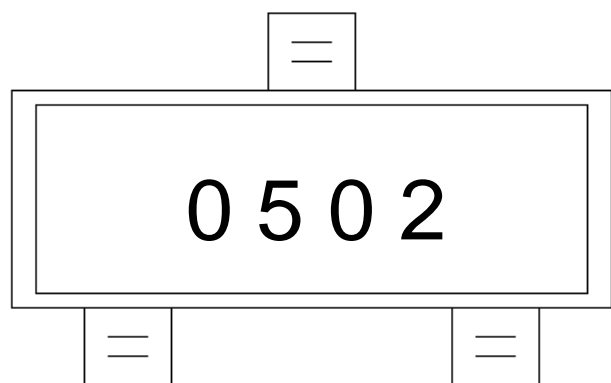
Note: Dimensions in mm



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Marking Information



0502: Device Number

Ordering Information

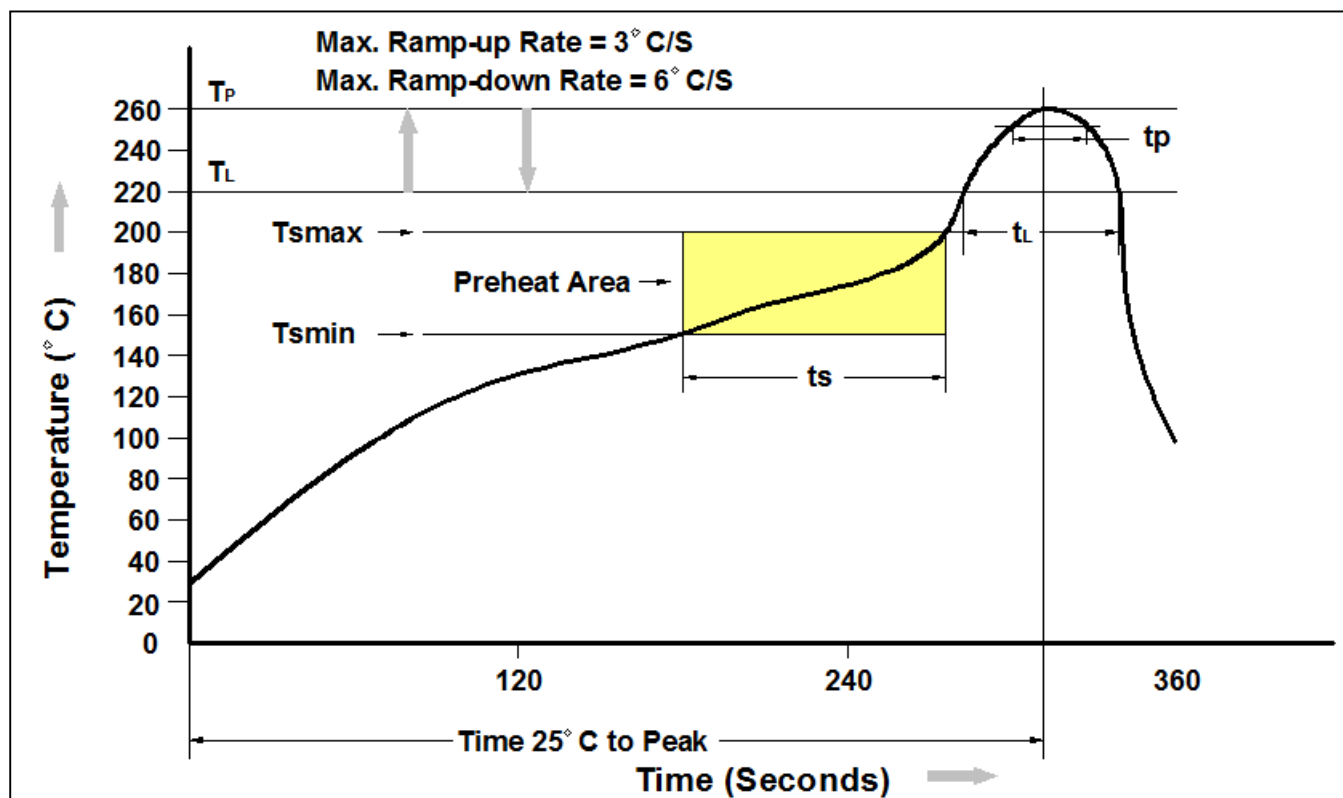
Part Number	Description	Quantity
CTL0502NS	SOT-23 Reel	3000 pcs



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Reflow Profile



Profile Feature	Pb-Free Assembly Profile
Temperature Min. (Tsmin)	150°C
Temperature Max. (Tsmax)	200°C
Time (ts) from (Tsmin to Tsmax)	60-120 seconds
Ramp-up Rate (tL to tP)	3°C/second max.
Liquidous Temperature (TL)	217°C
Time (tL) Maintained Above (TL)	60 – 150 seconds
Peak Body Package Temperature	260°C +0°C / -5°C
Time (tP) within 5°C of 260°C	30 seconds
Ramp-down Rate (TP to TL)	6°C/second max
Time 25°C to Peak Temperature	8 minutes max.



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