



N-Channel Enhancement MOSFET

Features

- Drain-Source Breakdown Voltage V_{DSS} 20 V
- Drain-Source On-Resistance
- $R_{DS(ON)}$ 55mΩ, at V_{GS} = 4.5V, I_D =3.2A
- $R_{DS(ON)}$ 65mΩ, at V_{GS} = 2.5V, I_D = 2.5A
- $R_{DS(ON)}$ 80mΩ, at V_{GS} = 1.8V, I_D = 2.0A
- Continuous Drain Current at $T_A=25^\circ\text{C}$ I_D = 3.2A
- Advanced high cell density Trench Technology
- RoHS Compliance & Halogen Free

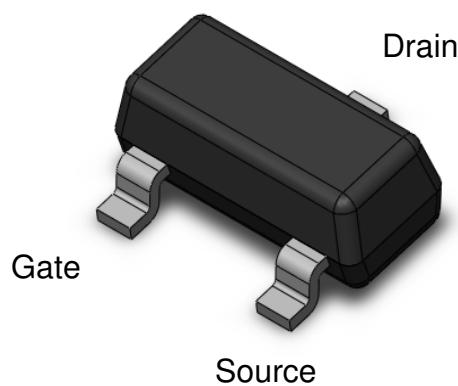
Description

The CT3A01-R3 uses high performance Trench Technology to provide excellent $R_{DS(ON)}$ and low gate charge which is suitable for most of the synchronous buck converter applications.

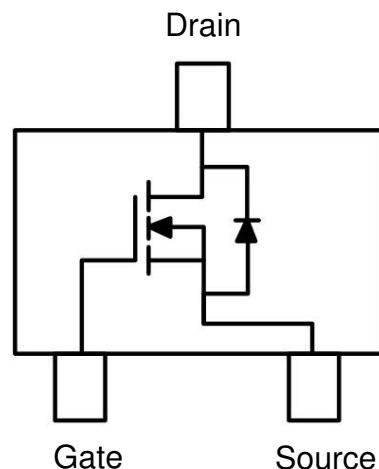
Applications

- Power Management
- Portable Equipment
- Load switch

Package Outline



Schematic



**Absolute Maximum Rating at 25°C**

| Symbol | Parameters | Test Conditions | Min | Notes |
|------------------|--|-----------------|-----|-------|
| V _{DS} | Drain-Source Voltage | 20 | V | |
| V _{GS} | Gate-Source Voltage | ±8 | V | |
| I _D | Continuous Drain Current @T _A =25°C | 3.2 | A | 1 |
| I _{DM} | Pulsed Drain Current | 15 | A | 1 |
| P _D | Total Power Dissipation @T _A =25°C | 1.4 | W | 2 |
| T _{STG} | Storage Temperature Range | -55 to 150 | °C | |
| T _J | Operating Junction Temperature Range | -55 to 150 | °C | |

Thermal Characteristics

| Symbol | Parameters | Test Conditions | Min | Typ | Max | Units | Notes |
|------------------|--|-----------------|-----|-----|-----|-------|-------|
| R _{θJA} | Thermal Resistance Junction-Ambient | | -- | 125 | -- | °C /W | 1,4 |



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Electrical Characteristics $T_A = 25^\circ\text{C}$ (unless otherwise specified)

Static Characteristics

| Symbol | Parameters | Test Conditions | Min | Typ | Max | Units | Notes |
|--------------|--------------------------------|---|-----|-----|-----------|---------------|-------|
| B_{VDSS} | Drain-Source Breakdown Voltage | $V_{GS} = 0\text{V}$, $I_D = 250\mu\text{A}$ | 20 | - | - | V | |
| $I_{DS(ON)}$ | Drain-Source Leakage Current | $V_{DS} = 20\text{V}$, $V_{GS} = 0\text{V}$ | - | - | 1 | μA | |
| I_{GSS} | Gate-Source Leakage Current | $V_{GS} = \pm 8\text{V}$, $V_{DS} = 0\text{V}$ | - | - | ± 100 | nA | |

On Characteristics

| Symbol | Parameters | Test Conditions | Min | Typ | Max | Units | Notes |
|--------------|-------------------------------|--|-----|------|-----|------------------|-------|
| $R_{DS(ON)}$ | Drain-Source On-Resistance | $V_{GS} = 4.5\text{V}$, $I_D = 3.2\text{A}$ | - | 55 | 85 | $\text{m}\Omega$ | 3 |
| | | $V_{GS} = 2.5\text{V}$, $I_D = 2.5\text{A}$ | - | 65 | 115 | $\text{m}\Omega$ | |
| | | $V_{GS} = 1.8\text{V}$, $I_D = 2.0\text{A}$ | - | 80 | 130 | $\text{m}\Omega$ | |
| $V_{GS(th)}$ | Gate-Source Threshold Voltage | $V_{GS} = V_{DS}$, $I_D = 250\mu\text{A}$ | | 0.75 | 1.2 | V | 3 |

Dynamic Characteristics

| Symbol | Parameters | Test Conditions | Min | Typ | Max | Units | Notes |
|-----------|------------------------------|--|-----|-----|-----|-------|-------|
| C_{iss} | Input Capacitance | $V_{GS} = 0\text{V}$, $V_{DS} = 15\text{V}$ $f = 1\text{MHz}$ | - | 455 | - | pF | |
| C_{oss} | Output Capacitance | | - | 90 | - | | |
| C_{rss} | Reverse Transfer Capacitance | | - | 60 | - | | |

Switching Characteristics

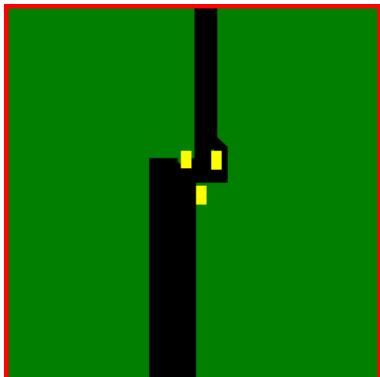
| Symbol | Parameters | Test Conditions | Min | Typ | Max | Units | Notes |
|--------------|----------------------------|---|-----|-----|-----|-------|-------|
| $T_{D(ON)}$ | Turn-On Delay Time | $V_{DS} = 15\text{V}$, $V_{GS} = 4.5\text{V}$, $R_G = 6\Omega$, $I_D = 1.0\text{A}$ | - | 9 | - | ns | |
| T_R | Rise Time | | - | 4 | - | | |
| $T_{D(OFF)}$ | Turn-Off Delay Time | | - | 20 | - | | |
| T_F | Fall Time | | - | 4.5 | - | | |
| Q_G | Total Gate Charge | $V_{DS} = 10\text{V}$, $V_{GS} = 4.5\text{V}$, $I_D = 3.2\text{A}$ | - | 6.4 | - | nC | |
| Q_{GS} | Gate-Source Charge | | - | 3 | - | | |
| Q_{GD} | Gate-Drain (Miller) Charge | | - | 3.0 | - | | |

**Drain-Source Diode Characteristics**

| Symbol | Parameters | Test Conditions | Min | Typ | Max | Units | Notes |
|----------|-------------------------------|-----------------------------|-----|-----|-----|-------|-------|
| V_{SD} | Body Diode Forward Voltage | $V_{GS} = 0V$, $I_D = 3.2$ | - | - | 1.2 | V | |
| I_{SD} | Body Diode Continuous Current | | - | - | 3.2 | A | 1 |

Note:

1. The power dissipation is limited by 150°C junction temperature.
2. Device mounted on a glass-epoxy board



FR-4
25.4 × 25.4 mm .
2 Oz Copper

Actual Size

3. The data tested by pulsed , pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
4. Thermal Resistance follow JESD51-3.



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Typical Characteristic Curves

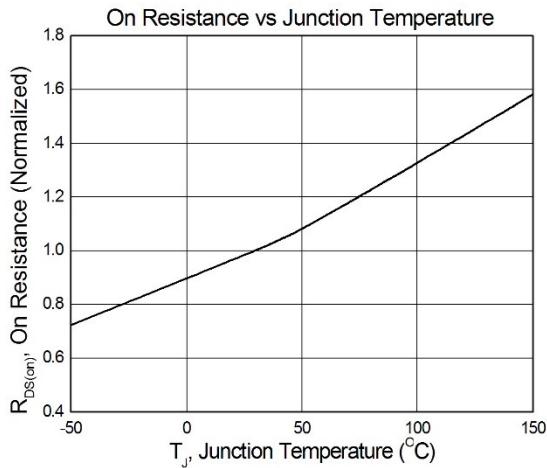


Figure 1

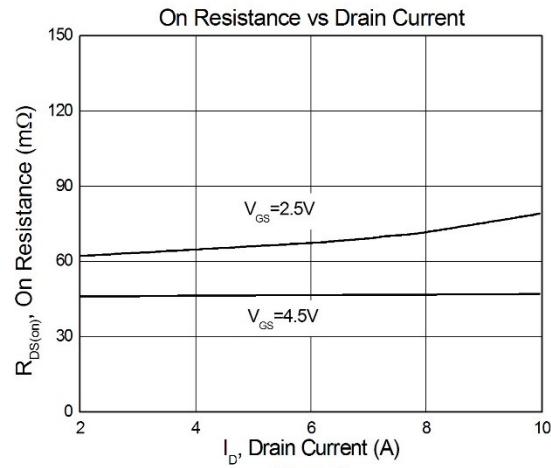


Figure 2

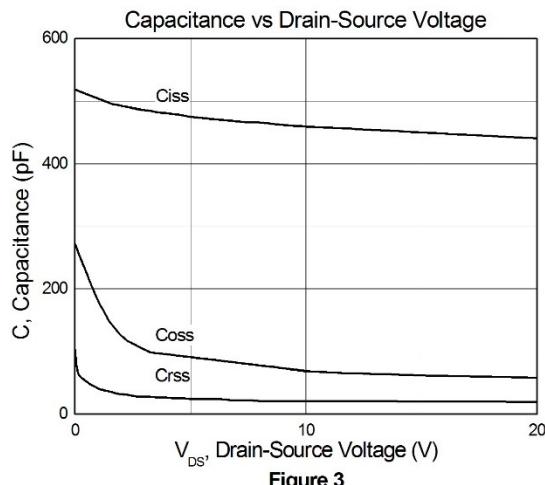


Figure 3

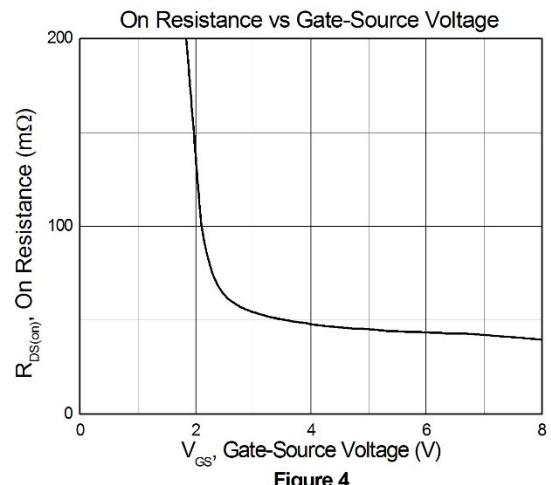


Figure 4

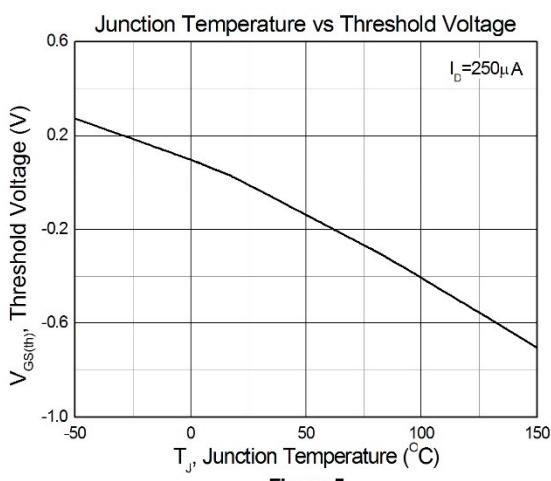


Figure 5

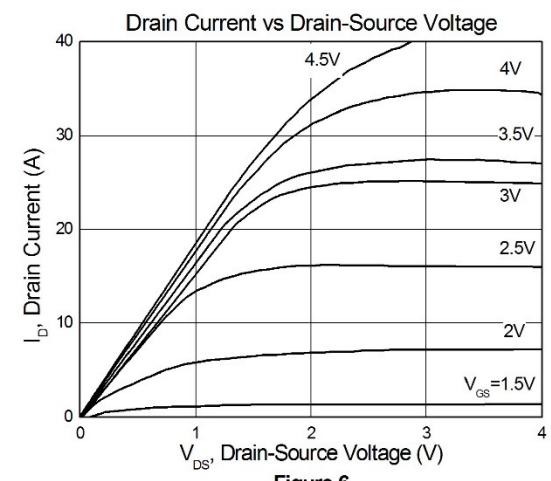
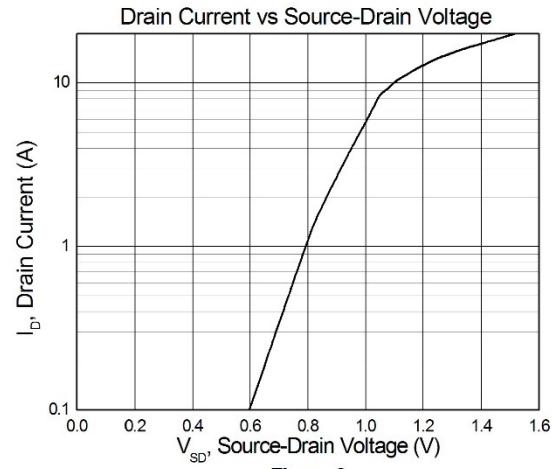
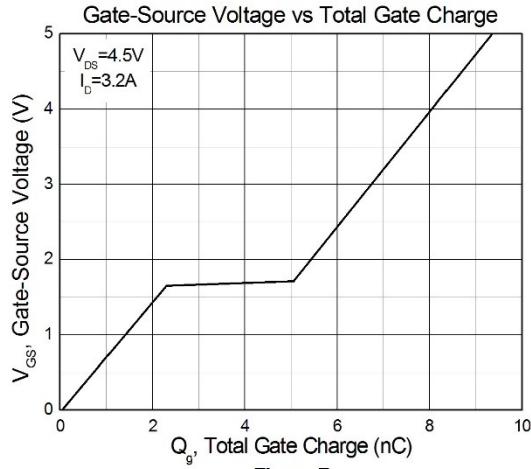


Figure 6



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Test Circuits & Waveforms

Figure 9: Gate Charge Test Circuit

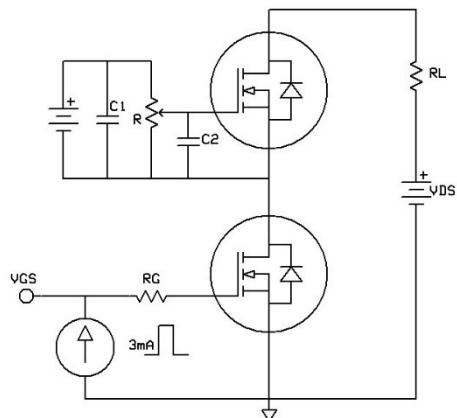


Figure 10: Gate Charge Waveform

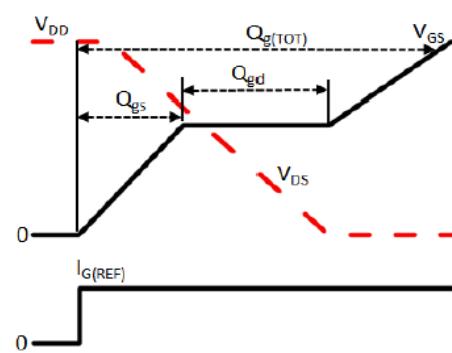


Figure 11: Switching Time Test Circuit

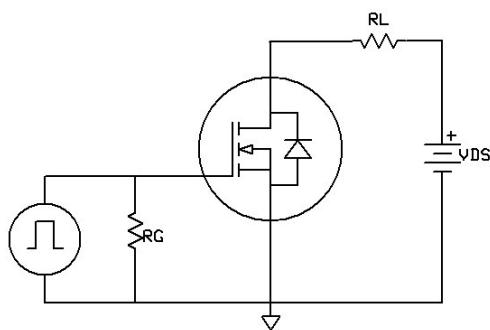
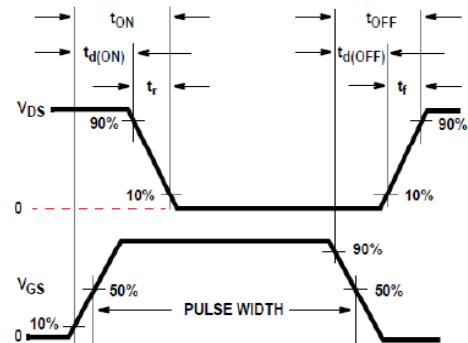
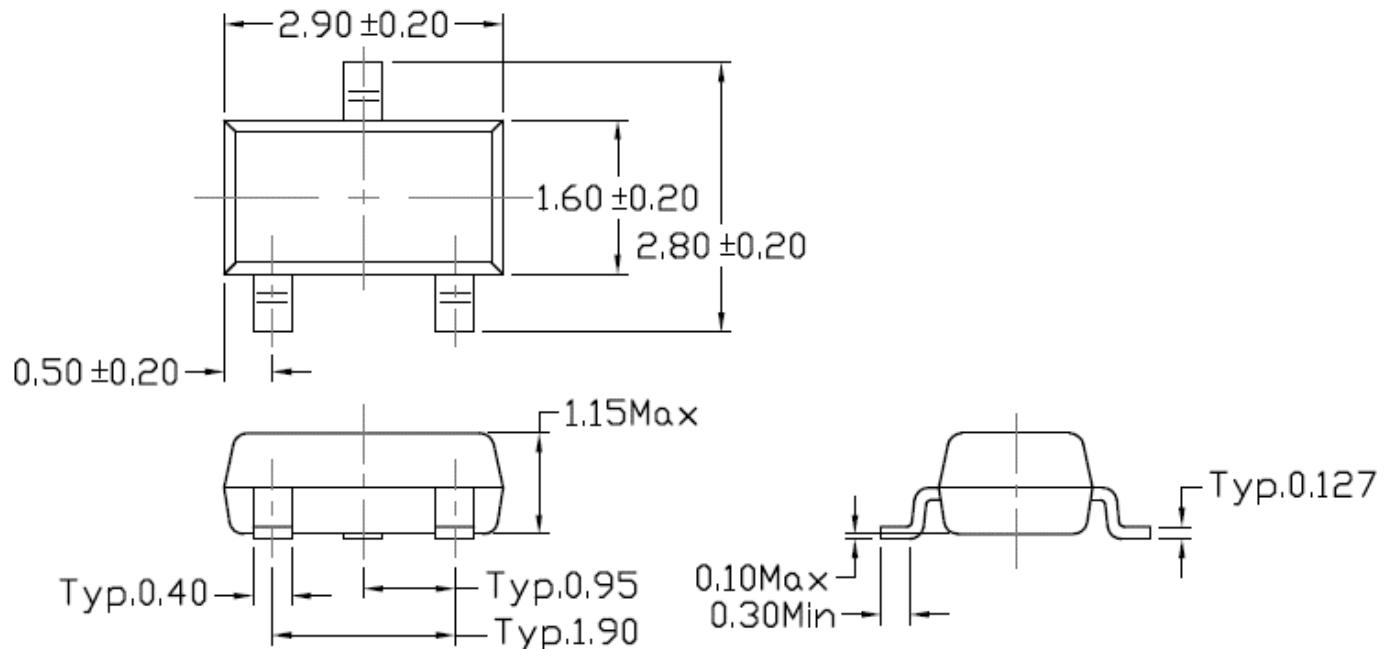


Figure 12: Switching Time Waveform

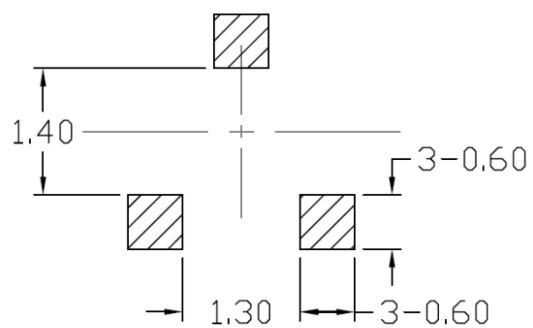




Package Dimension (SC-59)



Recommended pad layout for surface mount leadform

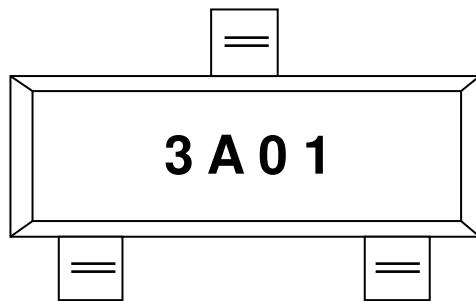




CT3A01-R3

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Marking Information



3A01 : Device Number

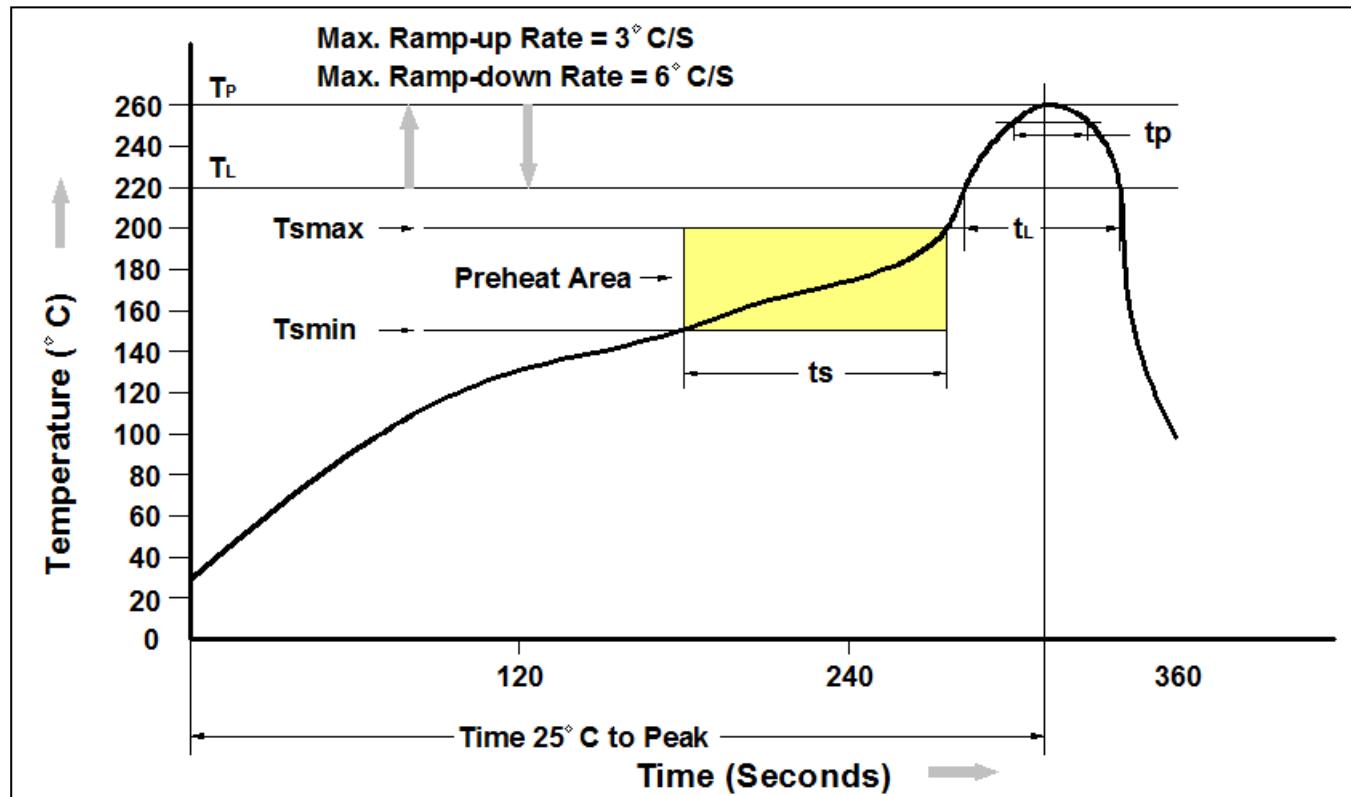
Ordering Information

| Part Number | Description | Quantity |
|--------------------|--------------------|-----------------|
| CT3A01-R3 | SC-59 Reel | 3000 pcs |



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Reflow Profile



| Profile Feature | Pb-Free Assembly Profile |
|---|--------------------------|
| Temperature Min. (T _{smin}) | 150°C |
| Temperature Max. (T _{smax}) | 200°C |
| Time (t _s) from (T _{smin} to T _{smax}) | 60-120 seconds |
| Ramp-up Rate (t _L to t _P) | 3°C/second max. |
| Liquidous Temperature (T _L) | 217°C |
| Time (t _L) Maintained Above (T _L) | 60 – 150 seconds |
| Peak Body Package Temperature | 260°C +0°C / -5°C |
| Time (t _P) within 5°C of 260°C | 30 seconds |
| Ramp-down Rate (T _P to T _L) | 6°C/second max |
| Time 25°C to Peak Temperature | 8 minutes max. |



CT3A01-R3

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