



P-Channel Enhancement MOSFET

Features

- Drain-Source Breakdown Voltage V_{DSS} 200 V
- Drain-Source On-Resistance
 - $R_{\text{DS(ON)}}\,2.3\Omega,$ at $V_{\text{GS}}\text{=}$ 10V, $I_{\text{DS}}\text{=}$ 0.2A

 $R_{\text{DS(ON)}}\,2.4\Omega,$ at V_GS= - 4.5V, I_DS= - 0.2A

- Continuous Drain Current at TA=25 $^\circ\text{C},~I_D$ = 0.4A
- Advanced high cell density Trench Technology
- RoHS Compliance & Halogen Free
- ESD Protection

Applications

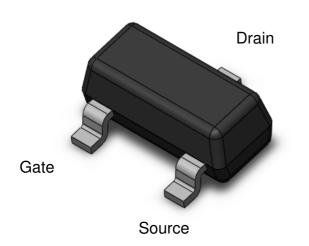
- Switches
- Power supply circuits
- Motor controls
- Drivers

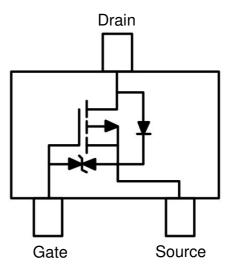
Package Outline

Description

The CT3331-R3 uses high performance Trench Technology to provide excellent $R_{DS(ON)}$ and low gate charge which is suitable for most of the synchronous buck converter applications .

Schematic







Absolute Maximum Rating at 25°C

Symbol	Parameters	Ratings	Units	Notes
V _{DS}	Drain-Source Voltage	-200	V	
V _{GS}	Gate-Source Voltage	±20	V	
I _D	Continuous Drain Current @TA=25°C	-0.4	A	1
I _{DM}	Pulsed Drain Current	-1.6	А	1
PD	Total Power Dissipation @TA=25°C	1.1	W	2
T _{STG}	Storage Temperature Range	-55 to 150	°C	
TJ	Operating Junction Temperature Range	-55 to 150	°C	

Thermal Characteristics

Symbol	Parameters	Test Conditions	Min	Тур	Max	Units	Notes
Devi	Thermal Resistance			121		°C /W	1.4
Roja	Junction-Ambient (t=10s)		-	121	-	°C /W	1,4



Electrical Characteristics $T_A = 25 \, ^{\circ} C (unless otherwise specified)$

Static Characteristics

Symbol	Parameters	Test Conditions	Min	Тур	Max	Units	Notes
Bvdss	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D = - 250µA	-200	-	-	V	
IDSS	Drain-Source Leakage Current	$V_{\text{DS}} = -200 V, V_{\text{GS}} = 0 V$	-	-	-1	μA	
I _{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 16V,\ V_{DS}=0V$	-	-	±10	μA	

On Characteristics

Symbol	Parameters	Test Conditions	Min	Тур	Max	Units	Notes
Deserve	Drain-Source On-Resistance	$V_{GS} = -10V, I_D = -200mA$	-	2.3	4		0
R _{DS(ON)}		$V_{GS} = -4.5V, I_D = -200mA$	-	2.4	4.5		3
V _{GS(TH)}	Gate-Source Threshold Voltage	$V_{GS}=V_{DS},\ I_{D}=-250\mu A$	-1.2	-	-2.6	V	3

Dynamic Characteristics

Symbol	Parameters	Test Conditions	Min	Тур	Max	Units	Notes
CISS	Input Capacitance	$V_{DS} = -20V$,	-	350	-		
Coss	Output Capacitance	$V_{GS} = 0V,$	-	30	-	pF	
Crss	Reverse Transfer Capacitance	f=1MHz	-	12	-		

Switching Characteristics

Symbol	Parameters	Test Conditions	Min	Тур	Max	Units	Notes
T _{D(ON)}	Turn-On Delay Time		-	10	-		
TR	Rise Time	V_{DS} = -100V , V_{GS} = - 4.5V,	-	4	-	20	
T _{D(OFF)}	Turn-Off Delay Time	$R_G=4.7\Omega, I_D=-2.8A$	-	43	-	ns	
TF	Fall Time		-	42			
Q _G	Total Gate Charge	V 100V V 10V	-	7.0	-		
Q _{GS}	Gate-Source Charge	$V_{DS} = -100V$, $V_{GS} = -10V$,	-	1.0	-	nC	
Q _{GD}	Gate-Drain (Miller) Charge	I _D = -400mA	-	0.8	-		

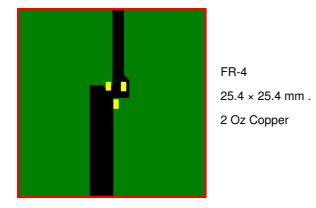


Drain-Source Diode Characteristics

Symbol	Parameters	Test Conditions	Min	Тур	Max	Units	Notes
Vsd	Body Diode Forward Voltage	$V_{GS}=0V,\ I_{SD}=-1.0A$		-0.78	-1.2	V	
Isd	Body Diode Continuous Current				-2.8	А	1

Note:

- 1. The power dissipation is limited by 150° C junction temperature.
- 2. Device mounted on a glass-epoxy board

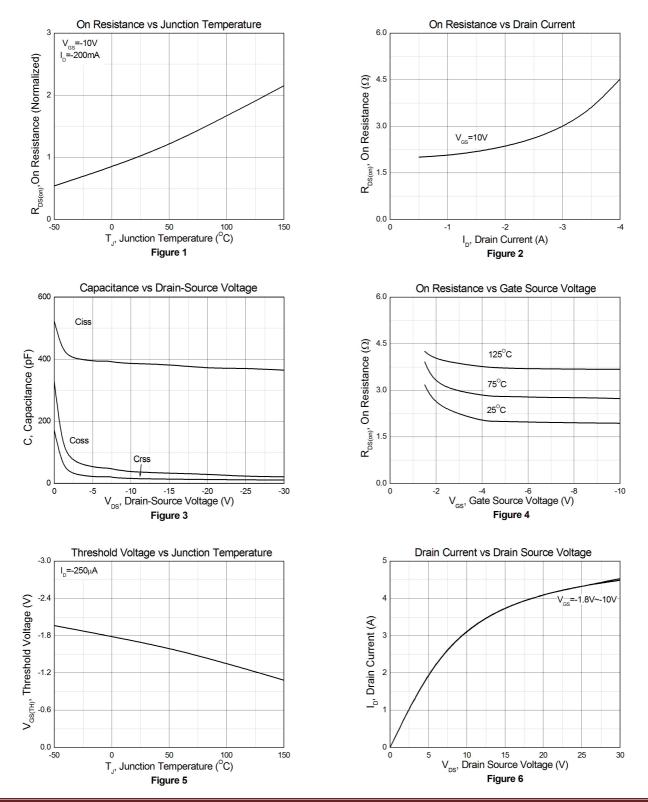


Actual Size

- 3. The data tested by pulsed , pulse width $\,\leq\,$ 300 μs , duty cycle $\,\leq\,$ 2%
- 4. Thermal Resistance follow JESD51-3.

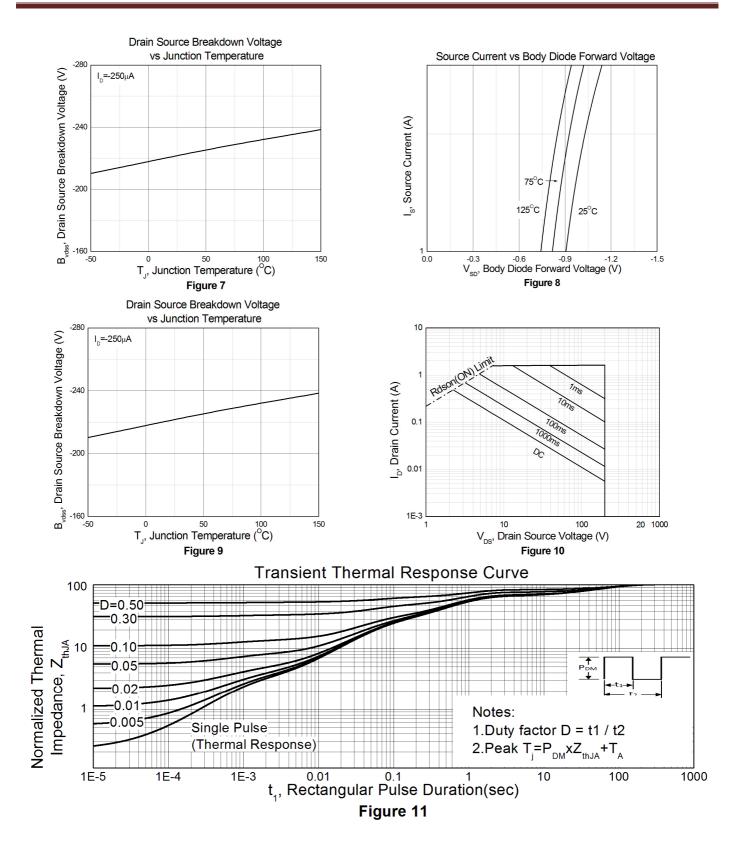


Typical Characteristic Curves





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Test Circuits & Waveforms

Figure 12: Gate Charge Test Circuit

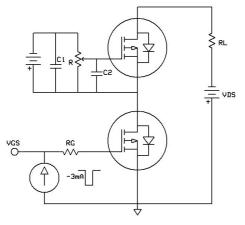


Figure 14: Switching Time Test Circuit

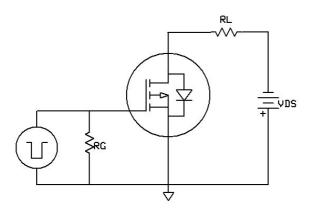


Figure 13: Gate Charge Waveform

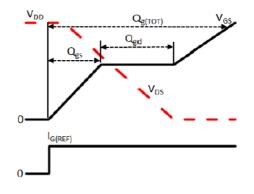
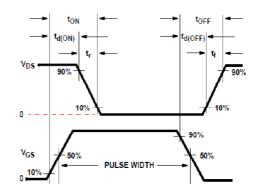
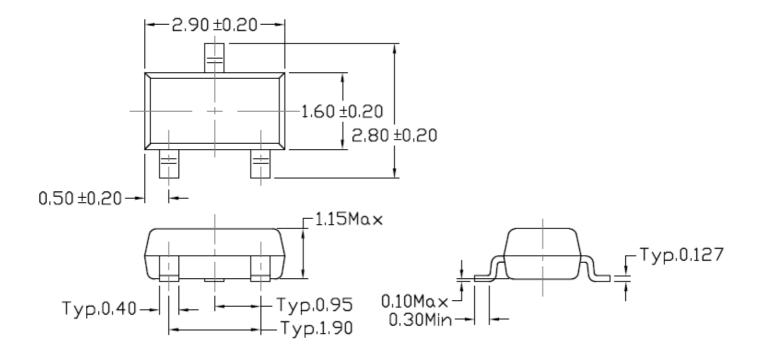


Figure 15: Switching Time Waveform



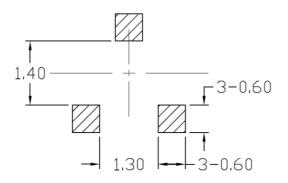


Package Dimension (SC-59)



Note: Dimensions in mm

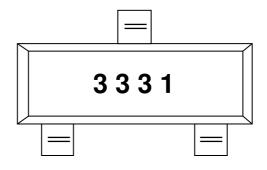
Recommended pad layout for surface mount leadform



Note: Dimensions in mm



Marking Information



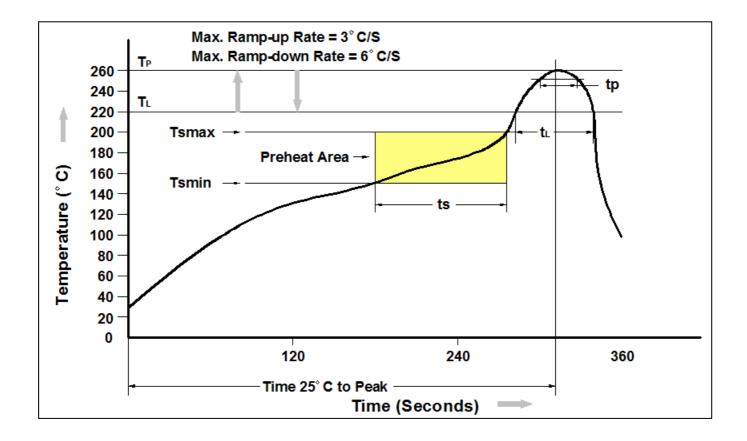
3331 : Device Number

Ordering Information

Part Number	Description	Quantity
CT3331-R3	SC-59 Reel	3000 pcs



Reflow Profile



Profile Feature	Pb-Free Assembly Profile
Temperature Min. (Tsmin)	150 <i>°</i> C
Temperature Max. (Tsmax)	200 <i>°</i> C
Time (ts) from (Tsmin to Tsmax)	60-120 seconds
Ramp-up Rate (t∟ to t _P)	3℃/second max.
Liquidous Temperature (TL)	217℃
Time (t_L) Maintained Above (T_L)	60 – 150 seconds
Peak Body Package Temperature	260 ℃ +0 ℃ / -5 ℃
Time (t _P) within 5 °C of 260 °C	30 seconds
Ramp-down Rate $(T_P \text{ to } T_L)$	6°C/second max
Time 25 °C to Peak Temperature	8 minutes max.



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