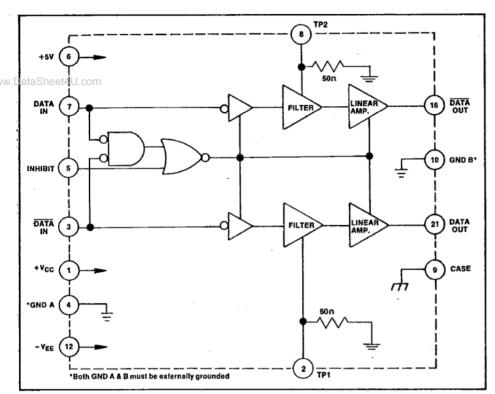


# CT 2077/CT 3077

## **Interface Driver**

for MIL-STD-1553A & MDC A3818

July 1982



### **FEATURES**

- Meets MIL-STD-1553A and MDC A3818 Interface Specifications
- Compatible with CT3078 Receiver
- **TTL Compatible**
- Meets MIL-STD-883 and MIL-M-38510 Requirements
- Thick Film Hybrid Technology

FIGURE 1.
FUNCTIONAL DIAGRAM AND PINOUTS

#### DESCRIPTION

The CT2077/CT3077 Interface Driver accepts complementary TTL Data at the input and produces a 32 volt nominal peak-to-peak differential signal at the output. When coupled to the Data Bus with a 1:1 transformer, isolated on the Data Bus side with two 52.5 ohm fault isolation resistors, and loaded by two 70 ohm terminations plus additional receivers, the Data Bus signal produced is 6.5 volts peak-to-peak nominal.

When both "Data" and "Data" inputs are held low, the driver output becomes a high impedance and is "removed" from the line. In addition, an overriding "Inhibit" input provides for removal of the driver output from the line. A logic "1" applied to the "Inhibit" takes priority over the condition of the data inputs and disables the driver.

The signal is filtered to suppress harmonics above 1 MHz, and the output amplifiers are linear.

Both the CT2077/CT3077 Driver and CT3078 Receiver may be connected to the same winding of the 1:1 transformer as shown in Figure 2. The transformer windings must have a self impedance greater than 5000 ohms from 100 kHz to 1MHz and must be capable of driving 13 volts r.m.s. into a 135 ohm load from 200 kHz to 1 MHz without distortion.

# CTI

## CIRCUIT TECHNOLOGY INCORPORATED

+ Vcc Supply	CT2077 + 5V ± 5% + 15V ± 3% to - 15V ± 3%	$+ 12V \pm 3\%$		
Transmitting Supply Currents 120 mA* (from Vcc and VEE supplies)				
Input Characteristics** Data and Data	0.7 mA maximu	m		
Output Characteristics:  Level	10 ohm maximu Output filtered t above 1 MHz ± 15 ns from 33	ım at 1 MHz to suppress h		
Inhibit Characteristics	(TTL "1" at "Inh at "0")	ibit" or Data a	and Data	
Output Noise Output Impedance	<10 mV p-p Dif		ИНz	
Power Dissipation	1 Watt in STAN See Figure 4 for			
Package	1.25" x 1.25" fla	t-pack, .100"	lead spacing	
Thermal Requirement	Case must be h continuous ope		maximum for	

<sup>\*</sup>Typical driving 1:1 transformer at 32 volts p-p at 1 MHz with fault isolation resistors in secondary and two 70 ohm terminations in parallel. (See Figure 4 for typical power dissipation as a function of duty cycle.)

<sup>\*\*</sup>DATA and DATA inputs must be complementary waveforms, of 50% duty cycle average, with no gate delays between them. It is recommended that those inputs be driven from a "D" type filp-flop.

# CT2077/CT3077 1553/A3818 INTERFACE DRIVER

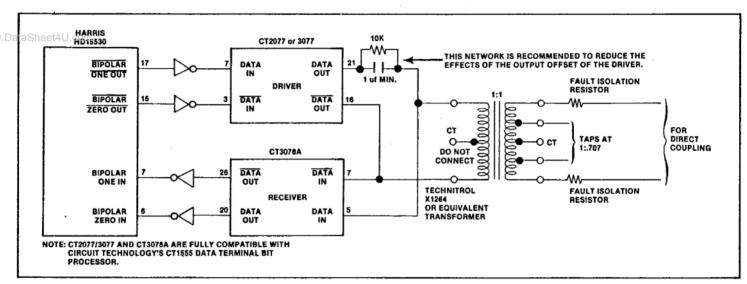


FIGURE 2.
TYPICAL DRIVER/RECEIVER CONNECTION

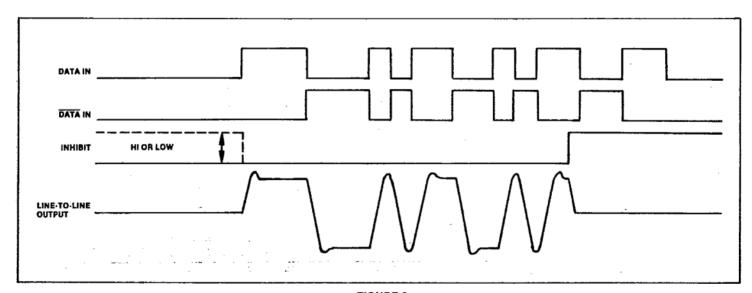


FIGURE 3.
DRIVER LOGIC WAVEFORM

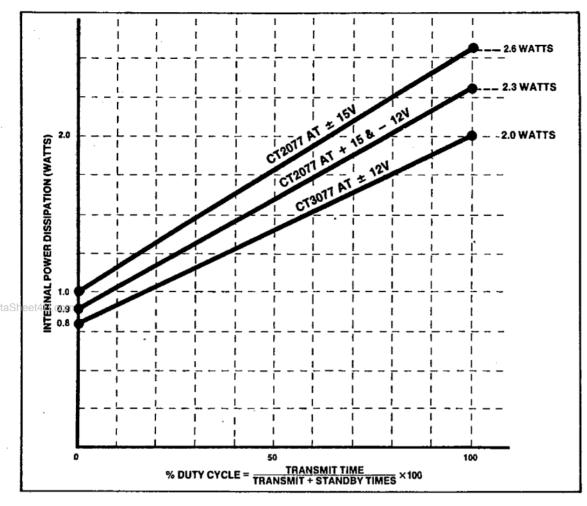


FIGURE 4.

INTERNAL POWER DISSIPATION AT 32 VOLTS P-P OUTPUT INTO A 135 OHM LOAD.

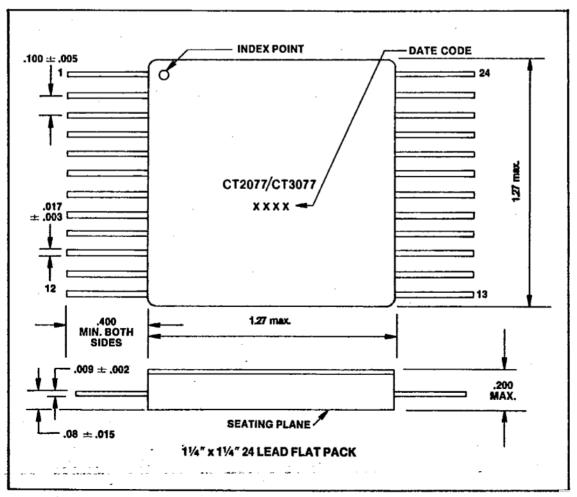


FIGURE 5.
PACKAĞE OUTLINE DRAWING

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Package		<del></del>	
Screening & Inspection			
Temperature Range	<del></del>		
www.DataSheet4U.comSpecial Requirements/Enhancements			

## PACKAGE

- A. Pin Grid Array
- C. Ceramic DIL
- E. Epic
- F. Flat Pack
- G. Cerdip
- L. Leadless Chip Carrier
- M. Module
- N. Naked Die
- P. Plastic DIL
- Q. Quad Plastic J-Lead
- R. Qual Cerpack J-Lead
- S. SO Plastic
- X. Special

## TEMPERATURE RANGE

- A. Special
- B. 0 to 70°C
- C. -55 to +125°C
- D. -25 to +70°C
- E. -25 to +85°C
- F. -40 to +85°C
- G. -55 to +85°C
- H. -40 to +125°C
- J. -10 to +80°C
- K. 0 to +200°C

## SCREENING & INSPECTION

- B. Mil Std-883C Class B
- G. Commercial Hermetic
- L. Commercial Plastic
- S. Mil Std-883C Class S
- T. ESA9000
- X. Special

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