#### N-Channel 30V, 8.0mΩ Typ. Power MOSFET

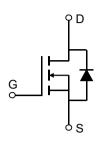
### **Description**

#### **Features**

• 30V, 22A

$$R_{DS(ON)}$$
 Typ = 8.0m $\Omega$  @  $V_{GS}$  = 10V  
 $R_{DS(ON)}$  Typ = 12.2m $\Omega$  @  $V_{GS}$  = 4.5V

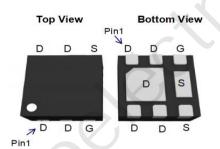
- Advanced Trench Technology
- Excellent R<sub>DS(ON)</sub> and Low Gate Charge
- Lead Free

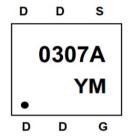




### **Application**

- Load Switch
- PWM Application
- Power Management





**Marking and Pin Assignment** 

#### **Package Marking and Ordering Information**

Device	Marking	Package	Outline	Reel Size	Reel (pcs)	Per Carton (pcs)
CRMVTL0307A	0307A	DFN2020-6L	TAPING	7"	3000	120000

#### Absolute Maximum Ratings (@ T<sub>J</sub> = 25°C unless otherwise specified)

Symbol	Parameter		Value	Units
$V_{ t DS}$	Drain-to-Source Voltage		30	V
$V_{GS}$	Gate-to-Source Voltage		±20	V
	Continuous Drain Current	T <sub>C</sub> = 25°C	22	Α
I <sub>D</sub>	Continuous Diain Current	T <sub>C</sub> = 100°C	13.2	Α
I <sub>DM</sub>	Pulsed Drain Current (1)		88	Α
$P_{D}$	Power Dissipation	T <sub>C</sub> = 25°C	7.8	W
$R_{ heta JC}$	Thermal Resistance, Junction to Case		16	°C/W
$T_J,T_STG$	Junction & Storage Temperature Range		-55 to 150	°C

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### **Electrical Characteristics** (T<sub>J</sub> = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Uni
Off Chara	acteristics					
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0 V$	30	-	-	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	$V_{DS} = 30V, V_{GS} = 0V$	-	-	1.0	μΑ
I <sub>GSS</sub>	Gate-Body Leakage Current	$V_{DS} = 0V, V_{GS} = \pm 20V$	-	-	±100	nA
On Chara	acteristics				6	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1	1.5	2.2	V
Б		V <sub>GS</sub> = 10V, I <sub>D</sub> = 5A	-	8	10.5	mΩ
$R_{DS(ON)}$	Static Drain-Source ON-Resistance <sup>(2)</sup>	$V_{GS} = 4.5V, I_D = 3A$	-	12	15.5	mΩ
Dynamic	Characteristics					
C <sub>iss</sub>	Input Capacitance			1061	-	pF
$C_{oss}$	Output Capacitance	$V_{GS} = 0V, V_{DS} = 15V,$ f = 1MHz	X-\	127	-	pF
$C_{rss}$	Reverse Transfer Capacitance	1 - 1101112	-	100	-	pF
Q <sub>g</sub>	Total Gate Charge		<b>U</b> -	20	-	nC
$Q_gs$	Gate Source Charge	$V_{GS} = 0 \text{ to } 10V$ $V_{DS} = 15V, I_{D} = 20A$	-	4	-	nC
$Q_{gd}$	Gate Drain("Miller") Charge	V <sub>DS</sub> - 13V, I <sub>D</sub> - 20A	-	5	-	nC
	g Characteristics					
t <sub>d(on)</sub>	Turn-On DelayTime	.( )	-	6	-	ns
t <sub>r</sub>	Turn-On Rise Time	V <sub>GS</sub> = 10V, V <sub>DD</sub> = 15V	-	19	-	ns
$t_{\text{d(off)}}$	Turn-Off DelayTime	$I_D$ = 20A, $R_{GEN}$ = $3\Omega$	-	22	-	ns
$\mathbf{t}_{f}$	Turn-Off Fall Time		-	5	-	ns
Drain-So	urce Diode Characteristics and M	Max Ratings				
I <sub>S</sub>	Maximum Continuous Drain to Source Di	ode Forward Current	-	-	22	Α
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode	Forward Current	-	-	88	Α
V <sub>SD</sub>	Drain to Source Diode Forward Voltage	$V_{GS} = 0V, I_{S} = 5A$	-	-	1.2	V
trr	Body Diode Reverse Recovery Time	1 - 200 4:/44 - 400 6 /	-	8	-	ns
Qrr	Body Diode Reverse Recovery Charge	$I_F = 20A$ , di/dt = 100A/us	_	1.6	-	nC

Notes:

<sup>1.</sup> Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.

<sup>2.</sup> Pulse Test: Pulse Width  $\leq$  300  $\mu$ s, Duty Cycle  $\leq$  0.5%.

# **Typical Performance Characteristics**

11 11 21 11 11 11 11

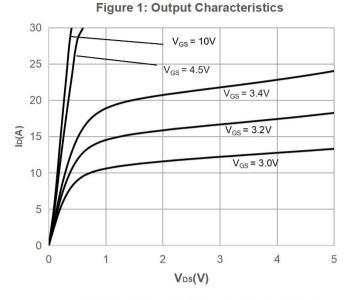


Figure 3: On-resistance vs. Drain Current

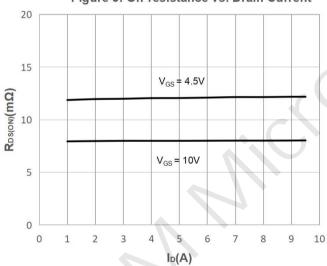


Figure 5: Gate Charge Characteristics

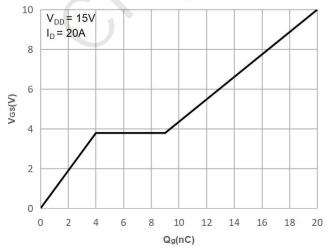


Figure 2: Typical Transfer Characteristics

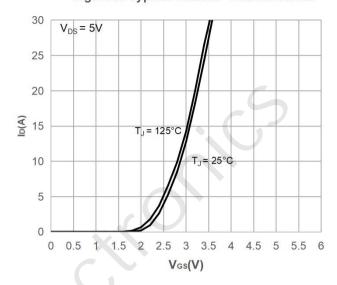


Figure 4: Body Diode Characteristics

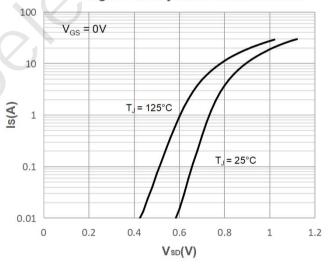
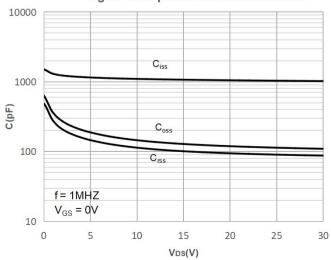


Figure 6: Capacitance Characteristics



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# **Typical Performance Characteristics**

Figure 7: Normalized Breakdown voltage vs.
Junction Temperature

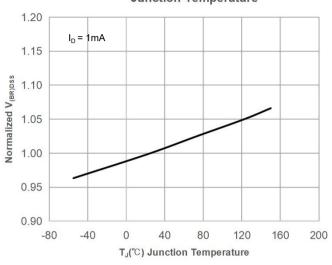


Figure 9: Maximum Safe Operating Area

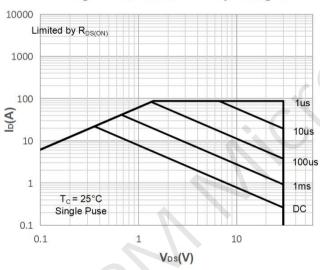


Figure 11: Normalized Maximum Transient

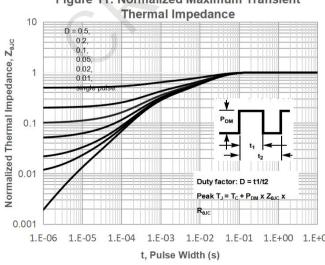


Figure 8: Normalized on Resistance vs. Junction Temperature

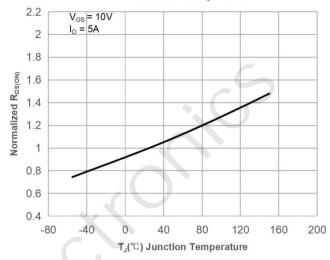


Figure 10: Maximum Continuous Drian Current vs. Case Temperature

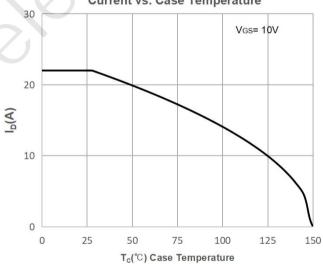
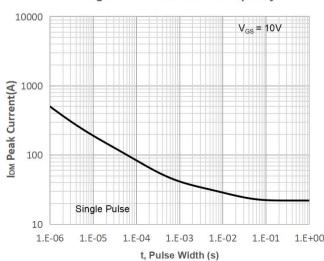


Figure 12: Peak Current Capacity



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### **Test Circuit**

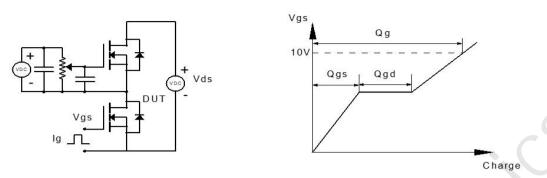


Figure 1: Gate Charge Test Circuit & Waveform

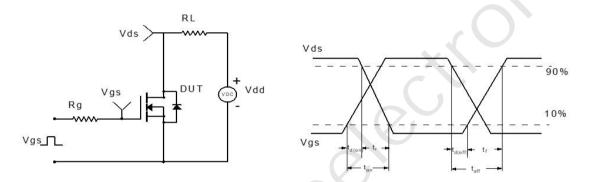


Figure 2: Resistive Switching Test Circuit & Waveform

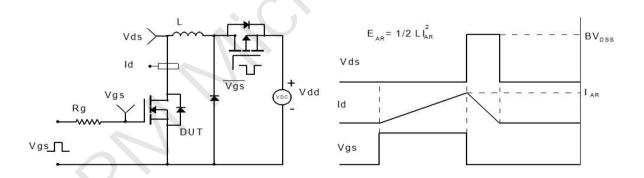


Figure 3: Unclamped Inductive Switching Test Circuit& Waveform

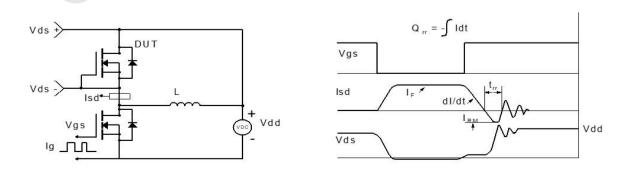
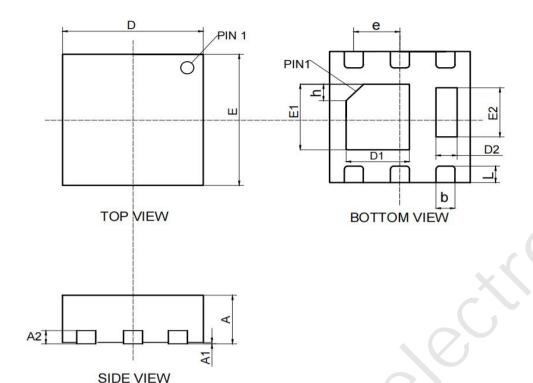


Figure 4: Diode Recovery Test Circuit & Waveform

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### Package Mechanical Data(DFN2020-6L)



SYMBOL	MIN	NOM	MAX
Α	0.70	0.75	0.80
A1	NA	0.02	0.05
A2	0.18	0.20	0.25
b	0.20	0.27	0.34
D	1.95	2.00	2.05
E	1.95	2.00	2.05
D1	0.80	0.90	1.00
E1	0.90	1.00	1.10
D2	0.20	0.30	0.40
E2	0.65	0.75	0.85
L	0.20	0.25	0.35
h	0.20	0.25	0.30
е	0.65 BSC		

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