N-Channel 60V, 15.5m Ω Typ. Power MOSFET

Description

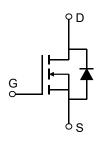
Features

• 60V, 15A

$$R_{DS(ON)}$$
 Typ = 15.5m Ω @ V_{GS} = 10V

$$R_{DS(ON)}$$
 Typ = $22m\Omega$ @ V_{GS} = 4.5V

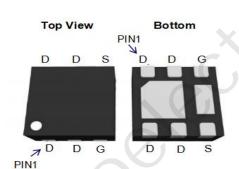
- Advanced Split Gate Trench Technology
- Excellent R_{DS(ON)} and Low Gate Charge
- Lead Free

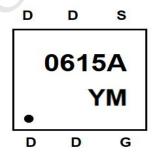


Schematic Diagram

Application

- Load Switch
- PWM Application
- Power Management





Marking and Pin Assignment

Package Marking and Ordering Information

Device	Marking	Package	Outline	Reel Size	Reel (pcs)	Per Carton (pcs)
CRMVGL0615A	0615A	DFN2020-6L	TAPING	7"	3000	120000

Absolute Maximum Ratings (@ T_J = 25°C unless otherwise specified)

Symbol	Parameter		Value	Units
V_{DS}	Drain-to-Source Voltage		60	V
V_{GS}	Gate-to-Source Voltage		±20	V
,	Continuous Drain Current	T _C = 25°C	15	Α
I _D	Continuous Diain Current	T _C = 100°C	9	Α
I _{DM}	Pulsed Drain Current (1)		60	Α
P_{D}	Power Dissipation	T _C = 25°C	8.6	W
$R_{ heta JC}$	Thermal Resistance, Junction to Case		14.5	°C/W
T_J,T_STG	Junction & Storage Temperature Range		-55 to 150	°C

1

N-Channel 60V, 15.5m Ω Typ. Power MOSFET

Electrical Characteristics (T_J = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Uni
Off Chara	acteristics					
V _{(BR)DSS}	Drain-Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0 V$	60	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 60V, V_{GS} = 0V$	-	-	1.0	μΑ
I _{GSS}	Gate-Body Leakage Current	$V_{DS} = 0V, V_{GS} = \pm 20V$	-	-	±100	nA
On Chara	acteristics				6	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1	1.5	2	V
D		$V_{GS} = 10V, I_D = 5A$	-	15.5	20	mΩ
$R_{DS(ON)}$	Static Drain-Source ON-Resistance ⁽²⁾	$V_{GS} = 4.5V, I_D = 3A$	-	22	29	mΩ
Dynamic	Characteristics					
C _{iss}	Input Capacitance		-(527	-	pF
C_{oss}	Output Capacitance	$V_{GS} = 0V, V_{DS} = 30V,$ f = 1MHz	X - \	157	-	pF
C_{rss}	Reverse Transfer Capacitance	1 – 1101112	-	7.5	-	pF
Q_g	Total Gate Charge		U -	12.1	-	nC
Q_gs	Gate Source Charge	$V_{GS} = 0 \text{ to } 10V$ $V_{DS} = 30V, I_{D} = 15A$	-	1.3	-	nC
Q_{gd}	Gate Drain("Miller") Charge	V _{DS} - 30 V, I _D - 13A	-	2.6	-	nC
	g Characteristics					
t _{d(on)}	Turn-On DelayTime	.r ()	-	3.3	-	ns
t _r	Turn-On Rise Time	$V_{GS} = 10V, V_{DD} = 30V$	-	4	-	ns
$t_{\text{d(off)}}$	Turn-Off DelayTime	I_D = 15A, R_{GEN} = 6Ω	-	14	-	ns
\mathbf{t}_{f}	Turn-Off Fall Time		-	5.5	-	ns
Drain-So	urce Diode Characteristics and M	Max Ratings				
I _S	Maximum Continuous Drain to Source Di	ode Forward Current	-	-	15	Α
I _{SM}	Maximum Pulsed Drain to Source Diode	Forward Current	-	-	60	Α
V _{SD}	Drain to Source Diode Forward Voltage	$V_{GS} = 0V, I_{S} = 5A$	-	-	1.2	V
trr	Body Diode Reverse Recovery Time	1 - 450 - 4:14 - 4004 !-	-	20	-	ns
Qrr	Body Diode Reverse Recovery Charge	$I_F = 15A$, di/dt = 100A/us	-	8.1	-	nC

Notes:

^{1.} Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.

^{2.} Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 0.5%.

N-Channel 60V, 15.5mΩ Typ. Power MOSFET

Typical Performance Characteristics

Figure 1: Output Characteristics

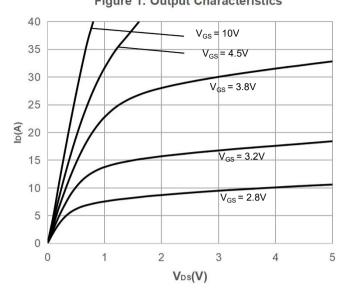


Figure 2: Typical Transfer Characteristics

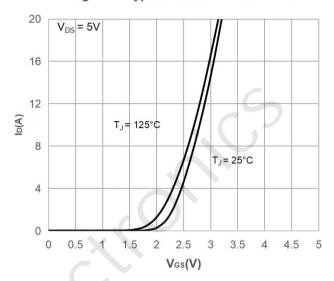


Figure 3: On-resistance vs. Drain Current

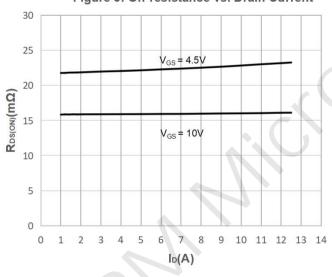


Figure 4: Body Diode Characteristics

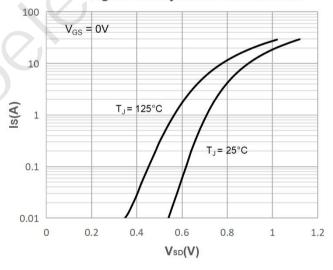


Figure 5: Gate Charge Characteristics

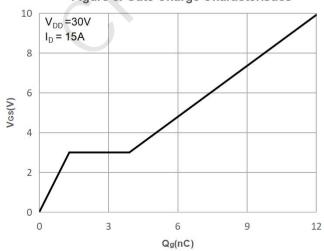
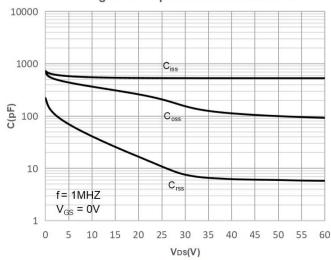


Figure 6: Capacitance Characteristics



Typical Performance Characteristics

Figure 7: Normalized Breakdown voltage vs.
Junction Temperature

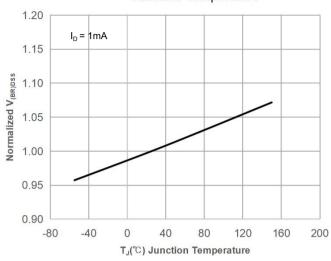


Figure 9: Maximum Safe Operating Area

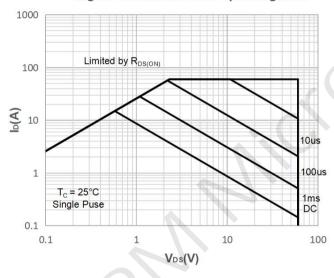


Figure 11: Normalized Maximum Transient

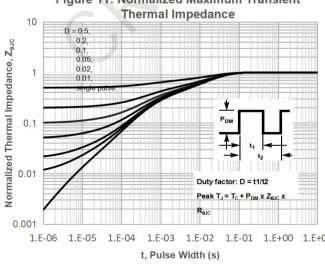


Figure 8: Normalized on Resistance vs. Junction Temperature

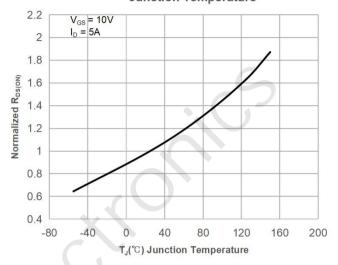


Figure 10: Maximum Continuous Drian
Current vs. Case Temperature

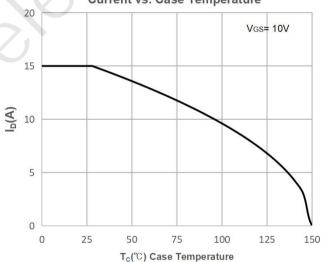
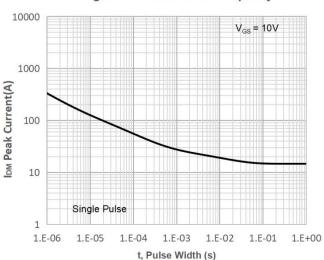


Figure 12: Peak Current Capacity



Test Circuit

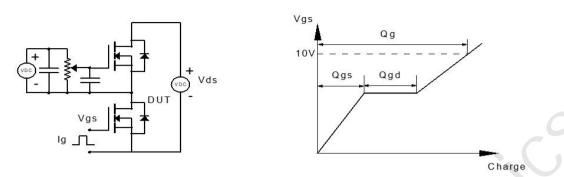


Figure 1: Gate Charge Test Circuit & Waveform

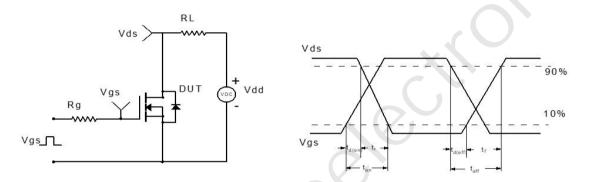


Figure 2: Resistive Switching Test Circuit & Waveform

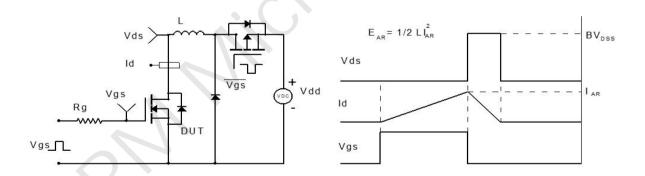


Figure 3: Unclamped Inductive Switching Test Circuit& Waveform

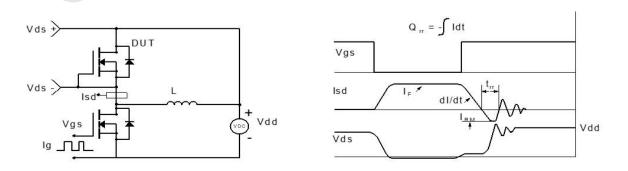
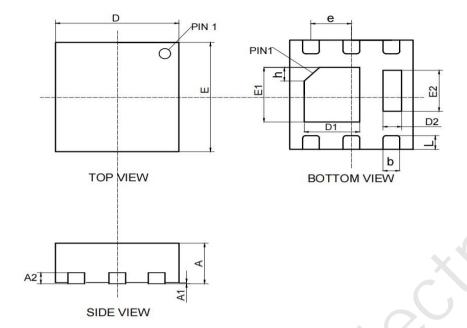


Figure 4: Diode Recovery Test Circuit & Waveform

N-Channel 60V, 15.5mΩ Typ. Power MOSFET

Package Mechanical Data(DFN2020-6L)



SYMBOL	MIN	NOM	MAX
Α	0.70	0.75	0.80
A1	NA	0.02	0.05
A2	0.18	0.20	0.25
b	0.20	0.27	0.34
D	1.95	2.00	2.05
E	1.95	2.00	2.05
D1	0.80	0.90	1.00
E1	0.90	1.00	1.10
D2	0.20	0.30	0.40
E2	0.65	0.75	0.85
L	0.20	0.25	0.35
h	0.20	0.25	0.30
е	0.65 BSC		

Important Notice

The information presented in datasheets is for reference only. CRM reserves the right to make changes at any time to any products or information herein, without notice.

Customers are responsible for the design and applications, including compliance with all laws, regulations and safety requirements or standards.

"Typical" parameters which provided in datasheets can vary in different applications and actual performance may vary over time. Customers are responsible for doing all necessary testing to minimize the risks associated with their applications and products.

is a registered trademark of Wuxi CRM Microelectronics Co., Ltd. Copyright ©2023 CRM Microelectronics Co., Ltd. All rights reserved.

Contact information

For more information, please visit: http://www.crm-semi.tech For sales information, please send an email to: sales@crm-semi.com