CRMTGH1002A

Description

N-channel Enhancement Mode Power MOSFET

Features

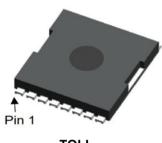
- 100V, 290A
 - $R_{DS(ON)}$ < 2m Ω @ V_{GS} = 10V
- Advanced Split Gate Trench Technology
- Excellent R_{DS(ON)} and Low Gate Charge

Applications

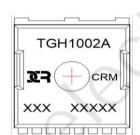
- Load Switch
- PWM Application
- Power Management

100% UIS TESTED! 100% ΔVds TESTED!

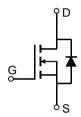








Marking and Pin Assignment



Schematic Diagram

Package Marking and Ordering Information

Device Marking	Device	Outline	Package	Reel Size	Reel(pcs)	Per Carton (pcs)
CRMTGH1002A	CRMTGH1002A	TAPING	TOLL	13"	2000	10000

Absolute Maximum Ratings (@ T_C = 25°C unless otherwise specified)

Symbol	Parameter		Value	Units
V _{DS}	Drain-to-Source Voltage		100	V
V _{GS}	Gate-to-Source Voltage		±20	V
	Continuous Drain Current	T _C = 25°C	290	Δ.
I _D		T _C = 100°C	183	А
I _{DM}	Pulsed Drain Current (1)		1160	Α
E _{AS}	Single Pulsed Avalanche Energy	, (2)	729	mJ
P _D	Power Dissipation	T _C = 25°C	313	W
$R_{\theta JC}$	Thermal Resistance, Junction to C	ase	0.4	°C/W
T _J , T _{STG}	Junction & Storage Temperature R	ange	-55 to 175	°C

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Electrical Characteristics (T_J = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Off Cha	racteristics					
V _{(BR)DSS}	Drain-Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0 V$	100	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 100V, V _{GS} = 0V	-	-	1.0	μА
I _{GSS}	Gate-Body Leakage Current	$V_{DS} = 0V, V_{GS} = \pm 20V$	-	-	±100	nA
On Cha	racteristics					
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.0	2.8	4.0	V
R _{DS(ON)}	Static Drain-Source ON-Resistance ⁽⁴⁾	$V_{GS} = 10V, I_D = 30A$	-	1.7	2.0	mΩ
Dynami	ic Characteristics					
C _{iss}	Input Capacitance		-	10123	-	pF
C _{oss}	Output Capacitance	$V_{GS} = 0V, V_{DS} = 50V,$ f = 1MHz	-	2049	-	pF
C _{rss}	Reverse Transfer Capacitance	I = IIVIDZ	-	77	-	pF
Qg	Total Gate Charge	$V_{GS} = 0 \text{ to } 10V$ $V_{DS} = 50V, I_{D} = 90A$	-	140	-	nC
Q_{gs}	Gate Source Charge		-	48	-	nC
Q_{gd}	Gate Drain("Miller") Charge	V _{DS} = 30V, I _D = 90A)-	30	-	nC
Switchi	ng Characteristics					
$t_{d(on)}$	Turn-On DelayTime		-	30	-	ns
t _r	Turn-On Rise Time	$V_{GS} = 10V, V_{DD} = 50V$	-	105	-	ns
$t_{\text{d(off)}}$	Turn-Off DelayTime	I_D = 90A, R_{GEN} = 3Ω	1	81	-	ns
t _f	Turn-Off Fall Time		-	109	-	ns
Drain-S	ource Diode Characteristics and N	Max Ratings				
Is	Maximum Continuous Drain to Source Diod	e Forward Current	-	-	290	Α
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	1160	Α
V_{SD}	Drain to Source Diode Forward Voltage	$V_{GS} = 0V, I_{S} = 30A$	-	-	1.4	V
trr	Body Diode Reverse Recovery Time	1 - 004 di/dt - 1004//-	-	56	-	ns
Qrr	Body Diode Reverse Recovery Charge	I _F = 90A, di/dt = 100A/us	-	96	-	nC

Notes:

^{1.} Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.

^{2.} E_{AS} condition: Starting T_J =25C, V_{DD} =20V, V_G =10V, R_G =25ohm, L=0.5mH, I_{AS} =54A

^{3.} Pulse Test: Pulse Width≤300µs, Duty Cycle≤2%.



Test Circuit

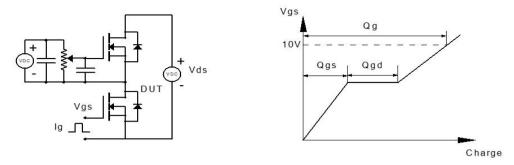


Figure 1: Gate Charge Test Circuit & Waveform

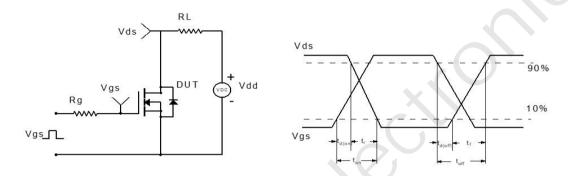


Figure 2: Resistive Switching Test Circuit & Waveform

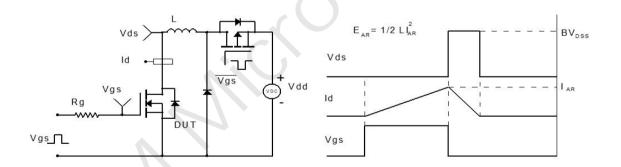


Figure 3: Unclamped Inductive Switching Test Circuit& Waveform

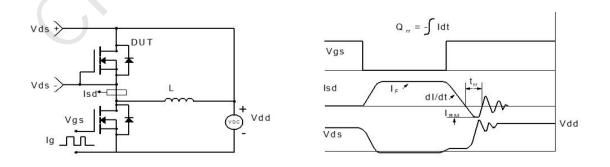
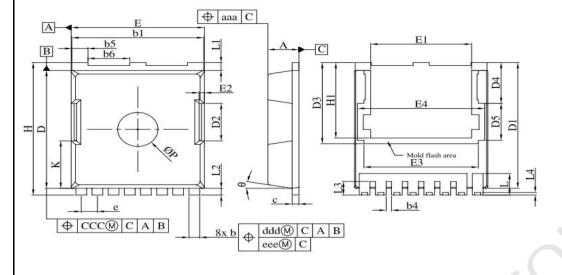


Figure 4: Diode Recovery Test Circuit & Waveform

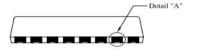


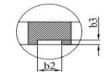
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Package Mechanical Data(TOLL)



M B O	MILLIMETER				
5	MIN.	NOMINAL	MAX		
A	2.20	2.30	2.40		
b	0.70	0.80	0.90		
b1	9.70	9.80	9,90		
52	0.36	0.45	0.55		
3	0.05	0.100	1		
04	0.30	0.40	0.50		
55	1.10	1.20	1.30		
06	3.00	3.10	3.20		
С	0.40	0.50	0.60		
D	10.28	10.38	10.55		
01	10.98	11.08	11.18		
)2	3.20	3.30	3.40		
03		7.15			
04		3,59			
05		3.26			
e	1.10	1.20	1.30		
E.	9.80	9.90	10.00		
11	7.40	7.50	7.60		
2	0.30	0.40	0.50		
3	8,50				
4	9,46				
I	11.50	11.68	11.85		
11	6.55	6.65	6.75		
(4.08	4.18	4.28		
L	1.60	1.90	2.10		
J.	0.50	0.70	0.90		
2	0.50	0.60	0.70		
.3	1.00	1.20	1.30		
4	0.13	0.23	0.33		
P	2.85	3.00	3.15		
9	10° REF				
aa	0.20				
cc		0.20			
dd	,	0.25			
ee.		0.20			





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