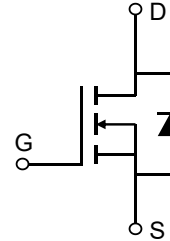


Description

Features

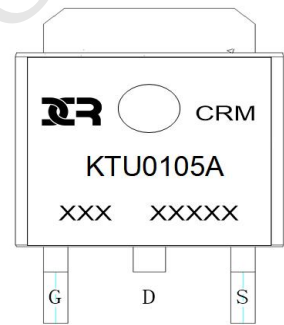
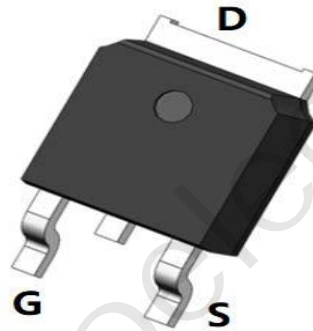
- 12V, 50A
- $R_{DS(ON)}$ Typ = 4.4mΩ @ $V_{GS} = 4.5V$
- $R_{DS(ON)}$ Typ = 6.2mΩ @ $V_{GS} = 2.5V$
- Advanced Trench Technology
- Excellent $R_{DS(ON)}$ and Low Gate Charge
- 100% UIS TESTED!
- 100% ΔV_{ds} TESTED!



Schematic Diagram

Application

- Load Switch
- PWM Application
- Power Management



Marking and Pin Assignment

Package Marking and Ordering Information

Device	Marking	Package	Outline	Reel Size	Reel (pcs)	Per Carton (pcs)
CRMKTU0105A	CRMKTU0105A	TO-252-3L	TAPING	13"	2500	25000

Absolute Maximum Ratings (@ $T_J = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Value	Units
V_{DS}	Drain-to-Source Voltage	12	V
V_{GS}	Gate-to-Source Voltage	± 12	V
I_D	Continuous Drain Current	$T_C = 25^\circ\text{C}$	50
		$T_C = 100^\circ\text{C}$	30
I_{DM}	Pulsed Drain Current ⁽¹⁾	200	A
E_{AS}	Single Pulsed Avalanche Energy ⁽²⁾	36	mJ
P_D	Power Dissipation	$T_C = 25^\circ\text{C}$	25
$R_{\theta JC}$	Thermal Resistance, Junction to Case	5	$^\circ\text{C/W}$
T_J, T_{STG}	Junction & Storage Temperature Range	-55 to 150	$^\circ\text{C}$

Electrical Characteristics ($T_J = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
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Off Characteristics

$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$I_D = 250\mu\text{A}$, $V_{GS} = 0\text{V}$	12	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 12\text{V}$, $V_{GS} = 0\text{V}$	-	-	1.0	μA
I_{GSS}	Gate-Body Leakage Current	$V_{DS} = 0\text{V}$, $V_{GS} = \pm 12\text{V}$	-	-	± 100	nA

On Characteristics

$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}$, $I_D = 250\mu\text{A}$	0.4	0.7	1	V
$R_{DS(ON)}$	Static Drain-Source ON-Resistance ⁽³⁾	$V_{GS} = 4.5\text{V}$, $I_D = 10\text{A}$	-	4.4	5.7	mΩ
		$V_{GS} = 2.5\text{V}$, $I_D = 7\text{A}$	-	6.2	8.1	mΩ

Dynamic Characteristics

C_{iss}	Input Capacitance	$V_{GS} = 0\text{V}$, $V_{DS} = 6\text{V}$, $f = 1\text{MHz}$	-	1632	-	pF
C_{oss}	Output Capacitance		-	350	-	pF
C_{rss}	Reverse Transfer Capacitance		-	296	-	pF
Q_g	Total Gate Charge	$V_{GS} = 0$ to 4.5V $V_{DS} = 10\text{V}$, $I_D = 3\text{A}$	-	32	-	nC
Q_{gs}	Gate Source Charge		-	9	-	nC
Q_{gd}	Gate Drain("Miller") Charge		-	12	-	nC

Switching Characteristics

$t_{d(on)}$	Turn-On DelayTime	$V_{GS} = 4.5\text{V}$, $V_{DD} = 10\text{V}$ $I_D = 3\text{A}$, $R_{GEN} = 3\Omega$	-	0.5	-	ns
t_r	Turn-On Rise Time		-	1.3	-	ns
$t_{d(off)}$	Turn-Off DelayTime		-	3.3	-	ns
t_f	Turn-Off Fall Time		-	3.2	-	ns

Drain-Source Diode Characteristics and Max Ratings

I_S	Maximum Continuous Drain to Source Diode Forward Current		-	-	50	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	200	A
V_{SD}	Drain to Source Diode Forward Voltage	$V_{GS} = 0\text{V}$, $I_S = 10\text{A}$	-	-	1.2	V

- Notes:
1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
 2. E_{AS} condition: Starting $T_J = 25^\circ\text{C}$, $V_{DD} = 6\text{V}$, $V_G = 10\text{V}$, $R_G = 25\Omega$, $L = 0.5\text{mH}$, $I_{AS} = 12\text{A}$
 3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 0.5\%$.

Test Circuit

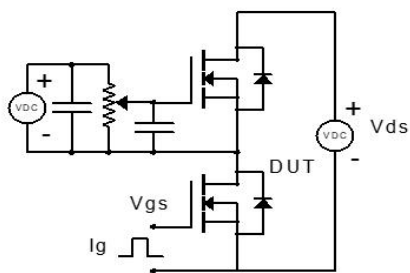


Figure 1: Gate Charge Test Circuit & Waveform

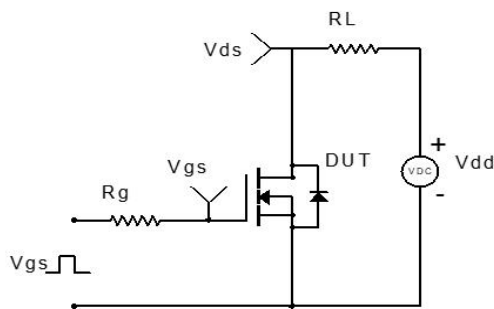


Figure 2: Resistive Switching Test Circuit & Waveform

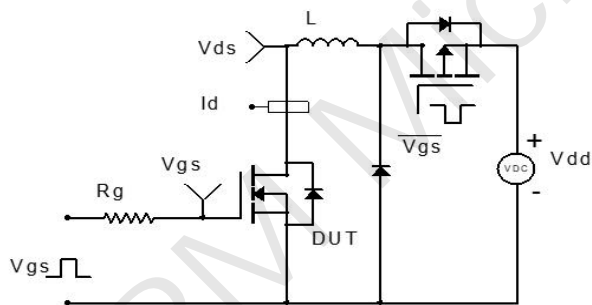


Figure 3: Unclamped Inductive Switching Test Circuit & Waveform

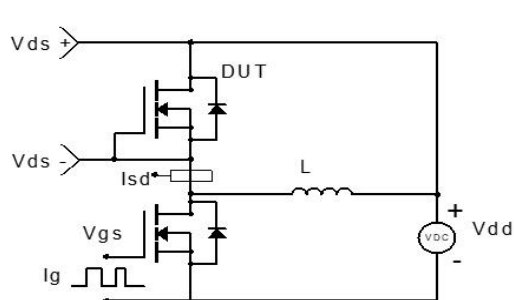
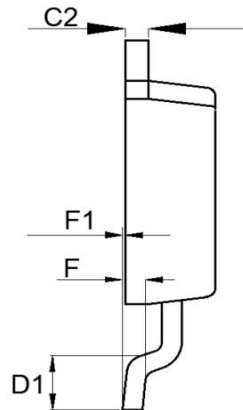
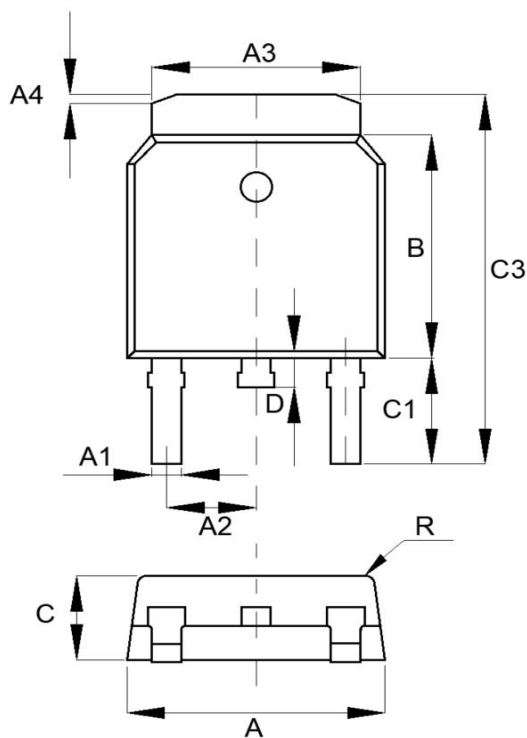


Figure 4: Diode Recovery Test Circuit & Waveform

Package Mechanical Data(TO-252-3L)




SYMBOL	MIN	NOM	MAX
A	6.550	6.600	6.650
A1	0.640	0.690	0.740
A2	—	2.286	—
A3	5.234	5.334	5.434
A4	0.070	0.270	0.470
B	6.050	6.100	6.150
C	2.250	2.300	2.350
C1	2.650	2.780	2.950
C2	0.504	0.508	0.510
C3	9.750	9.850	10.00
D	0.700	0.800	0.900
D1	1.400	1.500	1.600
F	—	0.508	—
F1	0	0.050	0.100
R	—	0.250	—

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