CRMKTL0317A

N-Channel 30V, 14.4mΩ Typ. Power MOSFET

Description

Features

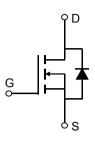
• 30V, 25A

$$R_{DS(ON)}$$
 Typ = 14.4m Ω @ V_{GS} = 10V
 $R_{DS(ON)}$ Typ = 22.5m Ω @ V_{GS} = 4.5V

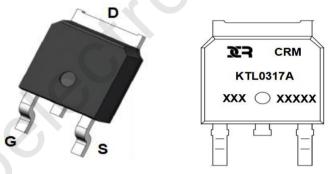
- Advanced Trench Technology
- Excellent R_{DS(ON)} and Low Gate Charge
- 100% UIS TESTED!
- 100% ΔVds TESTED!

Application

- Load Switch
- PWM Application
- Power Management







Marking and Pin Assignment

Package Marking and Ordering Information

Device	Marking	Package	Outline	Reel Size	Reel (pcs)	Per Carton (pcs)
CRMKTL0317A	CRMKTL0317A	TO-252-3L	TAPING	13"	2500	25000

Absolute Maximum Ratings (@ T_J = 25°C unless otherwise specified)

Symbol	Parameter		Value	Units
V_{DS}	Drain-to-Source Voltage		30	V
V_{GS}	Gate-to-Source Voltage		±20	V
	Continuous Drain Current	T _C = 25°C 25	25	Α
I _D	Continuous Drain Current	T _C = 100°C	15	Α
I _{DM}	Pulsed Drain Current (1)		100	Α
E _{AS}	Single Pulsed Avalanche Energy (2)		16	mJ
P_{D}	Power Dissipation	T _C = 25°C	19.8	W
$R_{\theta JC}$	Thermal Resistance, Junction to Case		6.3	°C/W
T_J,T_STG	Junction & Storage Temperature Range		-55 to 150	°C

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Electrical Characteristics (T_J = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Off Chara	acteristics					
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0 V$	30	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 30V, V_{GS} = 0V$	-	-	1.0	μΑ
I _{GSS}	Gate-Body Leakage Current	$V_{DS} = 0V, V_{GS} = \pm 20V$	-	-	±100	nA
On Chara	acteristics					
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1	1.5	2	V
R _{DS(ON)}	Static Drain-Source ON-Resistance ⁽³⁾	$V_{GS} = 10V, I_{D} = 8A$	-	14.4	18.7	mΩ
		$V_{GS} = 4.5V, I_{D} = 5A$	-	22.5	29.3	mΩ
Dynamic	Characteristics					
C_{iss}	Input Capacitance			510	-	pF
C_{oss}	Output Capacitance	$V_{GS} = 0V$, $V_{DS} = 15V$, f = 1MHz	-	61	-	pF
C_{rss}	Reverse Transfer Capacitance	1 - 1101112		51	-	pF
Q_g	Total Gate Charge		<u></u>	10	-	nC
Q_gs	Gate Source Charge	$V_{GS} = 0 \text{ to } 10V$ $V_{DS} = 15V, I_{D} = 3A$	-	2	-	nC
Q_{gd}	Gate Drain("Miller") Charge	VDS = 10 V, 10 = 0/1	-	2	-	nC
Switchin	g Characteristics					
$t_{d(on)}$	Turn-On DelayTime		-	4	-	ns
t_r	Turn-On Rise Time	$V_{GS} = 10V, V_{DD} = 15V$	-	6	-	ns
$t_{\text{d(off)}}$	Turn-Off DelayTime	$I_D = 3A$, $R_{GEN} = 3\Omega$	-	12	-	ns
t_{f}	Turn-Off Fall Time		-	3	-	ns
Drain-So	urce Diode Characteristics and M	Max Ratings				
Is	Maximum Continuous Drain to Source Diode Forward Current		-	-	25	Α
I _{SM}	Maximum Pulsed Drain to Source Diode	Forward Current	-	-	100	Α
V_{SD}	Drain to Source Diode Forward Voltage	$V_{GS} = 0V$, $I_S = 2A$	-	-	1.2	V
trr	Body Diode Reverse Recovery Time	I = 3A di/dt = 100A/us	-	8	-	ns
Qrr	Body Diode Reverse Recovery Charge	$I_F = 3A$, di/dt = 100A/us	-	2	-	nC

Notes:

^{1.} Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.

^{2.} E_{AS} condition: Starting T_J =25°C, V_{DD} =15V, V_G =10V, R_G =25ohm, L=0.5mH, I_{AS} =8A

^{3.} Pulse Test: Pulse Width≤300µs, Duty Cycle≤0.5%.

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Test Circuit

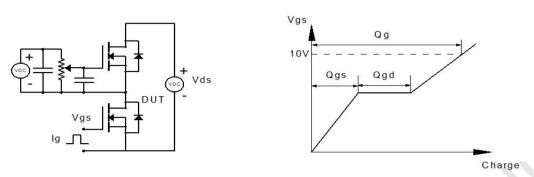


Figure 1: Gate Charge Test Circuit & Waveform

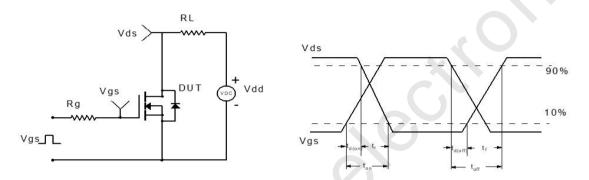


Figure 2: Resistive Switching Test Circuit & Waveform

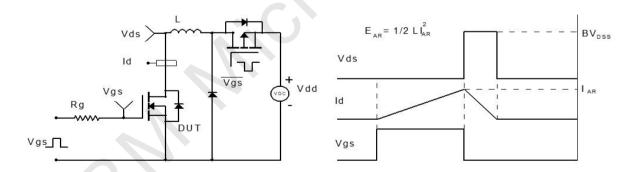


Figure 3: Unclamped Inductive Switching Test Circuit& Waveform

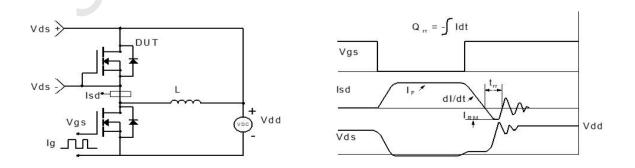
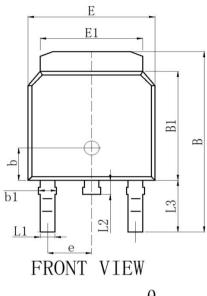


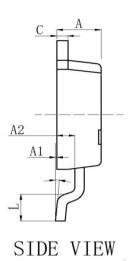
Figure 4: Diode Recovery Test Circuit & Waveform

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Package Mechanical Data(TO-252-3L)





MIN	NOM	MAX	
2. 20	2. 30	2.40	
0.00	-	0.10	
0.95	1.00	1.05	
0. 508REF			
1.40	1.50	1.60	
6.50	6.60	6. 70	
5. 20	5. 30	5. 40	
9. 90	10. 10	10.30	
6.00	6. 10	6. 20	
1. 70	1.80	1.90	
1. 00MAX			
0.60	0.75	0. 90	
0.70	0.90		
2. 95REF			
2. 286BSC			
7°			
	2. 20 0. 00 0. 95 1. 40 6. 50 5. 20 9. 90 6. 00 1. 70 0. 60 0. 70	2. 20 2. 30 0. 00 - 0. 95 1. 00 0. 508RE 1. 40 1. 50 6. 50 6. 60 5. 20 5. 30 9. 90 10. 10 6. 00 6. 10 1. 70 1. 80 1. 00MAY 0. 60 0. 75 0. 70 0. 90 2. 95REF 2. 286BSC	

BOTTOM VIEW

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