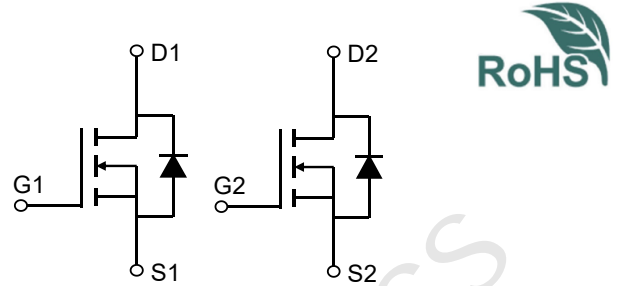


### Description

### Features

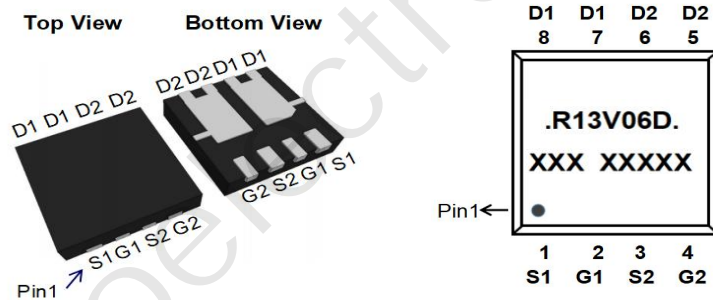
- 60V, 24A
- $R_{DS(ON)}$  Typ = 13.5mΩ @  $V_{GS} = 10V$
- $R_{DS(ON)}$  Typ = 17.5mΩ @  $V_{GS} = 4.5V$
- Advanced Split Gate Trench Technology
- Excellent  $R_{DS(ON)}$  and Low Gate Charge
- Lead Free
- 100% UIS TESTED!
- 100%  $\Delta V_d$ s TESTED!



Schematic Diagram

### Application

- Load Switch
- PWM Application
- Power Management



Marking and Pin Assignment

### Package Marking and Ordering Information

Device	Marking	Package	Outline	Reel Size	Reel (pcs)	Per Carton (pcs)
CRMHXR13V06D	R13V06D	DFN3.3x3.3-8L-D	TAPING	13"	5000	50000

### Absolute Maximum Ratings (@ $T_J = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Value	Units
$V_{DS}$	Drain-to-Source Voltage	60	V
$V_{GS}$	Gate-to-Source Voltage	$\pm 20$	V
$I_D$	Continuous Drain Current	$T_C = 25^\circ\text{C}$	24
		$T_C = 100^\circ\text{C}$	14.4
$I_{DM}$	Pulsed Drain Current <sup>(1)</sup>	96	A
$E_{AS}$	Single Pulsed Avalanche Energy <sup>(2)</sup>	27.5	mJ
$P_D$	Power Dissipation	$T_C = 25^\circ\text{C}$	19.8
$R_{\theta JC}$	Thermal Resistance, Junction to Case	6.3	$^\circ\text{C/W}$
$T_J, T_{STG}$	Junction & Storage Temperature Range	-55 to 150	$^\circ\text{C}$

### Electrical Characteristics ( $T_J = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
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#### Off Characteristics

$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$I_D = 250\mu\text{A}$ , $V_{GS} = 0\text{V}$	60	-	-	V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS} = 60\text{V}$ , $V_{GS} = 0\text{V}$	-	-	1.0	$\mu\text{A}$
$I_{GSS}$	Gate-Body Leakage Current	$V_{DS} = 0\text{V}$ , $V_{GS} = \pm 20\text{V}$	-	-	$\pm 100$	nA

#### On Characteristics

$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}$ , $I_D = 250\mu\text{A}$	1.0	1.3	1.8	V
$R_{DS(ON)}$	Static Drain-Source ON-Resistance <sup>(3)</sup>	$V_{GS} = 10\text{V}$ , $I_D = 20\text{A}$	-	13.5	18	mΩ
		$V_{GS} = 4.5\text{V}$ , $I_D = 10\text{A}$	-	17.5	23	mΩ

#### Dynamic Characteristics

$C_{iss}$	Input Capacitance	$V_{GS} = 0\text{V}$ , $V_{DS} = 30\text{V}$ , $f = 1\text{MHz}$	-	593	-	pF
$C_{oss}$	Output Capacitance		-	183	-	pF
$C_{rss}$	Reverse Transfer Capacitance		-	4	-	pF
$Q_g$	Total Gate Charge	$V_{GS} = 0$ to $10\text{V}$ $V_{DS} = 30\text{V}$ , $I_D = 20\text{A}$	-	13.9	-	nC
$Q_{gs}$	Gate Source Charge		-	1.6	-	nC
$Q_{gd}$	Gate Drain("Miller") Charge		-	3.1	-	nC

#### Switching Characteristics

$t_{d(on)}$	Turn-On DelayTime	$V_{GS} = 10\text{V}$ , $V_{DD} = 30\text{V}$ $I_D = 20\text{A}$ , $R_{GEN} = 6\Omega$	-	3.7	-	ns
$t_r$	Turn-On Rise Time		-	4.3	-	ns
$t_{d(off)}$	Turn-Off DelayTime		-	16.2	-	ns
$t_f$	Turn-Off Fall Time		-	6.5	-	ns

#### Drain-Source Diode Characteristics and Max Ratings

$I_S$	Maximum Continuous Drain to Source Diode Forward Current	$V_{GS} = 0\text{V}$ , $I_S = 20\text{A}$	-	-	24	A
$I_{SM}$	Maximum Pulsed Drain to Source Diode Forward Current		-	-	96	A
$V_{SD}$	Drain to Source Diode Forward Voltage		-	-	1.2	V
$t_{rr}$	Body Diode Reverse Recovery Time		-	24	-	ns
$Q_{rr}$	Body Diode Reverse Recovery Charge		-	9.3	-	nC

- Notes:
1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
  2.  $E_{AS}$  condition: Starting  $T_J = 25^\circ\text{C}$ ,  $V_{DD} = 30\text{V}$ ,  $V_G = 10\text{V}$ ,  $R_G = 25\Omega$ ,  $L = 0.5\text{mH}$ ,  $I_{AS} = 10.5\text{A}$
  3. Pulse Test: Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 0.5\%$ .

## Typical Performance Characteristics

Figure 1: Output Characteristics

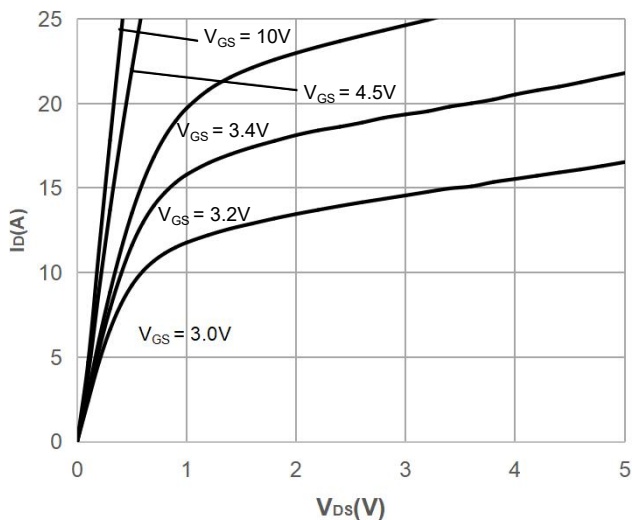


Figure 2: Typical Transfer Characteristics

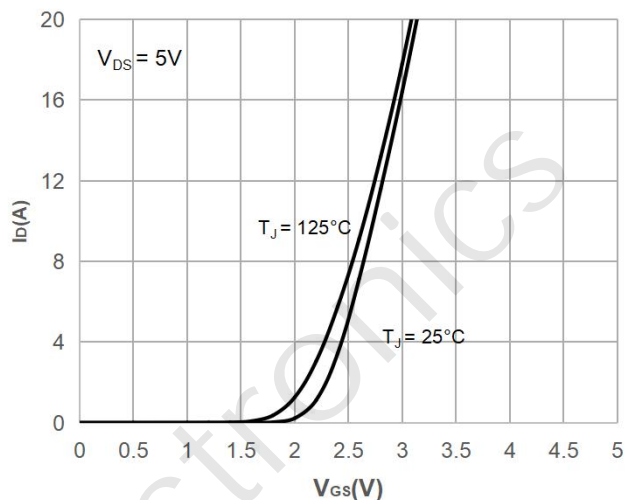


Figure 3: On-resistance vs. Drain Current

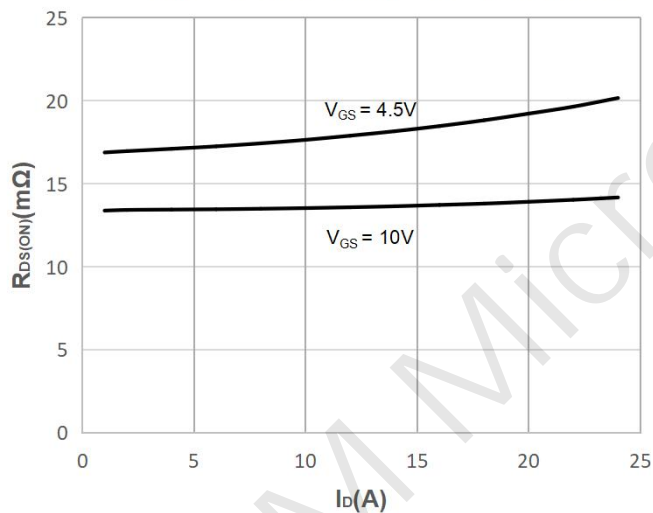


Figure 4: Body Diode Characteristics

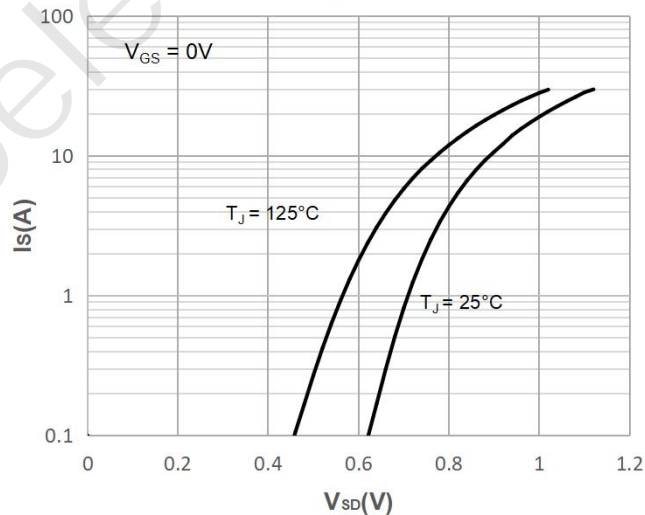


Figure 5: Gate Charge Characteristics

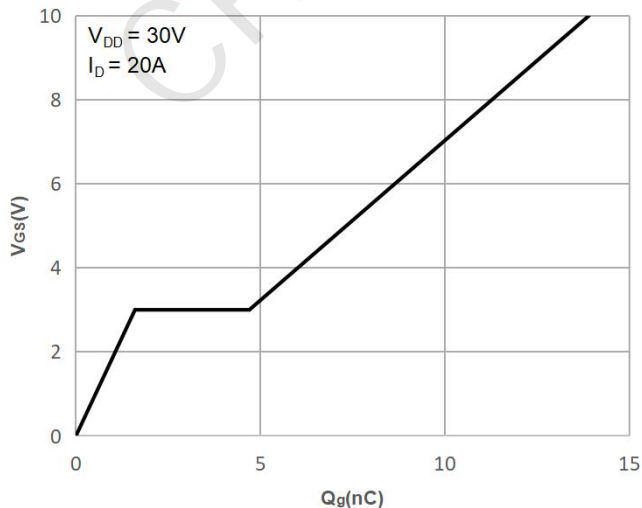
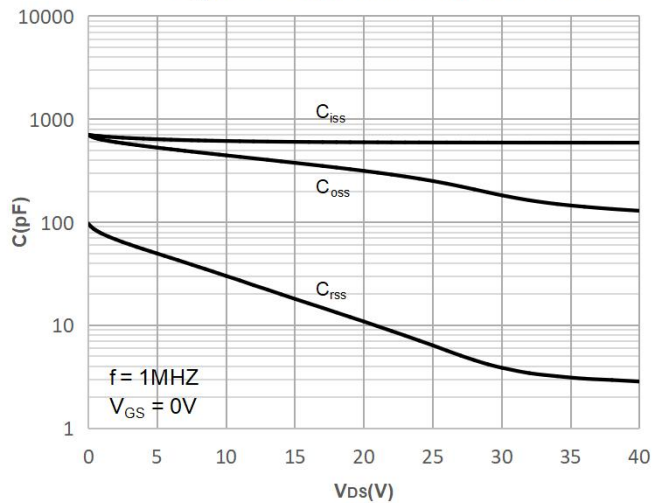


Figure 6: Capacitance Characteristics



## Typical Performance Characteristics

Figure 7: Normalized Breakdown voltage vs. Junction Temperature

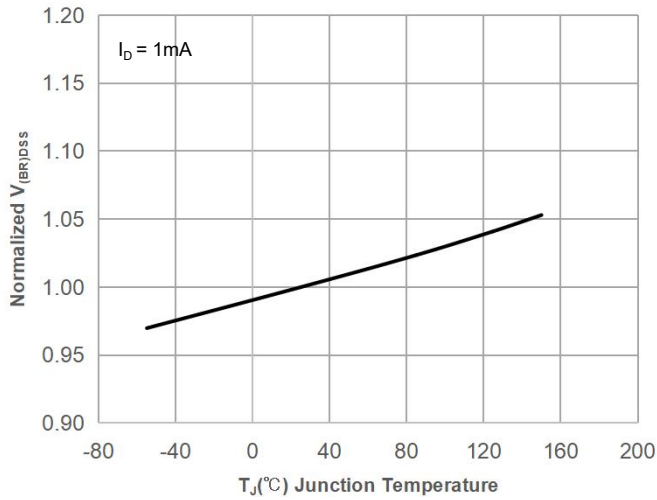


Figure 8: Normalized on Resistance vs. Junction Temperature

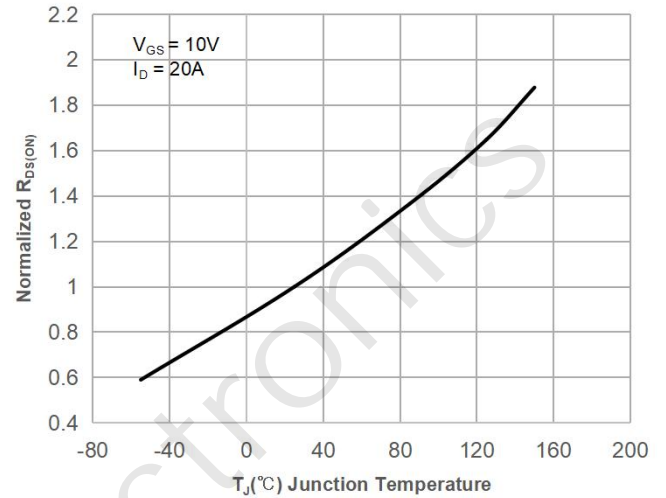


Figure 9: Maximum Safe Operating Area

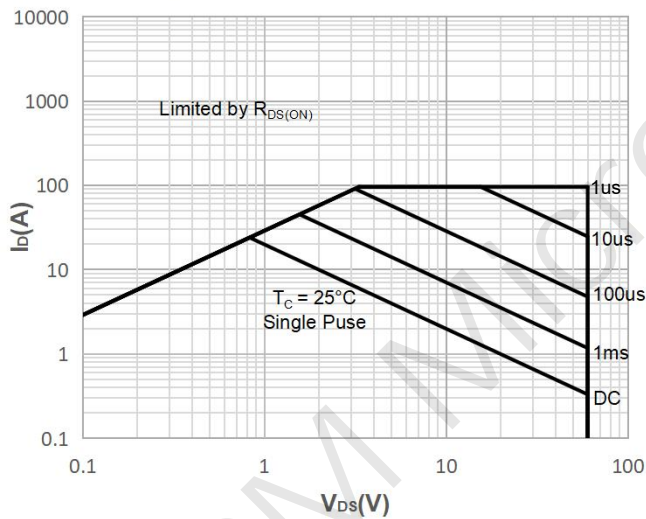


Figure 10: Maximum Continuous Drain Current vs. Case Temperature

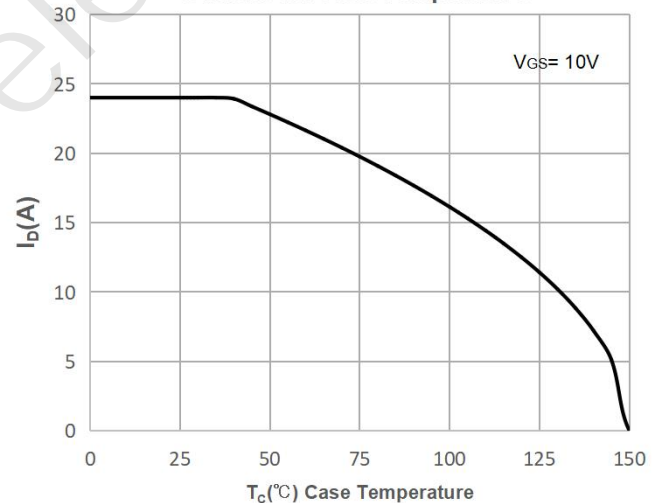


Figure 11: Normalized Maximum Transient Thermal Impedance

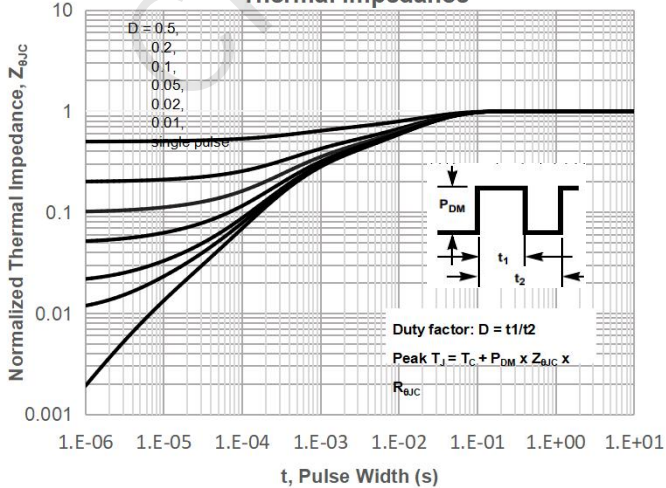
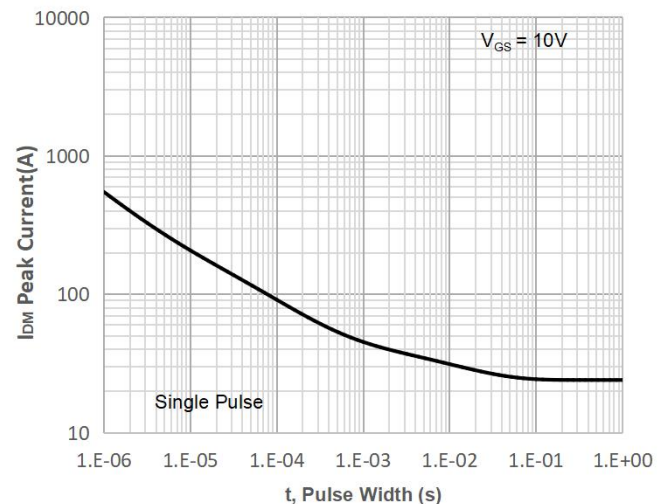


Figure 12: Peak Current Capacity



## Test Circuit



Figure 1: Gate Charge Test Circuit & Waveform



Figure 2: Resistive Switching Test Circuit & Waveform

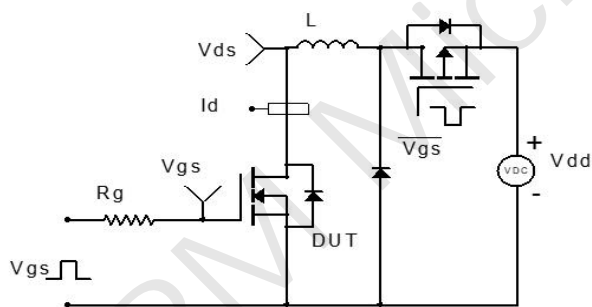
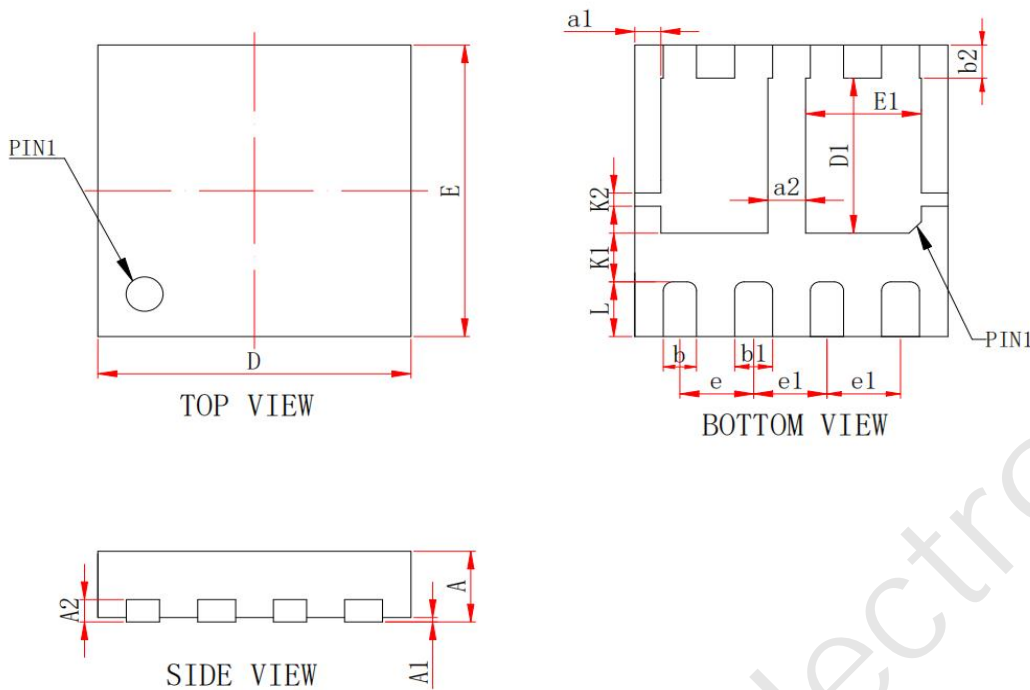


Figure 3: Unclamped Inductive Switching Test Circuit & Waveform



Figure 4: Diode Recovery Test Circuit & Waveform

### Package Mechanical Data(PDFN3.3x3.3-8L-D)



STMBOL	MIN	NOM	MAX
D	3.20	3.30	3.40
E	3.20	3.30	3.40
A	0.70	0.75	0.80
A1	0.00	-	0.05
A2	0.203REF		
L	0.50	0.60	0.70
b	0.30	0.35	0.40
b1	0.35	0.40	0.45
e	0.775BSC		
e1	0.725BSC		
K1	0.500BSC		
K2	0.200BSC		
b2	0.30	0.35	0.40
E1	0.10	1.15	1.20
D1	1.70	1.75	1.80
a1	0.30	0.35	0.40
a2	0.30	0.35	0.40

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