CRMEEH0606A

P-Channel -60V, 7.3mΩ Typ. Power MOSFET

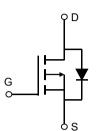
Description

Features

• -60V, -95A

 $R_{DS(ON)}$ Typ = 7.3m Ω @ V_{GS} = -10V

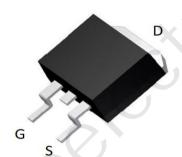
- Advanced Split Gate Trench Technology
- Excellent R_{DS(ON)} and Low Gate Charge
- 100% UIS TESTED!
- 100% ΔVds TESTED!

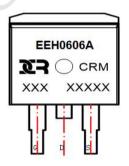


Schematic Diagram

Application

- Load Switch
- PWM Application
- Power Management





Marking and Pin Assignment

Package Marking and Ordering Information

Device	Marking	Package	Outline	Reel Size	Reel (pcs)	Per Carton (pcs)
CRMEEH0606A	CRMEEH0606A	TO-263-3L	TAPING	13"	800	4000

Absolute Maximum Ratings (@ T_J = 25°C unless otherwise specified)

Symbol	Parameter		Value	Units
V_{DS}	Drain-to-Source Voltage		-60	V
V _{GS}	Gate-to-Source Voltage		±20	V
	Continuous Drain Current	T _C = 25°C	-95	А
I _D	Continuous Drain Current	T _C = 100°C	-57	А
I _{DM}	Pulsed Drain Current (1)		-380	А
E _{AS}	Single Pulsed Avalanche Energy (2)		333	mJ
P_{D}	Power Dissipation	T _C = 25°C	146	W
$R_{ hetaJC}$	Thermal Resistance, Junction to Case		0.85	°C/W
T_{J} , T_{STG}	Junction & Storage Temperature Range		-55 to 150	°C



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Electrical Characteristics (T_J = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Off Chara	acteristics					
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$I_D = -250 \mu A, V_{GS} = 0 V$	-60	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = -60V, V_{GS} = 0V$	-	-	-1.0	μΑ
I _{GSS}	Gate-Body Leakage Current	$V_{DS} = 0V, V_{GS} = \pm 20V$	-	-	±100	nA
On Chara	acteristics				6	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	-2.4	-3	-3.6	V
R _{DS(ON)}	Static Drain-Source ON-Resistance ⁽³⁾	$V_{GS} = -10V, I_D = -30A$	-	7.3	9.5	mΩ
Dynamic	Characteristics					
C _{iss}	Input Capacitance		- /	3220	-	pF
C_{oss}	Output Capacitance	$V_{GS} = 0V, V_{DS} = -30V,$ f = 1MHz	-	1020	-	pF
C_{rss}	Reverse Transfer Capacitance	1 - 1101112	X -	60	-	pF
Q_g	Total Gate Charge			46.5	-	nC
Q_{gs}	Gate Source Charge	$V_{GS} = 0 \text{ to } -10V$ $V_{DS} = -30V, I_{D} = -20A$) .	9.5	-	nC
Q_{gd}	Gate Drain("Miller") Charge	V _{DS} 30V, I _D 20A	-	22	-	nC
Switchin	g Characteristics					
$t_{d(on)}$	Turn-On DelayTime		-	4.3	-	ns
t_r	Turn-On Rise Time	$V_{GS} = -10V, V_{DD} = -30V$	-	2.8	-	ns
$t_{\text{d(off)}}$	Turn-Off DelayTime	I_D = -20A, R_{GEN} = 3Ω	-	16.5	-	ns
t_{f}	Turn-Off Fall Time		-	6	-	ns
Drain-So	urce Diode Characteristics and M	Max Ratings				
I _S	Maximum Continuous Drain to Source Di	ode Forward Current	-	-	-95	Α
I _{SM}	Maximum Pulsed Drain to Source Diode	Forward Current	-	-	-380	Α
V_{SD}	Drain to Source Diode Forward Voltage	$V_{GS} = 0V, I_{S} = -30A$	-	-	-1.2	V
trr	Body Diode Reverse Recovery Time	1 004 177 40047	-	65	-	ns
Qrr	Body Diode Reverse Recovery Charge	$I_F = -20A$, di/dt = 100A/us	-	120	-	nC

Notes:

^{1.} Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.

^{2.} E_{AS} condition: Starting T_J =25°C, V_{DD} =-30V, V_G =-10V, R_G =25ohm, L=0.5mH, I_{AS} =-36.5A

^{3.} Pulse Test: Pulse Width $\!\!\leqslant\! 300\mu s,$ Duty Cycle $\!\!\leqslant\! 0.5\%.$

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Test Circuit

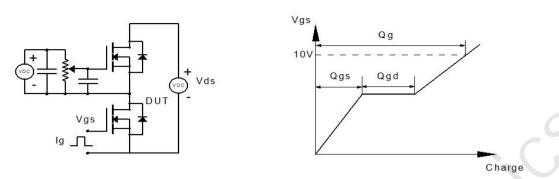


Figure 1: Gate Charge Test Circuit & Waveform

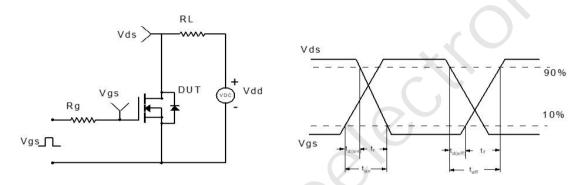


Figure 2: Resistive Switching Test Circuit & Waveform

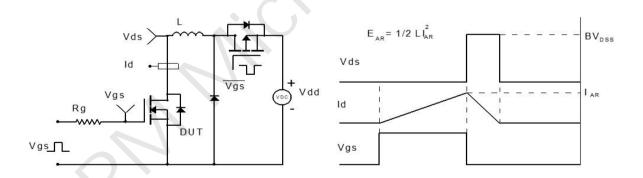


Figure 3: Unclamped Inductive Switching Test Circuit& Waveform

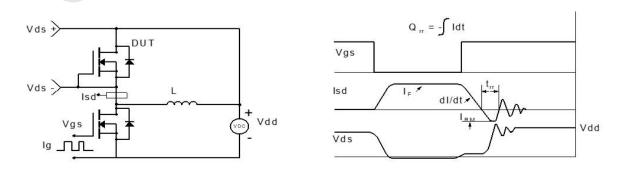
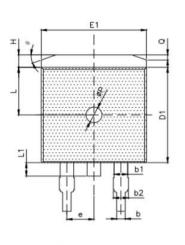


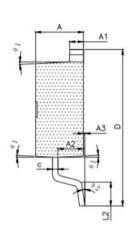
Figure 4: Diode Recovery Test Circuit & Waveform

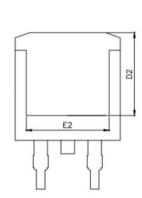
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Package Mechanical Data(TO-263-3L)







SYMBOL	MILLIMETER				
01111000	MIN	NOM	MAX		
Α	4.40	4.50	4.60		
A1	1.20	1.30	1.40		
A2	2.30	2.40	2.50		
A3	0.03	0.13	0.23		
b	0.70	0.80	0.90		
b1	1.21	1.27	1.40		
b2	1.25	1.35	1.45		
С	0.40		0.60		
D	14.80	15.10	15.40		
D1	9.10	9.20	9.30		
D2	8.00				
E	9.70	9.90	10.20		
E1	9.68	9.88	10.08		
E2	7.80				
е	2.54 (BSC)				
Н	1.00	1.20	1.40		
L	L 4.30		4.90		
L1	1.10 1.		1.50		
L2	2 2.10 2		2.50		
ØΡ	1.40 1.50		1.60		
Q	0.50 (REF)				
θ	16"	20°	24*		
91	1*	3*	5*		
θ2	0.	_	9,		

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