CRMCTH2020A

N-Channel 200V,17.8mΩ Typ. Power MOSFET

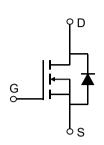
Description

Features

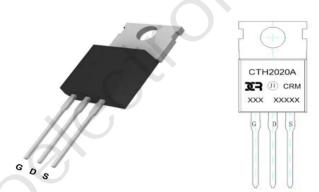
• 200V, 65A

 $R_{DS(ON)}$ Typ = 17.8m Ω @ V_{GS} = 10V Advanced Trench Technology

- Excellent R_{DS(ON)} and Low Gate Charge
- 100% UIS TESTED!
- 100% ΔVds TESTED!







Marking and Pin Assignment

Application

- Load Switch
- PWM Application
- Power Management

Package Marking and Ordering Information

Device	Marking	Package	Outline	TUBE(pcs)	Inner Box (pcs)	Per Carton (pcs)
CRMCTH2020A	CRMCTH2020A	TO-220C-3L	TUBE	50	1000	5000

Absolute Maximum Ratings (@ T_J = 25°C unless otherwise specified)

Symbol	Parameter		Value	Units
V_{DS}	Drain-to-Source Voltage		200	V
V_{GS}	Gate-to-Source Voltage		±20	V
	Continuous Drain Current	T _C = 25°C	65	Α
I _D		T _C = 100°C	39	Α
I _{DM}	Pulsed Drain Current (1)		260	Α
E _{AS}	Single Pulsed Avalanche Energy (2)		911	mJ
P_{D}	Power Dissipation	T _C = 25°C	223	W
$R_{ heta JC}$	Thermal Resistance, Junction to Case		0.56	°C/W
T_J, T_STG	Junction & Storage Temperature Range		-55 to 150	°C



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Electrical Characteristics (T_J = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Off Chara	acteristics					
V _{(BR)DSS}	Drain-Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0 V$	200	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 200V, V_{GS} = 0V$	-	-	1.0	μΑ
I _{GSS}	Gate-Body Leakage Current	$V_{DS} = 0V, V_{GS} = \pm 20V$	-	-	±100	nA
On Chara	acteristics				6)
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.4	3	3.6	V
R _{DS(ON)}	Static Drain-Source ON-Resistance ⁽³⁾	$V_{GS} = 10V, I_D = 30A$	-	17.8	23	mΩ
Dynamic	Characteristics					
C _{iss}	Input Capacitance		- /	12395	-	pF
C_{oss}	Output Capacitance	$V_{GS} = 0V, V_{DS} = 25V,$ f = 1MHz	-	480	-	pF
C_{rss}	Reverse Transfer Capacitance	1 - 11VII 12	X-\	429	-	pF
Q_g	Total Gate Charge	(-	300	-	nC
Q_{gs}	Gate Source Charge	$V_{GS} = 0 \text{ to } 10V$ $V_{DS} = 100V, I_{D} = 20A$	U .	25	-	nC
Q_{gd}	Gate Drain("Miller") Charge	V _{DS} - 100V, I _D -20A	-	100	-	nC
Switching	g Characteristics					
t _{d(on)}	Turn-On DelayTime		-	75	-	ns
t _r	Turn-On Rise Time	$V_{GS} = 10V, V_{DD} = 50V$	-	110	-	ns
$t_{\text{d(off)}}$	Turn-Off DelayTime	I_D = 20A, R_{GEN} = 25 Ω	-	105	-	ns
t_{f}	Turn-Off Fall Time		-	85	-	ns
Drain-So	urce Diode Characteristics and M	Max Ratings				
Is	Maximum Continuous Drain to Source D	iode Forward Current	-	-	65	Α
I _{SM}	Maximum Pulsed Drain to Source Diode	Forward Current	-	-	260	Α
V_{SD}	Drain to Source Diode Forward Voltage	$V_{GS} = 0V, I_{S} = 30A$	-	-	1.2	V
trr	Body Diode Reverse Recovery Time	1 - 004 -1:/-14 - 4004/	-	137	-	ns
Qrr	Body Diode Reverse Recovery Charge	$I_F = 20A$, di/dt = 100A/us	-	0.6	-	nC

Notes:

^{1.} Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.

^{2.} E_{AS} condition: Starting T_J =25°C, V_{DD} =50V, V_G =10V, R_G =25ohm, L=10mH, I_{AS} =13.5A

^{3.} Pulse Test: Pulse Width $\!\!\leqslant\! 300\mu s,$ Duty Cycle $\!\!\leqslant\! 0.5\%.$

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Test Circuit

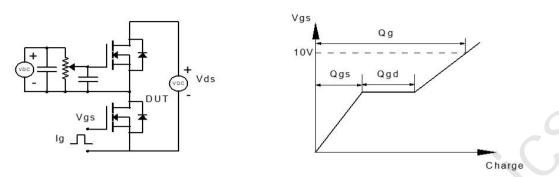


Figure 1: Gate Charge Test Circuit & Waveform

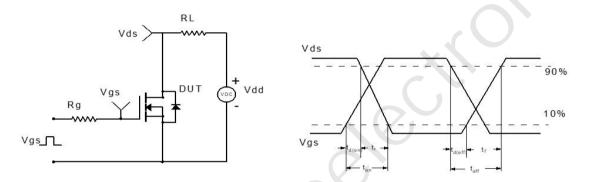


Figure 2: Resistive Switching Test Circuit & Waveform

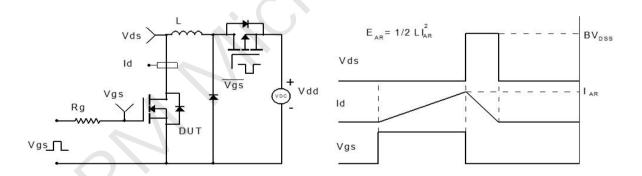


Figure 3: Unclamped Inductive Switching Test Circuit& Waveform

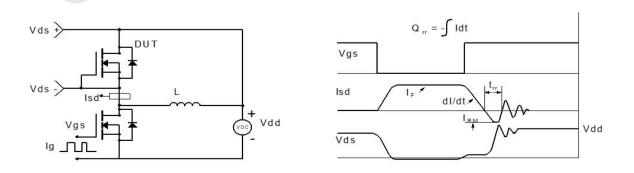
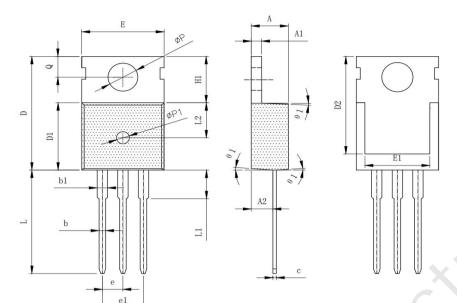


Figure 4: Diode Recovery Test Circuit & Waveform

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Package Mechanical Data(TO-220C-3L)



SYMBOL	MILLIMETER				
SIMDOL	MIN	NOM	MAX		
Α	4. 40	4. 50	4. 60		
A1	1. 25	1.30	1.35		
A2	2. 30	2.40	2.50		
b	0.70	0.80	0.90		
b1	1. 25	1.35	1.45		
c	0. 40	0. 50	0.60		
D	15. 50	15. 80	16. 10		
D1	9. 10	9. 20	9. 30		
D2	12. 73	12.83	12.93		
E	9. 70	9. 90	10. 20		
E1	7. 60	8. 00	8. 40		
е	2. 54 (BSC)				
e1	5. 08 (BSC)				
H1	6. 30	6. 50	6.80		
L	12. 75	13.08	13. 50		
Ll	-	(3. 10		
L2	4. 30	4. 60	4. 90		
ΦP	3. 50	3. 60	3. 70		
ØP1	1.40	1.50	1.60		
α	2. 70	1221	2. 90		
θ 1	2°	4°	6°		

NOTES:1. PKG SURFACE IS MATTE Ral. 2~1.4; OTHERS IS POLISHED Ra0.15;

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