## CRMCP40N20A

#### N-Channel 200V,50mΩ Typ. Power MOSFET

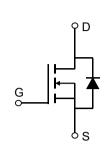
### **Description**

#### **Features**

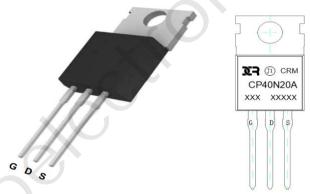
• 200V, 40A

 $R_{DS(ON)}$  Typ =  $50m\Omega$  @  $V_{GS}$  = 10V

- Fast Switching
- Improved dv/dt Capability
- 100% UIS TESTED!
- 100% ΔVds TESTED!







**Marking and Pin Assignment** 

Initial Version: 1.0

### **Application**

- Switch Mode Power Supply(SMPS)
- Uninterruptible Power Supply(UPS)

**Package Marking and Ordering Information** 

Device	Marking	Package	Outline	TUBE(pcs)	Inner Box (pcs)	Per Carton (pcs)
CRMCP40N20A	CRMCP40N20A	TO-220C-3L	TUBE	50	2000	8000

#### **Absolute Maximum Ratings** (@ T<sub>J</sub> = 25°C unless otherwise specified)

Symbol	Parameter		Value	Units
$V_{DS}$	Drain-to-Source Voltage		200	V
$V_{GS}$	Gate-to-Source Voltage		±20	V
I <sub>D</sub>	Continuous Drain Current	T <sub>C</sub> = 25°C	40	Α
		T <sub>C</sub> = 100°C	24	Α
I <sub>DM</sub>	Pulsed Drain Current (1)		160	Α
E <sub>AS</sub>	Single Pulsed Avalanche Energy (2)		484	mJ
$P_{D}$	Power Dissipation $T_C = 25^{\circ}C$		250	W
$R_{ heta JC}$	Thermal Resistance, Junction to Case		0.5	°C/W
$T_J,T_STG$	Junction & Storage Temperature Range		-55 to 150	°C



# CRMCP40N20A

## N-Channel 200V,50m $\Omega$ Typ. Power MOSFET

### **Electrical Characteristics** (T<sub>J</sub> = 25°C unless otherwise specified)

Cumala al	Damamatan	Conditions	, N#:	T	Mark	l limit
Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Off Chara	acteristics					
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0V$	200	-	-	V
$I_{\rm DSS}$	Zero Gate Voltage Drain Current	$V_{DS} = 200V, V_{GS} = 0V$	-	-	1.0	μΑ
$I_{\rm GSS}$	Gate-Body Leakage Current	$V_{DS} = 0V, V_{GS} = \pm 20V$	-	-	±100	nA
On Chara	acteristics				6	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2	2.8	4	V
R <sub>DS(ON)</sub>	Static Drain-Source ON-Resistance <sup>(3)</sup>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 20A	-	50	65	mΩ
Dynamic	Characteristics					
C <sub>iss</sub>	Input Capacitance		- /	2894	-	pF
$C_{oss}$	Output Capacitance	$V_{GS} = 0V, V_{DS} = 25V,$ f = 1MHz	-	333	-	pF
$C_{rss}$	Reverse Transfer Capacitance	I – TIVIMZ	X -\	95	-	pF
$Q_g$	Total Gate Charge		-	112	-	nC
$Q_gs$	Gate Source Charge	$V_{GS} = 0 \text{ to } 10V$	<b>)</b> .	11	-	nC
$Q_{gd}$	Gate Drain("Miller") Charge	$V_{DS} = 160V, I_{D} = 40A$	-	51	-	nC
Switchin	g Characteristics					
t <sub>d(on)</sub>	Turn-On DelayTime		-	30	-	ns
t <sub>r</sub>	Turn-On Rise Time	$V_{GS} = 15V, V_{DD} = 100V$	-	40	-	ns
$t_{d(off)}$	Turn-Off DelayTime	$I_D = 40A$ , $R_{GEN} = 5\Omega$	-	480	-	ns
$t_{f}$	Turn-Off Fall Time		-	110	-	ns
Drain-So	urce Diode Characteristics and M	Max Ratings				
I <sub>S</sub>	Maximum Continuous Drain to Source Diode Forward Current		-	-	40	Α
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode Forward Current		-	-	160	Α
V <sub>SD</sub>	Drain to Source Diode Forward Voltage	V <sub>GS</sub> = 0V, I <sub>S</sub> = 20A	-	-	1.2	V
trr	Body Diode Reverse Recovery Time		-	1.45	-	ns
Qrr	Body Diode Reverse Recovery Charge	$I_F = 20A$ , di/dt = 100A/us	-	1.1	-	nC
	, ,					

Notes:

<sup>1.</sup> Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.

<sup>2.</sup>  $E_{AS}$  condition: Starting  $T_J$ =25°C,  $V_{DD}$ =50V,  $V_G$ =10V,  $R_G$ =25ohm, L=0.5mH,  $I_{AS}$ =44A

<sup>3.</sup> Pulse Test: Pulse Width  $\!\!\leqslant\! 300\mu s,$  Duty Cycle  $\!\!\leqslant\! 0.5\%.$ 

### N-Channel 200V,50mΩ Typ. Power MOSFET

### **Test Circuit**

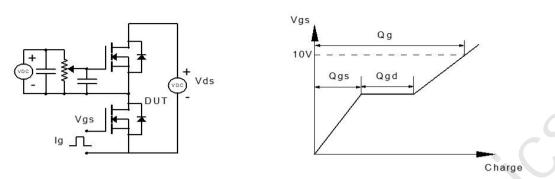


Figure 1: Gate Charge Test Circuit & Waveform

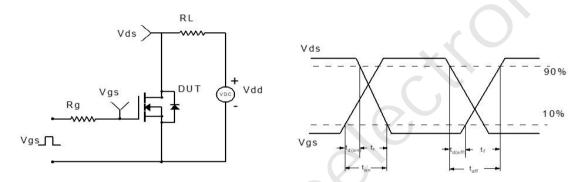


Figure 2: Resistive Switching Test Circuit & Waveform

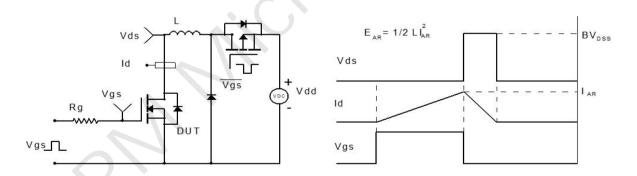


Figure 3: Unclamped Inductive Switching Test Circuit& Waveform

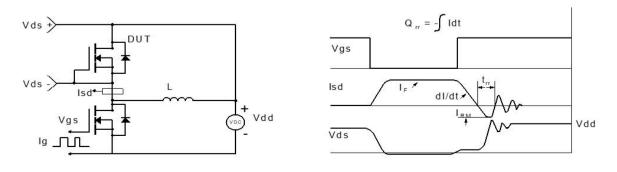
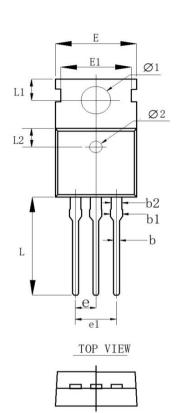


Figure 4: Diode Recovery Test Circuit & Waveform

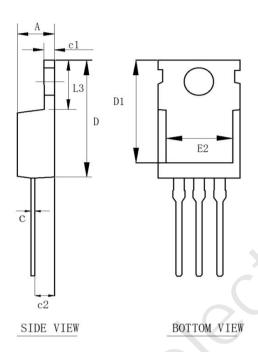
## CRMCP40N20A

N-Channel 200V,50mΩ Typ. Power MOSFET

### Package Mechanical Data(TO-220C-3L)



SIDE VIEW



		ON DIMENSIONS OF MEASURE=n		
SYMBOL	MIN	NOM NOM	MAX	
A	4. 30	4. 50	4. 70	
b	0. 70	0.80	0. 90	
b1			1. 42	
b2	1. 17	1. 27	1. 37	
С	0. 40	0. 50	0.60	
c1	1. 25	1. 30	1. 35	
c2	2. 20	2. 40	2. 60	
D	15. 45	15. 65	15. 85	
D1	13. 20	13. 40	13. 60	
Е	9. 80	10.00	10. 20	
E1	8.60	8. 70	8.80	
E2	7. 80	8. 00	8. 20	
e1	4.88	5. 08	5. 28	
L	12. 95	13. 15	13. 35	
L1	2.70	2.80	2.90	
L2	2.40	2. 50	2.60	
L3	6.30	6. 50	6.70	
Ø1	3. 50	3. 60	3. 70	
Ø2	1. 35	1. 50	1. 65	
е	2. 54BSC			

# **Important Notice**

The information presented in datasheets is for reference only. CRM reserves the right to make changes at any time to any products or information herein, without notice.

Customers are responsible for the design and applications, including compliance with all laws, regulations and safety requirements or standards.

"Typical" parameters which provided in datasheets can vary in different applications and actual performance may vary over time. Customers are responsible for doing all necessary testing to minimize the risks associated with their applications and products.

is a registered trademark of Wuxi CRM Microelectronics Co. , Ltd. Copyright ©2023 CRM Microelectronics Co. , Ltd. All rights reserved.

### **Contact information**

For more information, please visit: http://www.crm-semi.tech For sales information, please send an email to: sales@crm-semi.com