

# CPWR-0600S001 - Silicon Carbide Schottky Diode Chip

ZERO RECOVERY® RECTIFIER

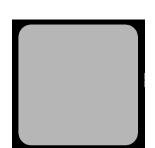
 $V_{RRM} = 600 \text{ V}$ 

 $\mathbf{I}_{\mathsf{F}(\mathsf{AVG})} = 1 \, \mathsf{A}$ 

 $Q_c = 3.3 \text{ nC}$ 

**Features** 

- 600-Volt Schottky Rectifier
- Zero Reverse Recovery
- Zero Forward Recovery
- High-Frequency Operation
- Temperature-Independent Switching Behavior
- Extremely Fast Switching
- Positive Temperature Coefficient on V<sub>E</sub>



**Chip Outline** 

Part Number	Anode	Cathode	Package	Marking
CPWR-0600S001B	Al	Ni/Ag	Sawn on Foil	Wafer # on Foil

# **Maximum Ratings**

Symbol	Parameter	Value	Unit	Test Conditions	Note
V <sub>RRM</sub>	Repetitive Peak Reverse Voltage	600	V		
$V_{\scriptscriptstyle{RSM}}$	Surge Peak Reverse Voltage	600	V		
V <sub>DC</sub>	DC Blocking Voltage	600	V		
$I_{\text{F(AVG)}}$	Average Forward Current	1	А	T <sub>3</sub> =175°C	
$\boldsymbol{I}_{\text{FRM}}$	Repetitive Peak Forward Surge Current	5	А	$T_c$ =25°C, $t_p$ =8.3 ms, Half Sine Wave	1
$\mathbf{I}_{FSM}$	Non-Repetitive Peak Forward Surge Current	20	А	$T_c$ =25°C, $t_p$ =10 µs, Pulse	1
$T_{\scriptscriptstyle \mathrm{J}}$ , $T_{\scriptscriptstyle \mathrm{stg}}$	Operating Junction and Storage Temperature	-55 to +175	°C		



# **Electrical Characteristics**

Symbol	Parameter	Тур.	Max.	Unit	Test Conditions	Note
V <sub>F</sub>	Forward Voltage	1.6 2.0	1.8 2.4	V	$I_F = 1 \text{ A } T_J = 25^{\circ}\text{C}$ $I_F = 1 \text{ A } T_J = 175^{\circ}\text{C}$	
I <sub>R</sub>	Reverse Current	2 20 40	5 100 500	μΑ	$V_R = 450 \text{ V } T_J = 25^{\circ}\text{C}$ $V_R = 600 \text{ V } T_J = 25^{\circ}\text{C}$ $V_R = 600 \text{ V } T_J = 150^{\circ}\text{C}$	
Q <sub>c</sub>	Total Capacitive Charge	3.3		nC	$V_R = 500 \text{ V, } I_F = 1 \text{ A}$ $di/dt = 500 \text{ A/}\mu\text{s}$ $T_J = 25^{\circ}\text{C}$	
С	Total Capacitance	80 11 8.5		pF	$V_R = 0 \text{ V, } T_J = 25^{\circ}\text{C, } f = 1 \text{ MHz}$ $V_R = 200 \text{ V, } T_J = 25^{\circ}\text{C, } f = 1 \text{ MHz}$ $V_R = 400 \text{ V, } T_J = 25^{\circ}\text{C, } f = 1 \text{ MHz}$	

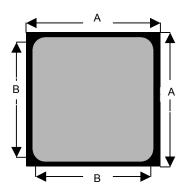
### **Mechanical Parameters**

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Parameter	Тур.	Unit				
Die Size	.84 x .84	mm				
Anode Pad Size	0.70 × 0.70	mm				
Anode Pad Opening	0.58 × 0.0.58	mm				
Thickness	377 ± 10%	μm				
Wafer Size	100	mm				
Anode Metalization (AI)	4	μm				
Cathode Metalization (Ni/Ag)	0.8	μm				
Frontside Passivation	Polyimide					

Note: 1. Assumes  $\theta$ JC Thermal Resistance of 1.8°C/W or less



## **Chip Dimensions**



symbol	dimension				
	mm	inch			
Α	0.84	0.0331			
В	0.70	0.0276			

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The die-on-tape method of delivering these SiC die may be considered a means of temporary storage only. Due to an increase in adhesion over time, die stored for an extended period may affix too strongly to the tape. These die should be stored in a temperature-controlled nitrogen dry box soon after receipt. Cree will further recommend that all die be removed from tape to a waffle pack, to a similar storage medium, or used in production within 2 – 3 weeks of delivery to assure 100% release of all die without issues.