

Ordering number : ENN7399

NPN Epitaxial Planar Silicon Composite Transistors

**CPH6519**

Low-Frequency General-Purpose Amplifier, Driver Applications

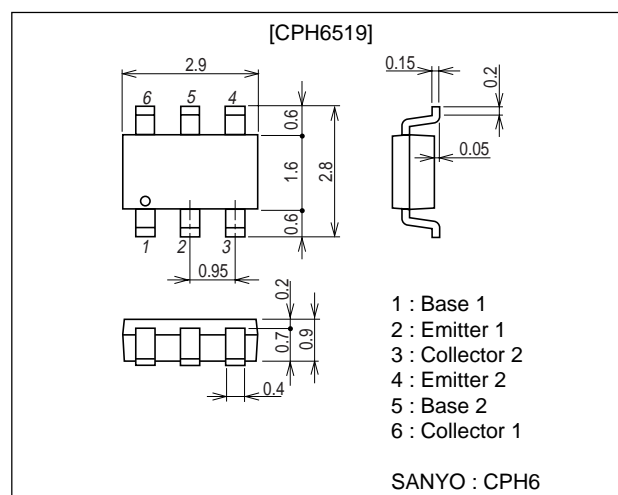
Features

- Composite type with 2 transistors contained in the CPH package currently in use, improving the mounting efficiency greatly.
- The CPH6519 is formed with two chips, being equivalent to the 2SC3689, placed in one package.
- Adoption of FBET process.
- High DC current gain ($h_{FE}=800$ to 3200).
- High V_{EBO} ($V_{EBO} \geq 15V$).
- Excellent in thermal equilibrium and pair capability.

Package Dimensions

unit : mm

2212



Specifications

Absolute Maximum Ratings at $T_a=25^\circ C$

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	V_{CB0}		60	V
Collector-to-Emitter Voltage	V_{CEO}		50	V
Emitter-to-Base Voltage	V_{EBO}		15	V
Collector Current	I_C		100	mA
Collector Current (Pulse)	I_{CP}		200	mA
Base Current	I_B		20	mA
Collector Dissipation	P_C	1unit	350	mW
Total Dissipation	P_T		500	mW
Junction Temperature	T_J		150	$^\circ C$
Storage Temperature	T_{stg}		-55 to +150	$^\circ C$

Electrical Characteristics at $T_a=25^\circ C$

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Collector Cutoff Current	I_{CB0}	$V_{CB}=40V, I_E=0$			0.1	μA
Emitter Cutoff Current	I_{EB0}	$V_{EB}=10V, I_C=0$			0.1	μA
DC Current Gain	h_{FE}	$V_{CE}=5V, I_C=10mA$	800	1500	3200	
DC Current Gain Ratio	$h_{FE}(\text{small/large})$	$V_{CE}=5V, I_C=10mA$	0.8	0.98		

Marking : 3F

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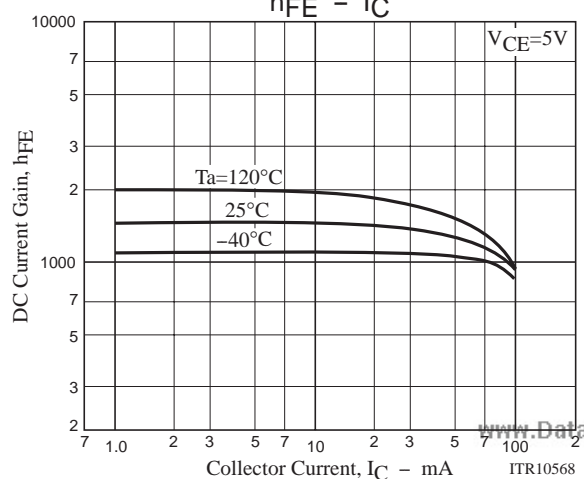
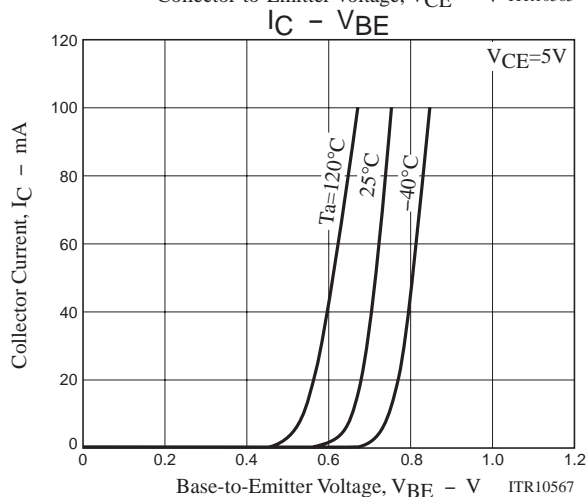
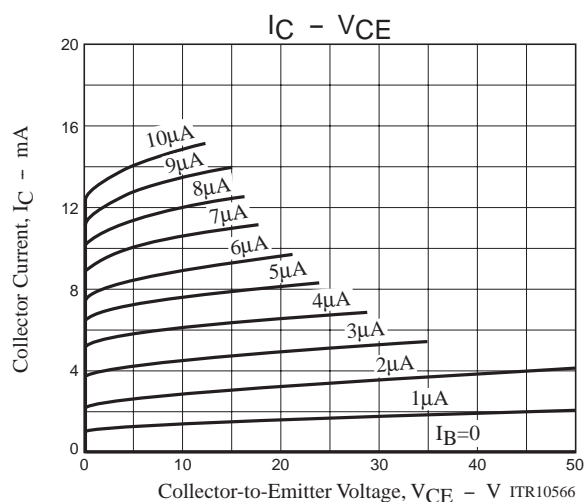
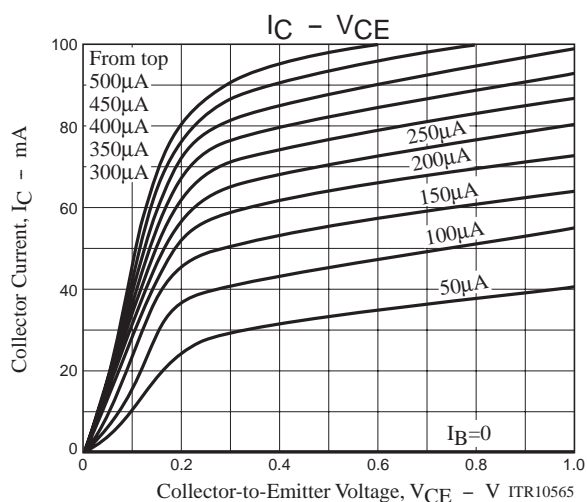
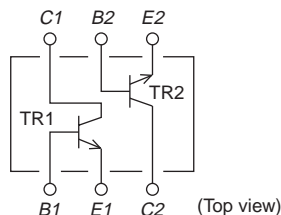
CPH6519

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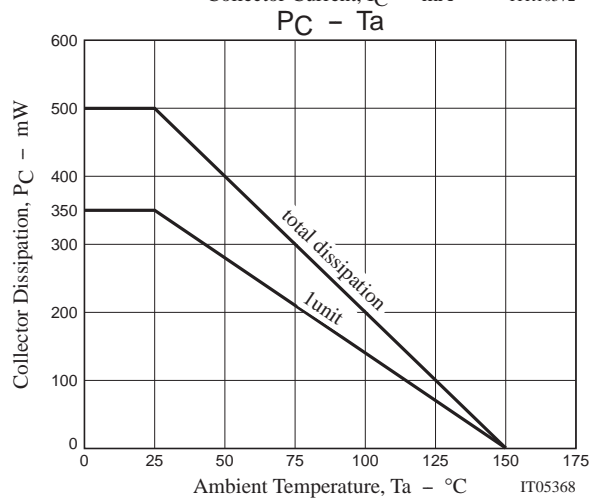
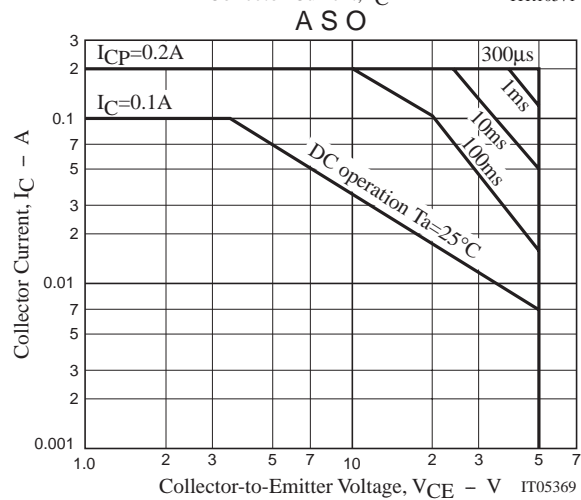
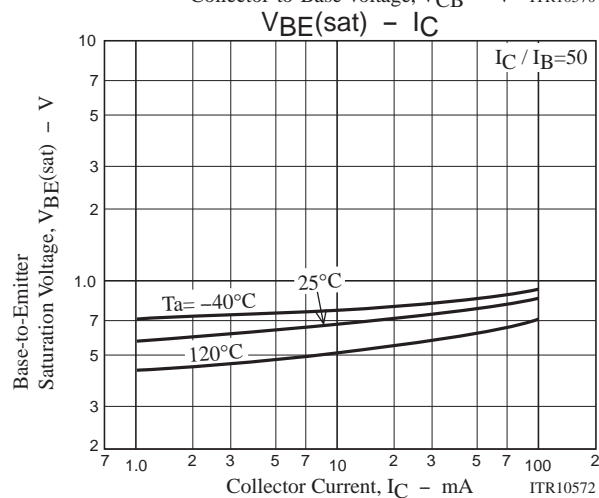
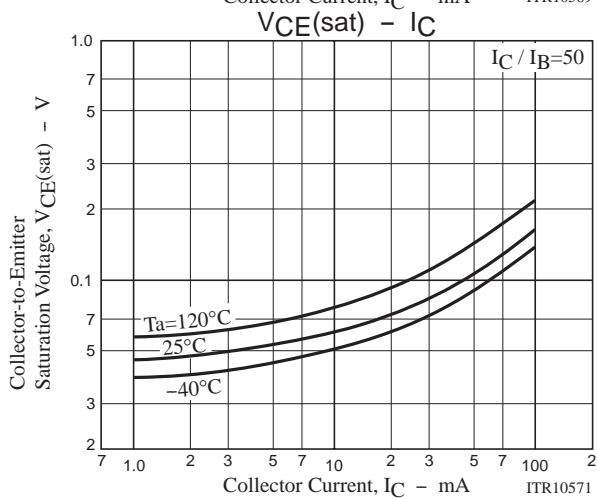
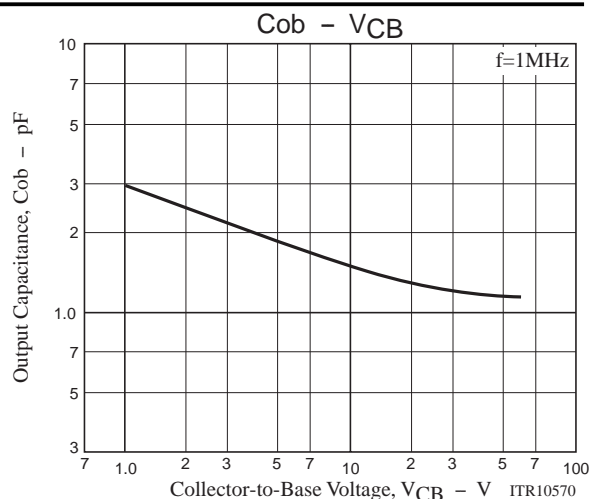
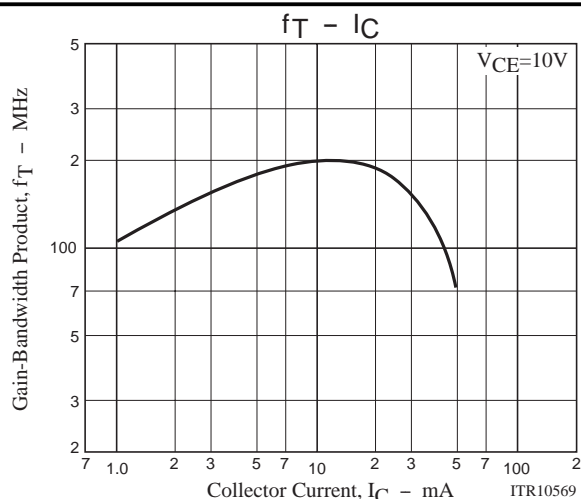
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Gain-Bandwidth Product	f_T	$V_{CE}=10V, I_C=10mA$		200		MHz
Output Capacitance	C_{ob}	$V_{CB}=10V, f=1MHz$		1.5		pF
Collector-to-Emitter Saturation Voltage	$V_{CE(sat)}$	$I_C=50mA, I_B=1mA$		0.1	0.5	V
Base-to-Emitter Saturation Voltage	$V_{BE(sat)}$	$I_C=50mA, I_B=1mA$		0.8	1.1	V
Collector-to-Base Breakdown Voltage	$V_{(BR)CBO}$	$I_C=10\mu A, I_E=0$	60			V
Collector-to-Emitter Breakdown Voltage	$V_{(BR)CEO}$	$I_C=1mA, R_{BE}=\infty$	50			V
Emitter-to-Base Breakdown Voltage	$V_{(BR)EBO}$	$I_E=10\mu A, I_C=0$	15			V

Note : The specifications shown above are for each individual transistor.

Electrical Connection



CPH6519



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