# Specifications for

## **TFT-LCD Monitor**

 $\frac{\text{Version 1.0}}{\text{(Please be sure to check the specifications latest version.)}}$ 

### MODEL COM65T6M13KSC

Customer's Approval		
Signature:		
Name:		
Section:		
Title:		
Date:		

# **ORTUSTECH**

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### 1. APPLICATION

This Specification is applicable to 16.56cm (6.5 inch) TFT-LCD back-light monitor for non-military use.

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- © This Product shall not be used for application which requires extremely higher level of reliability and/or safety such as aerospace equipment, telecommunication equipment for trunk lines, control equipment for nuclear facilities or life-support medical equipment.
- © It must be noted as an mechaniacl design manner, especial attention in housing design to prevent arcuation/flexureor caused by stress to the LCD module shall be considered.
- ORTUS TECHNOLOGY assumes no liability for any damage resulting from misuse, abuse, and/or miss-operation of the Product deviating from the operating conditions and precautions described in the Specification.
- © ORTUS TECHNOLOGY is not responsible for any nonconformities and defects that are not specified in this specifications.
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- © ORTUS TECHNOLOGY assumes no liability for defects such as electrostatic discharge failure occurred during peeling off the protective film or Purchaser's assembly process.

This Product is compatible for RoHS directive.

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Object substance	Maximum content [ppm]						
Cadmium and its compound	100						
Hexavalent Chromium Compound	1000						
Lead & Lead compound	1000						
Mercury & Mercury compound	1000						
Polybrominated biphenyl series (PBB series)	1000						
Polybrominated biphenyl ether series (PBDE series)	1000						

### 2. Outline Specifications

### 2.1 Features of the Product

- 6.5" diagonal with resolution of 1,920[H]x480[V] dots.
- 6-bit 262,144 color display capability.
- 3.3V voltage[TFT-LCD module] + (12)V voltage[Backlight] is required.
- Built in Timing generator (TG)
- Long life & high brightness LED back-light and built in LED driver.
- All-in-one type monitor with lead-free mounting. (Response to Phase 3A)

### 2.2 Display Method

Items	Specifications	Remarks
Display type	TN type 262,144 colors	
	Transmissive type, Normally white	
Driving method	a-Si TFT Active matrix	
	Line-scanning, Non-interlace	
Dot arrangement	RGB stripe arrangement	Refer to fig. 1
Signal input method	6-bit RGB, parallel input	
Backlight type	Long life & High bright white LED.	

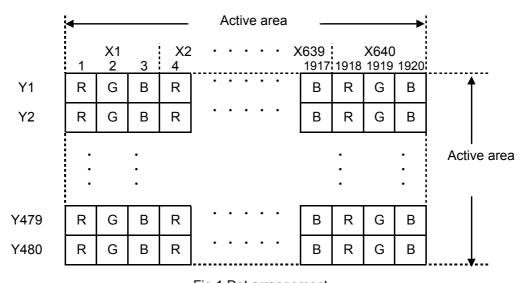
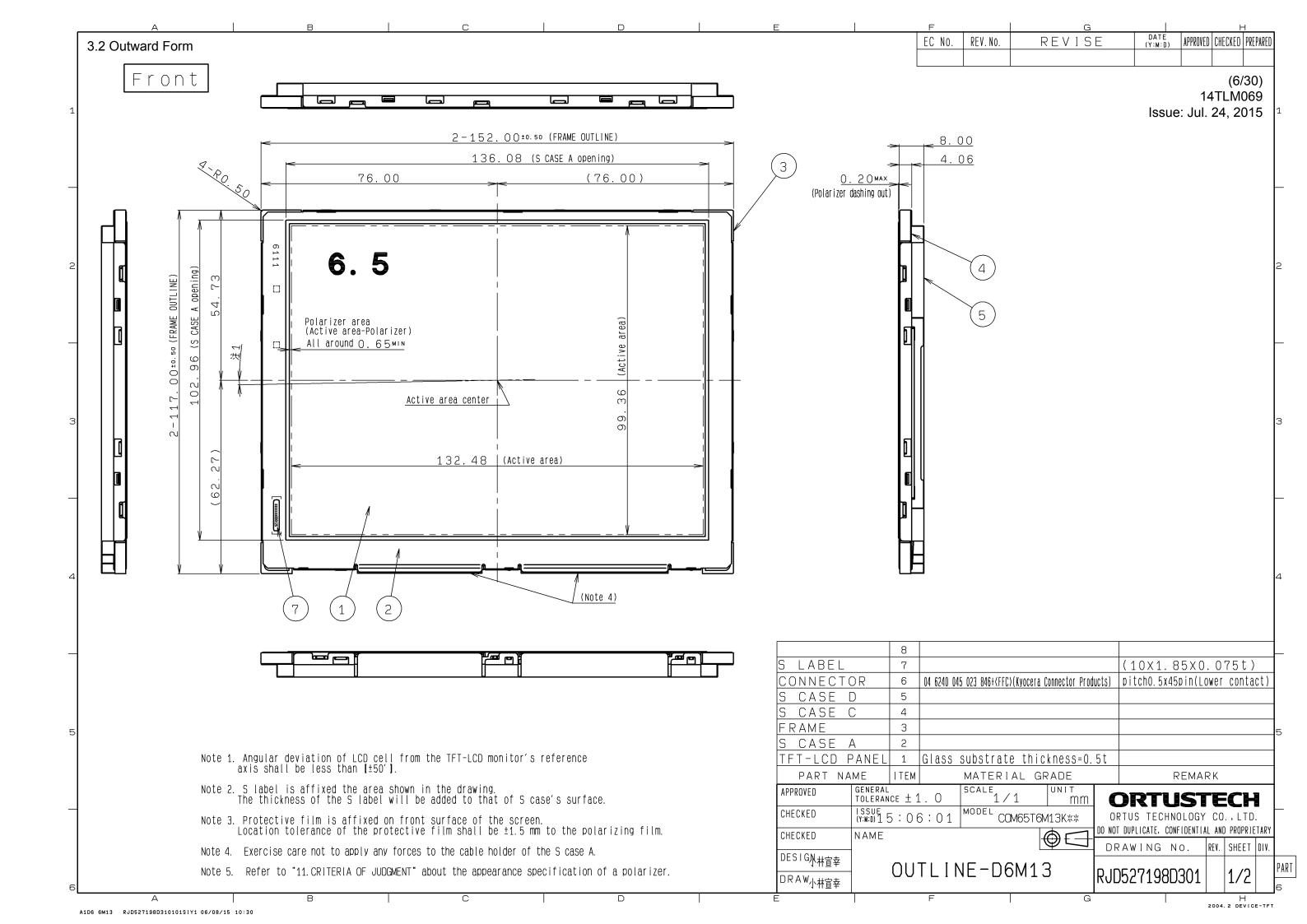


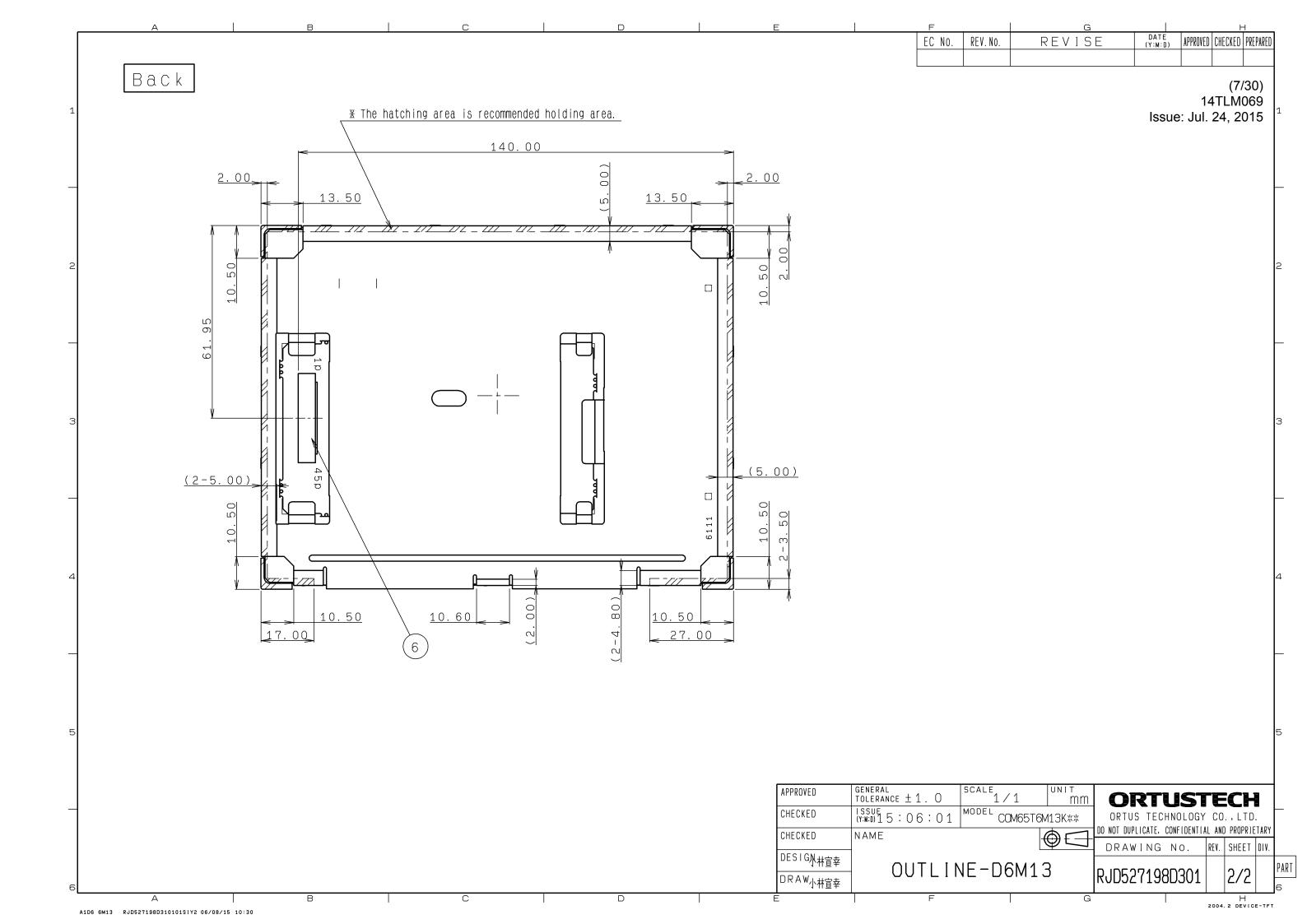
Fig 1 Dot arrangement (When "Product Number" logo on the front case is placed at the top left)

### 3. DIMENSIONS AND SHAPE

### 3.1 Dimensions

Items	Specifications	Unit	Remarks
Outline dimensions	152.00[H] × 117.00[V] × 8.00[D]	mm	
Active area	132.48[H] × 99.36[V]	mm	16.56cm diagonal
Number of dots	1,920[H] × 480[V]	dot	
Dot pitch	69.00[H] × 207.00[V]	μm	
Surface hardness of the	3	Н	Load: 2.0N
polarizer			
Weight	160	g	





### 3.3 SERIAL LABEL (S-LABEL)

1) Display Items

S-label indicates the least significant digit of manufacture year (1digit), manufacture month with below alphabet (1letter), model code (4characters or 5characters), serial number (6digits).

	Contents of display								
а	The least significant digit of manufacture year								
b	Manufacture month	lanufacture month Jan-A May-E Sep-I							
		Feb-B		Jun-F	Oct-J				
		Mar-C		Jul-G	Nov-K				
		Apr-D		Aug-H	Dec-L				
С	Model code	65ZC	(Made in Japa	an)					
		65AAC (Made in Malaysia)							
d	Serial number	•							

- \* Example of indication of Serial label (S-label)
- ·Made in Japan

6E65ZC000125

means "manufactured in May 2016, 6.5" Z type , C specifications, serial number 000125"

· Made in Malaysia

6E65AAC000125

means "manufactured in May 2016, 6.5" AA type , C specifications, serial number 000125"

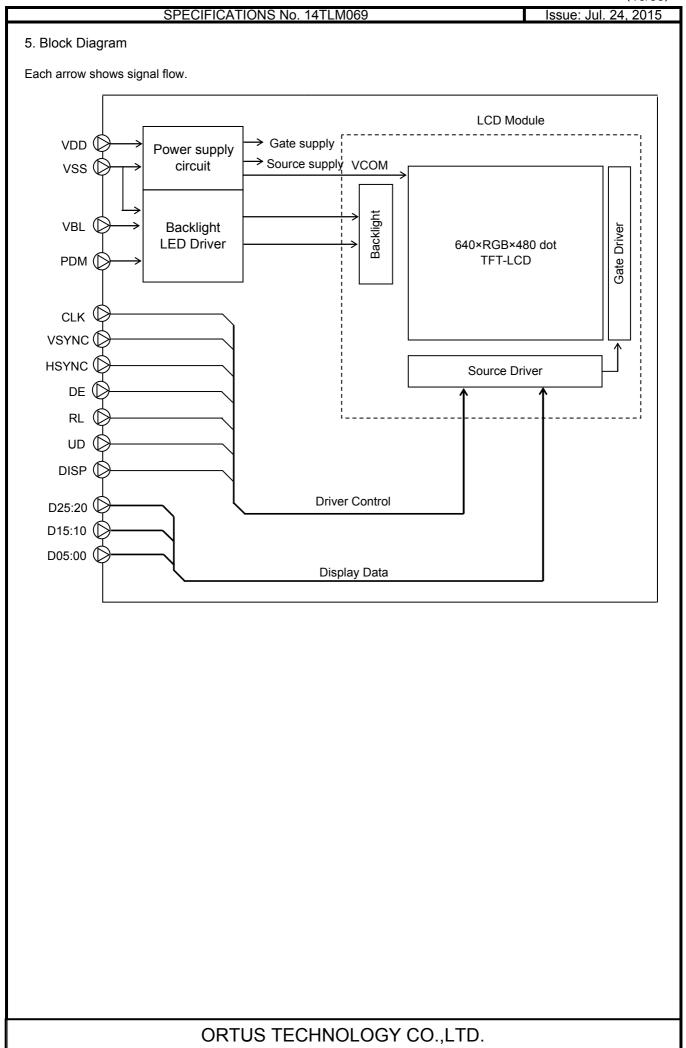
2) Location of Serial Label (S-label) Refer to 3.2 "Outward Form".

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### 4. PIN ASSIGNMENT

No.	Symbol	PIN	Functions
	-,	treatment	
1	VSS	_	GND.
2	CLK	_	Clock signal.Latching data at the rising edge.
3	VSS	_	GND.
4	HSYNC	_	Horizontal sync signal. (Low active)
5	VSYNC	_	Vertical sync signal. (Low active)
6	VSS	_	GND.
7	TEST1	pull-down	Short to VSS
8	TEST2	1	Short to VSS
9	D20	1	Display data(B).
10	D21	1	00h: Black
11	D22	1	D20:LSB D25:MSB
12	D23		
13	D24		Driver has internal gamma conversion.
14	D25		2 m o monta gamma oo moroton
15	VSS	_	GND.
16	TEST3	pull-down	Short to VSS
17	TEST4	1' '	Short to VSS
18	D10	1	Display data(G).
19	D11	1	00h: Black
20	D12	1	D10:LSB D15:MSB
21	D13	1	
22	D14	1	Driver has internal gamma conversion.
23	D15		2 m o monta gamma oo moroton
24	VSS	_	GND.
25	TEST5	pull-down	Short to VSS
26	TEST6	1' '	Short to VSS
27	D00	1	Display data(R).
28	D01	1	00h: Black
29	D02		D00:LSB D05:MSB
30	D03	1	
31	D04	1	Driver has internal gamma conversion.
32	D05		garanta con control
33	VSS	_	GND.
34	RL	_	Horizontally Flipped (right/left) Signal.
			(Lo: Horizontally Flipped Display, Hi: Normal display)
35	VDD	_	Power supply input
36	VDD	_	Power supply input
37	DISP	pull-up	Display on/off control signal(Lo : display off、Hi: display on)
38	DE	pull-down	Input data effective signal. (It is effective for the period of "Hi")
39	UD		Vertically Flipped (up/down) Signal. (Lo: Normal display,Hi: Vertically Flipped Display)
40	VSS	_	GND.
41	VBL	_	Power supply input(Backlight)
42	VBL	_	Power supply input(Backlight)
43	PDM	_	Brightness control pulse signal (Lo:0%(Backlight off) brightness, Hi:100%)
44	VSS	_	GND
45	VSS	1 – 1	GND

- Used connector: KYOCERA CONNECTOR PRODUCTS 6240 series [04 6240 045 023 846+]
- Please refer to the section "3.2 Outward Form" for pin assignment.
- The corrosion phenomenon by the different kind metal uniting is generated according to the system requirements, and there is a possibility of becoming a loose connection. Please select very carefully, and design the FPC cable used.



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### 6. ABSOLUTE MAXIMUM RATING

VSS=0V

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Item	Symbol	Condition	Ra	Rating		Applicable terminal
			MIN	MAX		
Supply voltage	VDD	Ta=25°C	-0.3	6.0	V	VDD
Input voltage for logic	VI		-0.3	VDD+0.3	V	CLK,VSYNC,HSYNC,DE D[25:00],RL,UD,DISP
Supply voltage for Backlight	VBL		_	14.0	V	VBL
Input voltage for Backlight	VIP		_	7.0	V	PDM
Storage temperature range	Tstg		-30	80	°C	
Storage humidity range	Hstg		Non condensing in an environmental moisture at or less than 40° C90%RH		%	

Note: Please input the logic signal after turning on VDD.

Do not input the logic signal while blocking VDD.

Absolute maximum ratings is parametric values, should never be exceed any value at any moment.

Beyond which, it could be suffered from changes in characteristics and never be restored.

Moreover, it could even be suffered from permanent destruction.

Therefore, please note enough the fluctuation of input voltage, the characteristics of connected parts, I/O signal line serge, and ambient temperature, on designing the circuit.

### 7. RECOMMENDED OPERATING CONDITIONS

VSS=0V

Item	Symbol	Condition		Rating			Applicable terminal
			MIN	TYP	MAX		
Supply voltage	VDD		3.0	3.3	3.6	V	VDD
Input voltage for logic	VI	VDD=3.0~3.6V	0	_	VDD	V	CLK,VSYNC, HSYNC,DE, D[25:00],RL,UD, DISP
Supply voltage for Backlight	VBL		10.8	12.0	13.2	V	VBL
Input voltage for Backlight	VIP		0	_	VDD	V	PDM
Operational temperature range	Тор	Note1 Note2	-20	+25	+70	°C	Panel surface temperature
Operating humidity	Нор	Ta ≦ 30°C	20	_	80	%	
range		Ta > 30°C	Non condensing in an environmental moisture at or less than 30° C80%RH.				

- Note 1: The temperature within the display will increase due to the heat radiated from the back light while in operation.

  Necessary measures have to be taken in the product design to make sure that the display has proper ventilation so that temperature on any surface of this display should not exceed 70°C.
- Note 2: Allowance ON Duty of LED changes depending on the ambient temperature.

  Do not exceed Allowable ON Duty shown on the chart below.

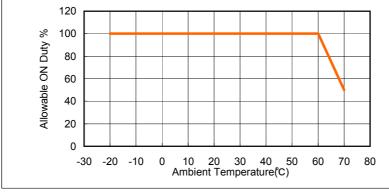


Fig. 2: Allowable ON Duty

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### 8. CHARACTERISTICS

### 8.1 Electrical characteristics

### 8.1.1 Display Module

(Unless otherwise noted, Ta=25°C,VDD=3.3V,VSS=0V)

Item	Symbol	Condition	Rating			Unit	Applicable terminal
			MIN	TYP	MAX		
Input voltage for logic	VIH		0.7×VDD	Í	VDD	>	CLK,VSYNC,HSYNC, DE,D[25:00],RL,UD,
	VIL		0	_	0.3×VDD	V	DISP
Pull down resister value	Rpd		300	450	600	kΩ	DE,D[25:00]
Pull up resister value	Rpu		300	450	600	kΩ	DISP
Current consumption		fCLK=25MHz Color bar display		165	330	mA	VDD

### 8.1.2 Backlight

(Unless otherwise noted, Ta=25°C,VBL=12.0V,VDD=3.3V,VSS=0V)

Item	Symbol	Condition		Rating		Unit	Applicable terminal
			MIN	TYP	MAX		
Input voltage	VIPH		1.4	_	VDD	V	PDM
for BACKLight	VIPL	]	0	_	0.2	V	
Operating	IBL	Brightness control	_	106	212	mA	VBL
Current		ON Duty=100%					
Estimated Life	LL	Note1	_	(50,000)	_	hr	
of LED		Brightness control					
		ON Duty=100%					

Note1: Life is defined as the brightness decrease to half of its initial brightness.

This number is for reference, and not a guaranteed spec.

This presumption value shows the estimated life expectancy in LED side light single purpose operation.

It is different from presumption with the monitoring because the environment is different.

Life is depend on environmental temperature. Especially using high temperature decreases life.

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### 8.2 AC CHARACTERISTICS

### 8.2.1 Display Module

(Unless otherwise noted, Ta=25°C,VDD=3.3V,VSS=0V)

					,	,	, ,
Item	Symbol	Condition		Rating		Unit	Applicable terminal
			MIN	TYP	MAX		
CLK frequency	fCLK		_	25	27	MHz	CLK
CLK Low period	tw1L	0.3×VDD or less	14.8	_	_	ns	CLK
CLK High period	tw1H	0.7×VDD or more	14.8	_	_	ns	CLK
Setup time	tsp		10	_	_	ns	CLK,DE,D[25:00]
Hold time	thd		10	_	_	ns	HSYNC,VSYNC
VSYNC pulse width	tw2H		1	3	5	Н	VSYNC
HSYNC pulse width	tw3H		5	30	_	CLK	HSYNC

### 8.2.2 Backlight

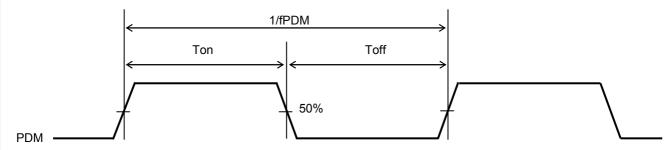
(Unless otherwise noted, Ta=25°C,VDD=3.3V,VBL=12.0V,VSS=0V)

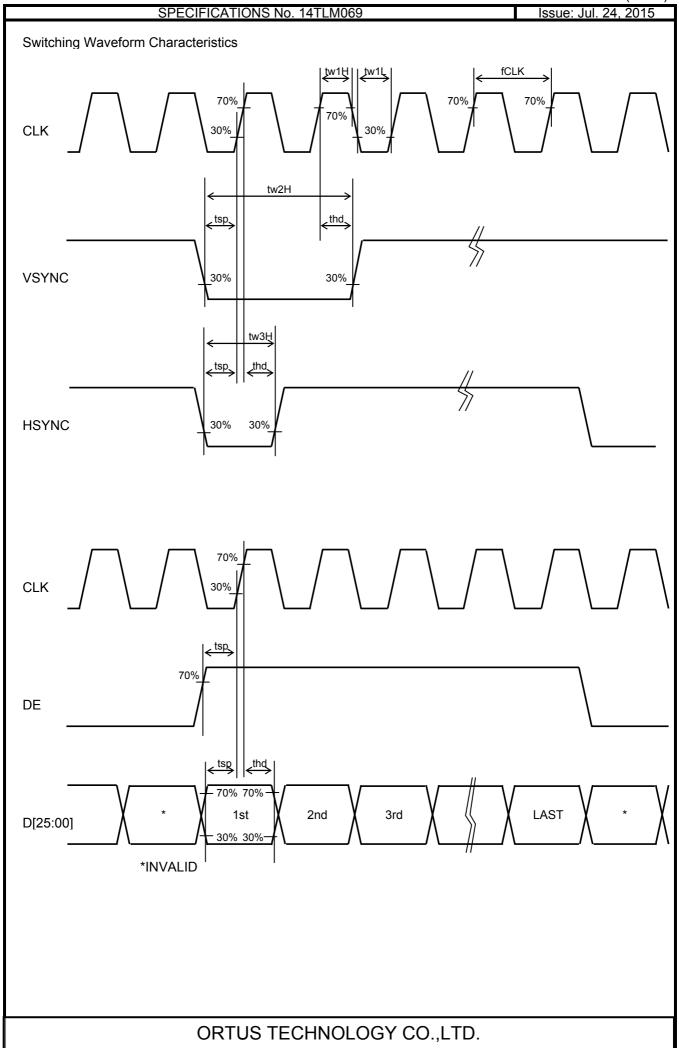
		\•	000 04.10.11.	00 110100, 11		0.0.,.	== :=:0:;:00 0:;
Item	Symbol	Condition	Rating			Unit	Applicable terminal
			MIN	TYP	MAX		
PDM frequency	fPDM		100	200	300	Hz	PDM
Backlight ON duty	ONduty	100×Ton/(Ton+Toff)	20	_	100	%	

### NOTE:

The interference fringes might be generated by optical interference with the PDM frequency and the VSYNC frequency on the screen.

Please evaluate it enough in all operating temperature limits when you set the PDM frequency.





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8.3 INPUT TIMING CHARACTERISTICS

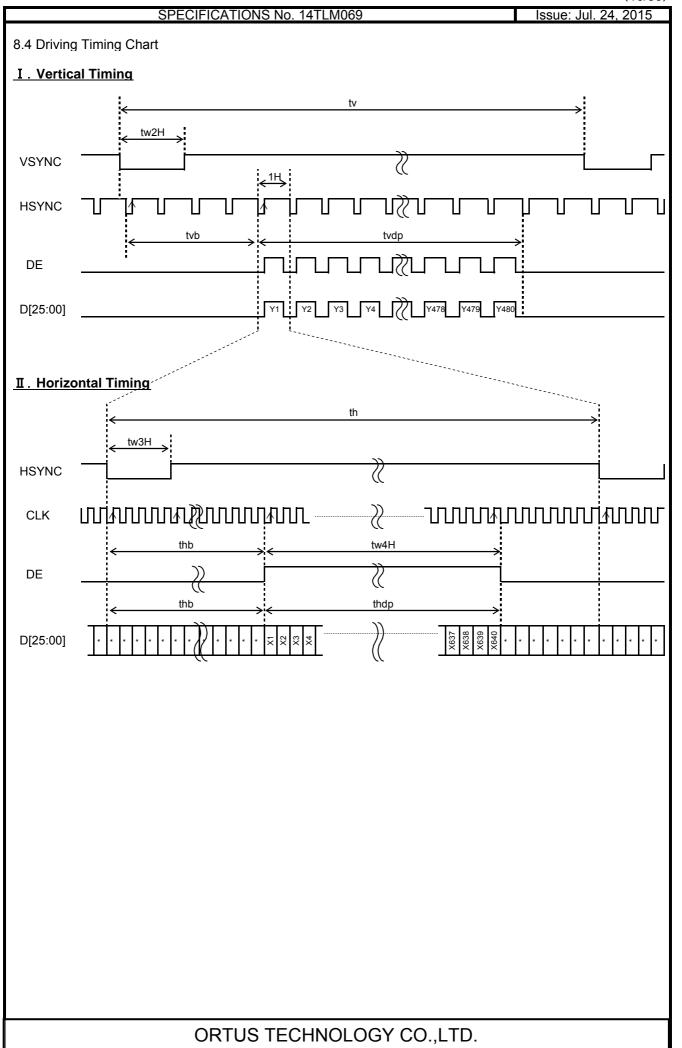
Item	Symbol	Rating		Unit	Applicable terminal	
		MIN	TYP	MAX		
CLK frequency	fCLK		25	27	MHz	CLK
VSYNC signal cycle time	tv		525	_	Ι	VSYNC,HSYNC
VSYNC frequency Note1	fVSYNC	54	60	66	Hz	VSYNC
VSYNC pulse width	tw2H	1	3	5	Н	VSYNC,HSYNC
Vartical back porch	tvb		35	_	Η	VSYNC,HSYNC,DE,D[25:00]
Vartical display period	tvdp	_	480	_	Н	VSYNC,HSYNC,DE,D[25:00]
HSYNC signal cycle time	th		800	_	CLK	HSYNC,CLK
HSYNC pulse width	tw3H	5	30	_	CLK	HSYNC,CLK
Horizontal back porch	thb	112	_	144	CLK	HSYNC,CLK,DE,D[25:00]
				Note 2		
Horizontal display period	thdp	_	640	_	CLK	HSYNC,CLK,DE,D[25:00]
DE pulse width	tw4H	_	640	_	CLK	DE,CLK

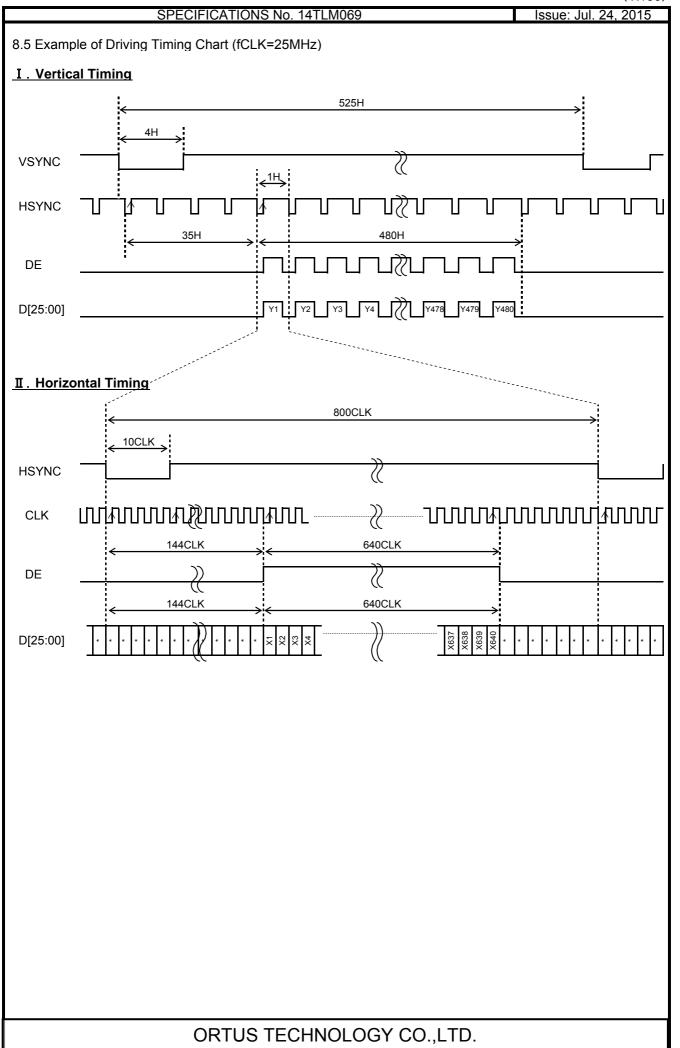
Note1: The characteristic of this item is recommended standard.

Please use it after it confirms it enough like the display fineness etc.

when it comes off from this characteristic and it is used.

Note2: When "DE" keeps "Lo" for 144CLK or longer, start capturing data automatically from 144CLK.





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### 10.1 Optical Characteristics

< Measurement Condition >

Backlight:

10. CHARACTERISTICS

Measuring instruments: CS1000 (KONICA MINOLTA) , LCD7200(OTSUKA ELECTRONICS) ,

EZcontrast160D(ELDIM)

Driving condition: VDD = 3.3V, VSS = 0V Optimized VCOMDC

VBL=12.0V(Brightness control ON Duty=100%)

Measured temperature:  $Ta = 25^{\circ} C$ 

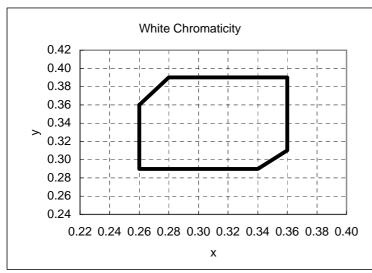
	Item	Symbol	Condition	MIN	TYP	MAX	Unit	Note No.	Remark
Respons e time	Rise time	TON	[Data]= 3Fh→00h	_		40	ms	1	*
Res <sub>l</sub> e ti	Fall time	TOFF	[Data]= 00h→3Fh	_		60	ms		
Co	ontrast ratio	CR	[Data]= 3Fh/00h	240	400	_		2	
<b>D</b>	Left	θL	[Data]=	80	_	_	deg	3	*
lie e	Right	θR	3Fh/00h	80	_	_	deg		
Viewing angle	Up	φU	CR≧10	80	_	_	deg		
>	Down	φD		80	_	_	deg		
\\/hit/	e Chromaticity	Х	[Data]=3Fh	White ch	romatici	y range		4	
VVIIILE	Cilionialicity	у							
	Burn-in			No noticeable burn-in image shall be observed after 2 hours of window pattern display.			•	5	
Center brgihtness		[Data]=3Fh	280	400	_	cd/m <sup>2</sup>	6		
	tness distributi	on	[Data]=3Fh	70	_	_	%	7	

<sup>\*</sup> Note number 1 to 7: Refer to the APPENDIX of "Reference Method for Measuring Optical Characteristics".

\* Measured in the form of LCD module.

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[White Chromaticity Range]

Х	у
0.26	0.36
0.26	0.29
0.34	0.29
0.36	0.31
0.36	0.39
0.28	0.39

White Chromaticity Range

### 10.2 Temperature Characteristics

< Measurement Condition >

Measuring instruments: CS1000 (KONICA MINOLTA) , LCD7200(OTSUKA ELECTRONICS)

Driving condition: VDD = 3.3V, VSS = 0V

Optimized VCOMDC

Backlight: VBL=12.0V(Brightness control ON Duty=100%)

Į.	tem		Specif	Specification	
	ILEITI		Ta=-20°C	Ta=70° C	Remark
Contr	ast ratio	CR	40 or more	40 or more	
Response time	Rise time	TON	200 msec or less	30 msec or less	*
Response time	Fall time	TOFF	300 msec or less	50 msec or less	*
Display Quality			No noticeable display defect or ununiformity should be observed		Use the criteria for judgment specified in the section 11.

Measured in the form of LCD module.

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### 11. CRITERIA OF JUDGMENT

### 11.1 Defective Display and Screen Quality

Test Condition: Observed TFT-LCD monitor from front during operation with the following conditions

Driving Signal Raster Patter (RGB, white, black)
Signal condition [Data]:3Fh,1Ch,00h(3steps)

Observation distance 30 cm Illuminance 200 to 350 lx

Backlight VBL=12.0V(Brightness control ON Duty=100%)

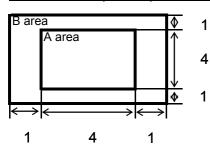
De	efect item Defect content			Criteria
	Line defect	Black, white or colo	r line, 3 or more neighboring defective dots	Not exists
Display Quality	Dot defect	TFT or CF, or dust (brighter dot, darke High bright dot: Vis Low bright dot: Vis	on dot-by-dot base due to defective is counted as dot defect r dot) ible through 2% ND filter at [Data]=00h ible through 5% ND filter at [Data]=10h ark through white display at [Data]=1Ch	Refer to table 1
		Invisible through 5%	6 ND filter at [Data]=00h	ignored
	Dirt	Uneven brightness	(white stain, black stain etc)	Invisible through 1% ND filter
₹			0.25mm< φ	N=0
Quality	Foreign	Point-like	0.20mm< φ ≦0.25mm	N≦2
Ø	_		φ ≦0.20mm	Ignored
ы	particle Liner	3.0mm <length, 0.08mm<width<="" td=""><td>N=0</td></length,>	N=0	
Screen		Lillei	length≤3.0mm, width≤0.08mm	Ignored
Š	Others	Use boundary sam	ple for judgment when necessary	

 $\varphi$ (mm): Average diameter = (major axis + minor axis)/2

Permissible number: N

Table 1

Area	High bright dot	Low bright dot	Dark dot	Total	Criteria
Α	0	2	2	3	Permissible distance between same color bright dots (includes neighboring dots): 3 mm or more
В	2	4	4	6	Permissible distance between same color high bright dots (includes neighboring dots): 5 mm or more
Total	2	4	4	7	



Division of A and B areas

B area: Active area

Dimensional ratio between A and B areas: 1: 4: 1 (Refer to the left figure)

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11.2 Screen and Other Appearance

Testing conditions

Illuminance 1200~2000 lx

Observation distance 30cm

	Item	Criteria	Remark
Polarizer	Flaw Stain Bubble Dust Dent	Ignore invisible defect when the backlight is on.	Applicable area: Active area only (Refer to the section 3.2 "Outward form")
S-ca	se	No functional defect occurs	
Connector No functional defect occurs		No functional defect occurs	

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### 12. RELIABILITY TEST

	Test item	Test condition	number of failures
	High temperature storage Ta=80°C 240hr		0/3
	Low temperature storage	Ta=-30°C 240hr	0/3
, t	High temperature & high	Ta=60°C, RH=90% 240hr	0/3
Ę			0/3
Durability test	humidity test High temperature operation	non condensing <u>%1</u> Tp=70° C 240hr	0.72
liq		Tp=-20°C 240hr	0/3
l is	Low temperature operation		0/3
	High temp & humid operation	Tp=40°C, RH=90% 240hrr	0/3
	The second all sections as	non condensing <u>%1</u>	2 /2
	Thermal shock storage	-30←→80° C(30min/30min) 100 cycles	0/3
		Confirms to EIAJ ED-4701/300	0/3
st	Electrostatic discharge test	C=200pF,R=0Ω,V=±200V	
l te	(Non operation)	Each 3 times of discharge on and power supply	
ta		and other terminals.	
Je I	Surface discharge test	C=250pF, R=100Ω, V=±12kV	0/3
	(Non operation)	Each 5 times of discharge in both polarities	
jë	(14011 operation)	on the center of screen with the case grounded.	
Vechanical environmental test	Vibration test	Total amplitude 1.5mm, f=10 ∼55Hz, X,Y,Z	0/3
$\frac{\theta}{a}$	VIDIATION test	directions for each 2 hours	
li i		Use ORTUS TECHNOLOGY original jig (see next	0/3
ра		page) and make an impact with peak acceleration	
ec	Impact test	of 1000m/s <sup>2</sup> for 6 msec with half sine-curve at	
≥		3 times to each X, Y, Z directions in	
		conformance with JIS 60068-2-27-2011.	
st		Acceleration of 19.6m/s <sup>2</sup> with frequency of	0/1 Packing
te e	Packing vibration-proof test	10→55→10Hz, X,Y, Zdirection for each	
ing		30 minutes	
Packing test	Dooking drap tost	Drop from 75cm high.	0/1 Packing
Ъ	Packing drop test	1 time to each 6 surfaces, 3 edges, 1 corner	

%1 The profile of high temperature/humidity storage and High Temperature/humidity operation (Pure water of over  $10M\Omega \cdot cm$  shall be used.)

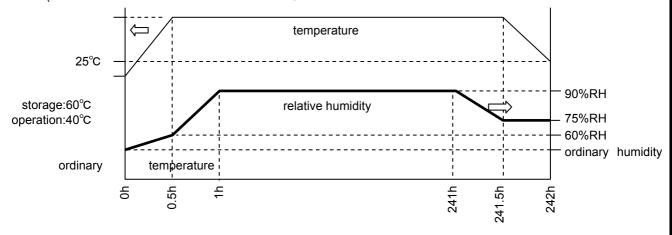
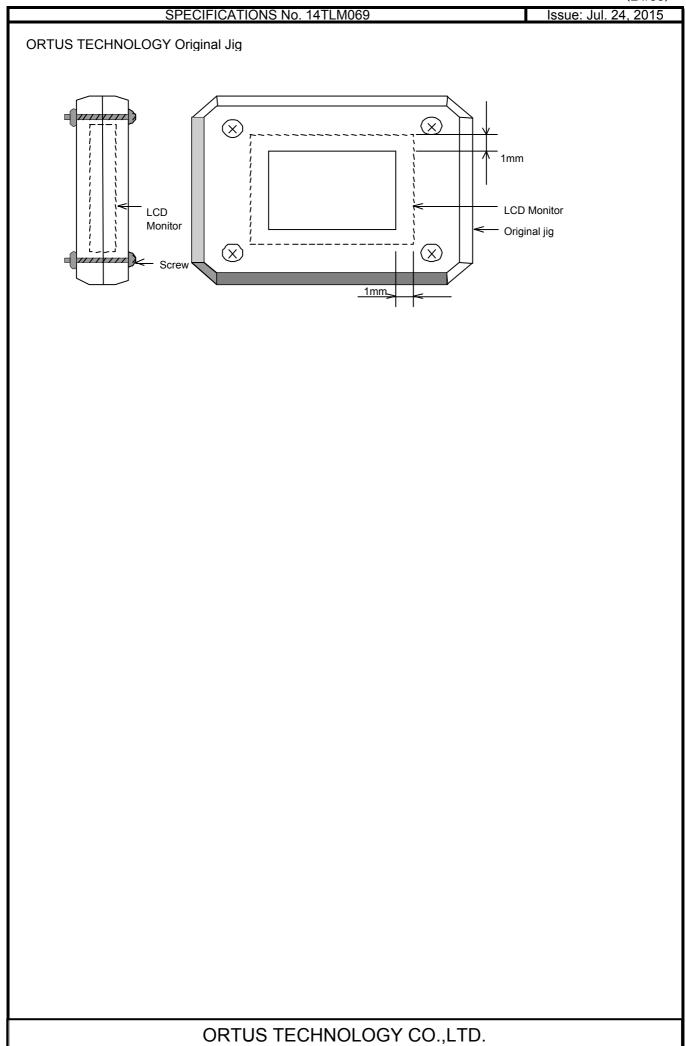


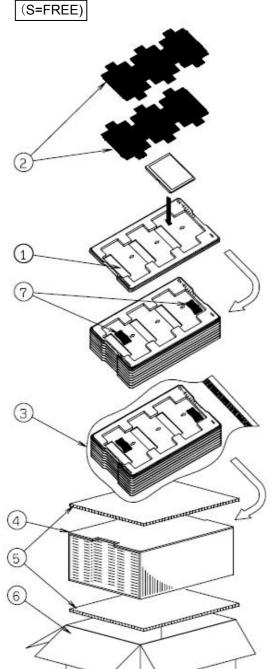
Table2.Reliability Criteria

Measure the parameters after leaving the monitor at the ordinary temperature for 24 hours or more after the test completion.

item	Standard	Remarks
Display quality		As criteria of 11"CRITERIA OF JUDGMENT".
Contrast ratio	40 or more	



### 13. PACKING SPECIFICATIONS



- Step 1 Each product is to be placed in one of the cut-outs of the tray with the display surface facing upward.(3products per tray)

  Antistatic foam sheet is to be placed on the products in the tray.
- Step 2 Each tray needs to be same orientation respect to the tray below or above it and the trays be in a stack of 7.

One empty tray is to be put on the top of stack of 7 trays.

Step 3 2 packs of moisture absobers are to be placed on the top tray as shown in the drawing.

Put piled trays into a sealing bag.

Vacuum and seal the sealing bag with the vacuum sealing machine.

- Step 4 The stack of trays in the plastic back is to be inserted into a inner carton.
- Step 5 A corrugated board is to be placed on the top and on the bottom of the inner carton.

The two corrugated boards and the inner carton is to be inserted into an outer carton.

Step 6 The outer carton needs to sealed with packing tape as shown in the drawing.

The model number, quantity of products, and shipping date are to be printed on the outer carton.

If necessary, shipping labels or impression markings are to be put on the outer carton.

Step 7 The outer carton is to be inserted into a extra outer carton with same direction.

The extra outer carton needs to sealed with packing tape as shown in the drawing.

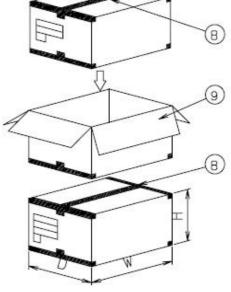
Step 8 The model number, quantity of products, and shipping date are to be printed on the extra outer carton.

If necessary, shipping labels or impression markings are to be put on the extra outer carton.



Remark: The return of packing materials is not required.

Packing item name		Specs., Material
1	Tray	PP
2	Antistatic foam sheet	
3	Sealing bag	
4	Inner carton	Corrugated cardboard
(5)	Inner board	Corrugated cardboard
6	Outer carton	Corrugated cardboard
7	Drier	Moisture absorber
8	Packing tape	
9	Extra outer carton	Corrugated cardboard



Dimension of extra outer carton			
D : Approx.	(338mm)		
W : Approx.	(549mm)		
H : Approx.	(198mm)		
Quantity of products	3pcsx7=21pcs		
packed in one carton:			
Gross weight : Approx.	6.6Kg		

### 14. HANDLING INSTRUCTION

14.1 Cautions for Handling LCD panels



### Caution

- (1) Do not make an impact on the LCD panel glass because it may break and you may get injured from it.
- (2) If the glass breaks, do not touch it with bare hands. (Fragment of broken glass may stick you or you cut yourself on it.
- (3) If you get injured, receive adequate first aid and consult a medial doctor.
- (4) Do not let liquid crystal get into your mouth. (If the LCD panel glass breaks, try not let liquid crystal get into your mouth even toxic property of liquid crystal has not been confirmed.
- (5) If liquid crystal adheres, rinse it out thoroughly. (If liquid crystal adheres to your cloth or skin, wipe it off with rubbing alcohol or wash it thoroughly with soap. If liquid crystal gets into eyes, rinse it with clean water for at least 15 minutes and consult an eye doctor.
- (6) If you scrap this products, follow a disposal standard of industrial waste that is legally valid in the community, country or territory where you reside.
- (7) Do not connect or disconnect this product while its application products is powered on.
- (8) Do not attempt to disassemble or modify this product as it is precision component.
- (9) For protection your circuit, we recommend you to add excess current protection circuit to power supply.



This mark is used to indicate a precaution or an instruction which, if not correctly observed, may result in bodily injury, or material damages alone.

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### 14.2 Precautions for Handling

- Wear finger tips at incoming inspection and for handling the TFT monitors to keep display quality and keep the working area clean.
   Do not touch the surface of the polarizer as it is easily scratched.
- Wear grounded wrist-straps and use electrostatic neutralization blowers to prevent static charge and discharge when handling the TFT monitors as the LED in this TFT monitors is damageable to electrostatic discharge, Properly set up equipment, jigs and machines, and keep working area clean and tidy for handling the TFT monitors.
- 3) Avoid strong mechanical shock including knocking, hitting or dropping to the TFT monitors for protecting their glass parts. Do not use the TFT monitors that have been experienced dropping or strong mechanical shock.
- 4) Do not use or storage the TFT monitors at high temperature and high humidity environment. Particularly, never use or storage the TFT monitors at a location where condensation builds up.
- 5) Avoid using and storing TFT monitors at a location where they are exposed to direct sunlight or ultraviolet rays to prevent the LCD panels from deterioration by ultraviolet rays.
- 6) Do not stain or damage the contacts of the connector .

  Otherwise, it may cause poor contact or deteriorate reliability of the connector.
- 7) Peel off the protective film on the TFT monitors during mounting process. Refer to the section 14.5 on how to peel off the protective film. We are not responsible for electrostatic discharge failures or other defects occur when peeling off the protective film.
- 8) The volume attached to the monitor is set to the optimal value at the time of shippment from our factory, so please do not change it.

### 14.3 Precautions for Operation

- Since this TFT monitors are not equipped with light shielding for the driver IC, do not expose the driver IC to strong lights during operation as it may cause functional failures.
- In case of powering up or powering off this LCD module, be sure to comply the sequence as instructed in this specification.
- 3) Do not plug in or out the connector while power supply is switch on. Plug the connector in and out while power supply is switched off.
- 4) Do not operate the TFT monitors in the strong magnetic field. It may break the TFT monitors.
- Do not display a fixed image on the screen for a long time. Use a screen-saver or other measures to avoid a fixed image displayed on the screen for a long time. Otherwise, it may cause burn-in image on the screen due the characteristics of liquid crystal.

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### 14.4 Storage Condition for Shipping Cartons

Storage environment

Temperature 0 to 40°C
 Humidity 60%RH or less

No-condensing occurs under low temperature with high humidity condition.

Atmosphere No poisonous gas that can erode electronic components and/or wiring

materials should be detected.

Time period 3 months

Unpacking To protect the TFT monitors from static damage during unpacking, keep

room humidity more than 50%RH and implement effective countermeasures against static electricity such as establishing a ground (an earth) before

unpacking.

Maximum piling up 7 cartons

### 14.5 Precautions for Peeling off the Protective film

The followings work environment and work method are recommended to prevent the TFT monitors from static damage or adhesion of dust when peeling off the protective films.

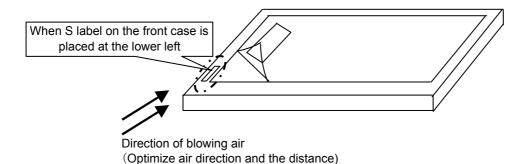
### A) Work Environment

- a) Humidity: 50 to 70 %RH, Temperature15 to 27°C
- b) Operators should wear conductive shoes, conductive clothes, conductive finger tips and grounded wrist-straps. Anti-static treatment should be implemented to work area's floor.
- c) Use a room shielded against outside dust with sticky floor mat laid at the entrance to eliminate dirt.

### B) Work Method

The following procedures should taken to prevent the driver ICs from charging and discharging.

- a) Use an electrostatic neutralization blower to blow air on the TFT monitors to its lower left when S label on the front case is placed at the lower left.
   Optimize direction of the blowing air and the distance between the TFT monitors and the electrostatic neutralization blower.
- b) Put an adhesive tape (Scotch tape, etc) at the lower left corner area of the protective film to prevent scratch on surface of TFT monitors.
- c) Peel off the adhesive tape slowly (spending more than 2 secs to complete) by pulling it to opposite direction.



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### **APPENDIX**

Reference Method for Measuring Optical Characteristics and Performance

### 1. Measurement Condition

Measuring instruments: CS1000 (KONICA MINOLTA) , LCD7200(OTSUKA ELECTRONICS) ,EZcontrast160D (ELDI

Driving condition: Refer to the section "Optical Characteristics"

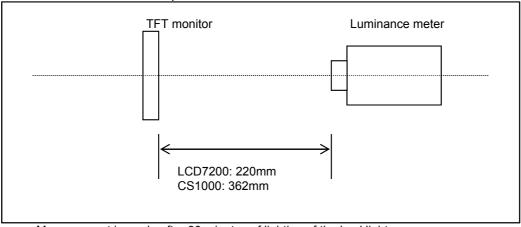
Measured temperature: 25°C unless specified

Measurement system: See the chart below. The luminance meter is placed on the normal line of

measurement system.

Measurement point: At the center of the screen unless otherwise specified

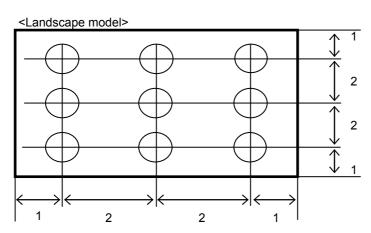
Dark box at constant temperature



Measurement is made after 30 minutes of lighting of the backlight.

Measurement point: At the center point of the screen

Brightness distribution: 9 points shown in the following drawing.



Unit: fraction Backlight VBL=12.0V(Brightness control ON Duty=100%)

### Issue: Jul. 24, 2015 SPECIFICATIONS No. 14TLM069 2. Test Method Notice Item Test method Measuring Remark instrument LCD7200 Response Measure output signal waveform by the luminance Black display time meter when raster of window pattern is changed from [Data]=00h White display white to black and from black to white. [Data]=3Fh TON White White Rise time Black **TOFF** White Fall time 100% 90% 10% 0% Black TON **TOFF** Contrast ratio Measure maximum luminance Y1([Data]=3Fh) and CS1000 minimum luminance Y2([Data]=00h) at the center of the screen by displaying raster or window pattern. Then calculate the ratio between these two values. Contrast ratio = Y1/Y2 Diameter of measuring point: 8mmφ Viewing Move the luminance meter from right to left and up EZcontrast160D and down and determine the angles where angle Horizontalθ contrast ratio is 10. Verticalφ CS1000 White Measure chromaticity coordinates x and y of CIE1931 chromotically colorimetric system at [Data] = 3Fh Color matching faction: 2°view 6 Burn-in Visually check burn-in image on the screen after 2 hours At optimized VCOMDC of "window display" ([Data]=3Fh/00h). 7 Center CS1000 Measure the brightness at the center of the screen. brightness 8 (Brightness distribution) = 100 x B/A % CS1000 **Brightness** distribution A: max. brightness of the 9 points B: min. brightness of the 9 points