

Low-Dropout High-Power LED Driver CN5612

General Description:

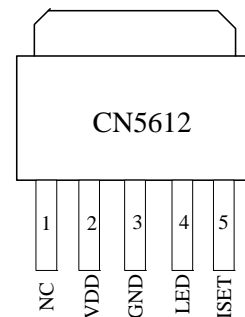
The CN5612 current regulator operates from a 2.7V to 6V input voltage range and delivers a constant current that is up to 1.2A to high-brightness LEDs, including high-brightness white LED. The LED current of CN5612 can be adjusted from 30mA to 1.2A by using an external resistor. An on-chip pass element minimizes external components while providing $\pm 8\%$ output current accuracy. Additional features include over temperature protection, LED short and open protection.

The CN5612 is available in a thermally enhanced 5-pin TO252 package.

Features:

- Operating Supply Voltage Range: 2.7V to 6V
- On-chip Pass Element
- Low-Dropout Voltage
- Adjustable Output Current up to 1.2A
- Output Current Accuracy: $\pm 8\%$
- Over Temperature Protection
- LED Open/Short Protection
- Operating Temperature Range:
-40°C to 85°C
- Available in 5-pin TO252 Packages
- Pb-free

Pin Assignment



Applications:

- High-Power LED Driver
- LED Cap-Lamp
- Flash Light and Lighting
- Cell Phone, DSC and MP3 Player
- LCD and Keyboard Backlight

Typical Application Circuit

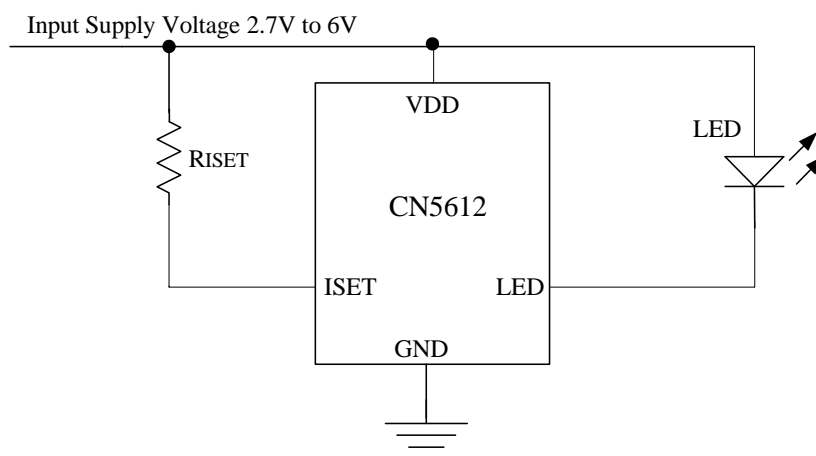


Figure 1 Typical Application Circuit

Block Diagram

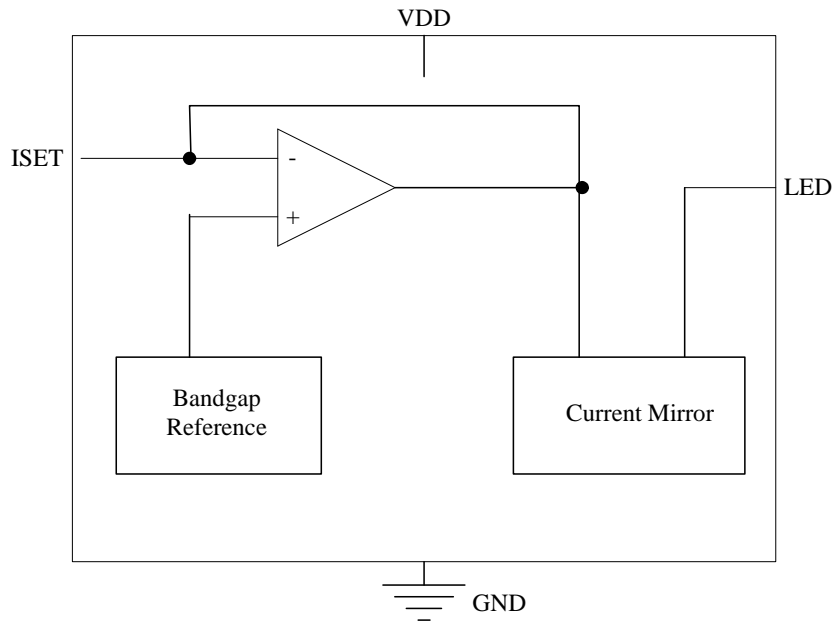


Figure 2 Block Diagram

Pin Description

Pin No.	Name	Function Description
1	NC	No Connection.
2	VDD	Positive Supply Voltage. V _{DD} is the power supply to the internal circuit.
3	GND	Ground Terminal.
4	LED	LED Cathode Connection Pin.
5	ISET	<p>Constant LED Current Setting Pin. The constant LED current is set by connecting a resistor R_{ISET} from this pin to VDD. The LED current is determined by the following equation:</p> $I_{out} = 910V / R_{ISET}$ <p>Where, I_{out} is in ampere(A) R_{ISET} is in ohm(Ω)</p>

Absolute Maximum Ratings

All Terminal Voltage.....—0.3V to 6.5V
 Operating Temperature.....—40°C to 85°C
 Thermal Resistance(Junction to case)10°C/W

Maximum Junction Temperature.....150°C
 Storage Temperature.....—65°C to 150°C
 Lead Temperature(Soldering).....300°C

Stresses beyond those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions above those indicated in the operational sections of the specifications is not implied. Exposure to Absolute Maximum Rating Conditions for extended periods may affect device reliability.

Electrical Characteristics

(VIN=3.7V, TA=25°C, unless otherwise noted)

Parameters	Symbol	Test Conditions	Min	Typ	Max	Unit
Input Supply Voltage	VDD		2.7		6	V
Operating Current	IVDD	R _{ISET} =10K Ω		335		uA
LED Pin Sink Current	I _{LED}	R _{ISET} =910 Ω	920	1000	1080	mA
LED Current Tolerance			-8		+8	%
Over Temperature Protection Temperature	T _{OTP}			125		°C
Over Temperature Protection Hysteresis	T _H			11		°C
LED pin Leakage Current	I _{LKG}	VDD=GND, V _{LED} =5.5V			1	uA
LED Dropout Voltage	V _{DROP}	I _{LED} =100mA × 90%		95		mV
		I _{LED} =200mA × 90%		135		
		I _{LED} =300mA × 90%		175		
		I _{LED} =500mA × 90%		280		
		I _{LED} =700mA × 90%		390		
		I _{LED} =1100mA × 90%		560		
Thermal Resistance	θ _{JC}	Junction to Case		10		°C/W
Thermal Resistance	θ _{JA}	Junction to Ambient, No heat sink, No air flow		85		°C/W

Detailed Description

The CN5612 is a current regulator capable of providing LED current up to 1.2A to high-power LEDs. In addition, CN5612 features over temperature protection, LED open/short protection.

The CN5612 enters a thermal-shutdown mode in the event of over temperature. This typically occurs in overload or LED short-circuit conditions. When CN5612's junction temperature exceeds T_J = +125°C (typical), the current flowing into LED pin is about 1% of that set by R_{ISET} to prevent the device from damage. CN5612 recovers from thermal-shutdown mode once the junction temperature drops by 11°C (typical). The device will therefore protect itself by thermally cycling in the event of LED short-circuit or overload condition.

Application Information

Adjusting LED Current

CN5612 uses a resistor between ISET pin and VDD pin to set the LED current. The LED current is given by the following equation:

$$I_{LED} = 910V / R_{ISET}$$

Where: I_{LED} is the LED current in ampere(A)

R_{ISET} is the resistance from the ISET pin to VDD pin in ohm(Ω)

For best stability over temperature and time, 1% and 1/10W metal film resistors are recommended.

Thermal Consideration

The CN5612's maximum allowable power dissipation is given by the following equation:

$$P_{Dmax} = (T_J - T_A) / \theta_{JA}$$

Where, P_{Dmax} is the maximum allowable power dissipation

T_J is the maximum junction temperature of CN5612, $T_J=125^{\circ}\text{C}$ due to the effect of over temperature protection circuit

T_A is the CN5612's ambient temperature

θ_{JA} is the thermal resistance of CN5612, when there is no heat sink and no air flow, θ_{JA} is $85^{\circ}\text{C}/\text{W}$, and it will be decreased to a great extent when there is heat sink, So in order to maximize LED current, careful thermal consideration must be given when designing PCB.

The actual power dissipation of CN5612 is given by the following equation:

$$P_{\text{Dact}} = V_{\text{LED}} \times I_{\text{LED}}$$

where, P_{Dact} is CN5612's actual power dissipation

V_{LED} is the maximum voltage at LED pin

I_{LED} is the designed output current

For the purpose of normal operation for CN5612, P_{Dact} must be less than P_{Dmax} .

Drive Multi-LEDs

When multi-LEDs are needed to be driven, the circuit in Figure 3 or in Figure 4 can be used. V_{IN} is the high input supply voltage that is used to drive multi-LEDs, CN5612's operating voltage can be from an independent source as shown in figure 3, or generated from V_{IN} as shown in figure 4.

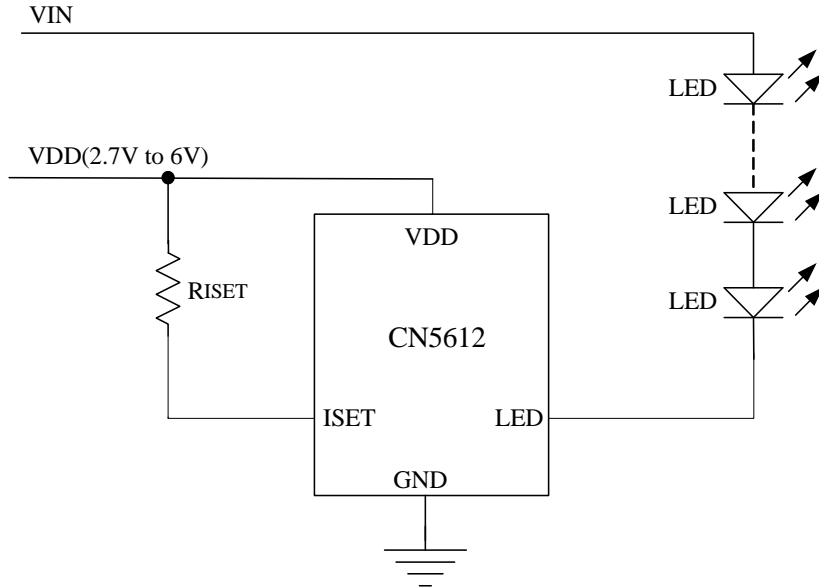


Figure 3 Drive Multi-LEDs from 2 Voltage Sources

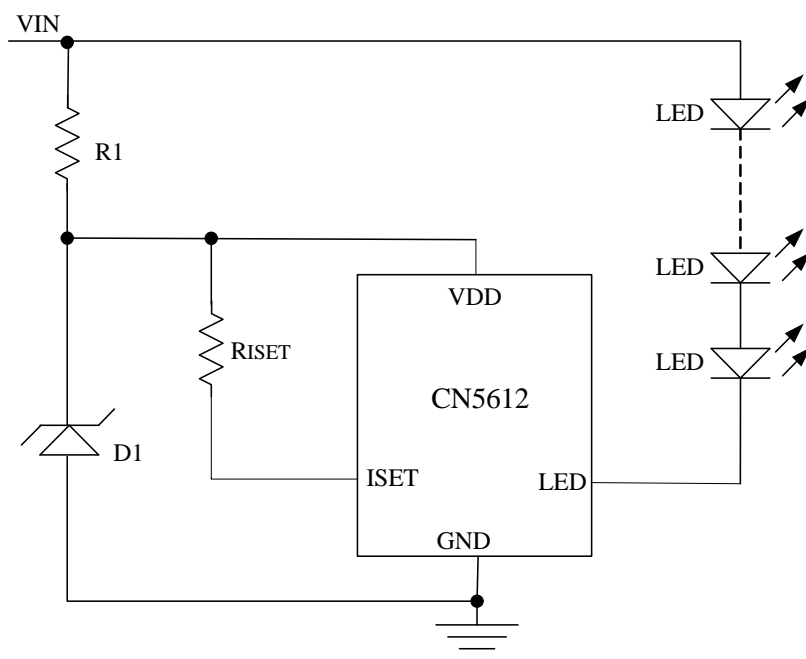


Figure 4 Drive Multi-LEDs from a High Voltage Sources

In the circuit of Figure 3 and Figure 4, care must be taken to make sure that the voltage at CN5612's LED pin should be kept less than 6V under any conditions.

In the circuit of Figure 4, D1 is the zener diode whose breakdown voltage is between 2.7V to 6V. R1's resistance should be small enough so that the current flowing through R1 is greater than 3mA under worst case.

For more application circuits, please refer to "CN5611-CN5612 Application Circuit Collection".

Dimming Control

There are 3 different ways of dimming control:

1. Using a PWM signal for dimming control as shown in Figure 5

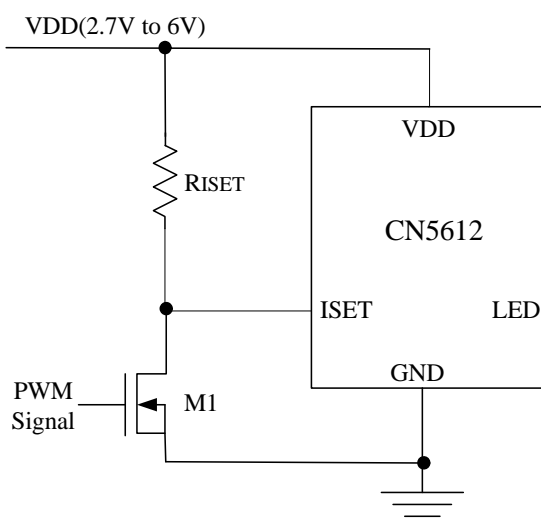


Figure 5 Dimming Control by Using PWM Signal

With PWM signal applied to the gate of N channel MOSFET M1, the LEDs operate at either zero or full current. The average LED current decreases with the increasing duty cycle of PWM signal. A 100% duty cycle will turn off LEDs completely, a 0% duty cycle corresponds to full LED current. The frequency of PWM signal should be under 10KHz.

2. Using a logic signal for dimming control as shown in Figure 6

For applications that need to adjust the LED current in discrete steps, a logic signal can be used as shown in Figure 6. R_{ISET1} sets the minimum LED current (when the PMOS is off, namely logic signal is high). R_{ISET2} sets how much the LED current increases when the PMOS is turned on (logic signal is logic low).

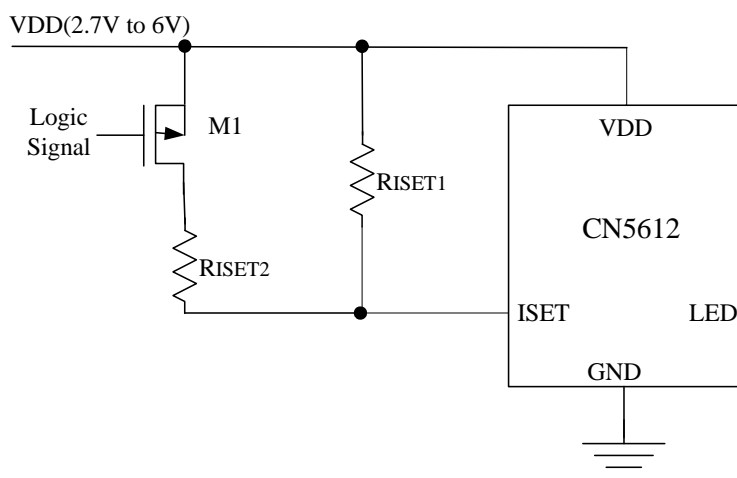


Figure 6 Dimming Control by Using Logic Signal

3. Using a variable resistor for dimming control as shown in Figure 7

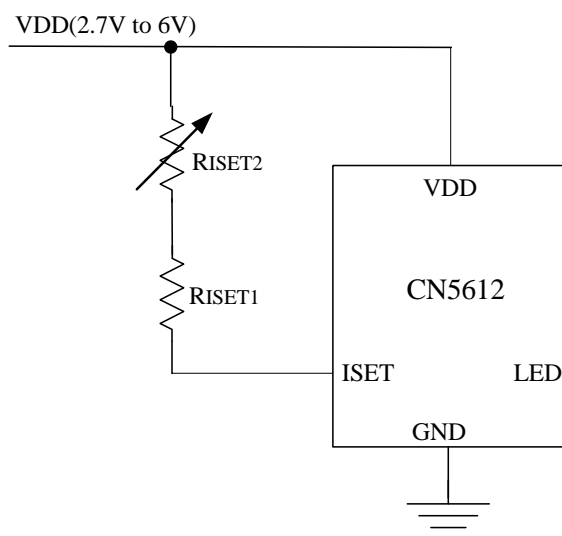


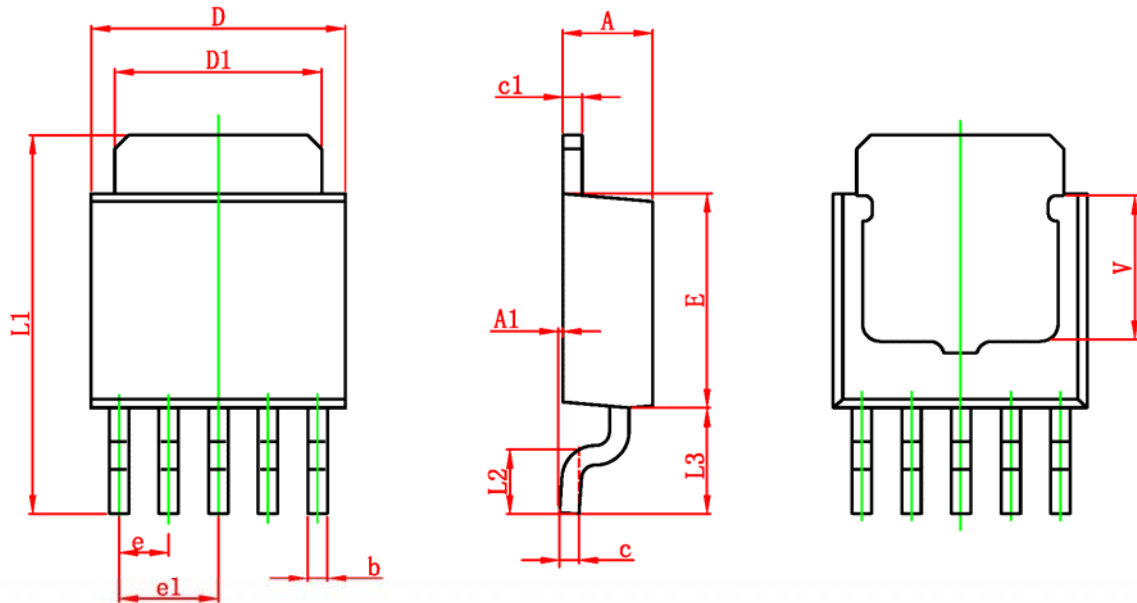
Figure 7 Dimming Control by Using Variable Resistor

Board Layout Considerations

It is very important to use a good thermal PC board layout to maximize LED current. The thermal path for the heat generated by the IC is from the die to the copper lead frame through the package lead (especially the LED and ground lead) to the PC board copper, the PC board copper is the heat sink. The footprint copper pads should be as wide as possible and expand out to larger copper areas to spread and dissipate the heat to the surrounding ambient. Feedthrough vias to inner or backside copper layers are also useful in improving the overall thermal performance of CN5612. Other heat sources on the board, not related to CN5612, must also be considered when designing a PC board layout because they will affect overall temperature rise and the maximum LED current.

The ability to deliver maximum LED current under all conditions require that the exposed metal pad on the back side of the CN5612 package be soldered to the PC board ground, or adding a heat sink. Failure to do these will result in larger thermal resistance, hence smaller LED current.

Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
b	0.400	0.600	0.016	0.024
c	0.430	0.580	0.017	0.023
c1	0.430	0.580	0.017	0.023
D	6.350	6.650	0.250	0.262
D1	5.200	5.400	0.205	0.213
E	5.400	5.700	0.213	0.224
e	1.270 TYP		0.050 TYP	
e1	2.540 TYP		1.000 TYP	
L1	9.500	9.900	0.374	0.390
L2	1.400	1.780	0.055	0.070
L3	2.550	2.900	0.100	0.114
V	3.800 REF		0.150 REF	

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