

Continental Device India Limited

An ISO/TS 16949, ISO 9001 and ISO 14001 Certified Company





SOT-23 Formed SMD Package

CMBT 6520

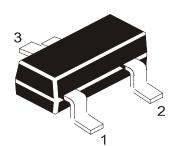
HIGH-VOLTAGE TRANSISTOR

P-N-P transistor

Marking

CMBT6520 = 2Z

www.datasheet4u.com



Pin configuration

1 = BASE

2 = EMITTER

3 = COLLECTOR



2

ABSOLUTE MAXIMUM RATINGS

Collector-base voltage (open emitter)	$-V_{CBO}$	max.	<i>350</i>	V
Collector-emitter voltage (open base)	$-V_{CEO}$	max.	<i>350</i>	V
Emitter-base voltage (open collector)	$-V_{EBO}$	max.	5	V
Collector current (d.c.)	$-I_C$	max.	<i>500</i>	mA
Total power dissipation at $T_{amb} = 25^{\circ}C$	P_{tot}	max	225	mW
D.C. current gain				
$-I_C = 10 \text{ mA; } -V_{CE} = 10 \text{ V}$	h_{FE}	min.	30	

RATINGS (at $T_A = 25^{\circ}C$ unless otherwise specified)

Limiting values

Collector-base voltage (open emitter)	$-V_{CBO}$	max.	350	V
Collector-emitter voltage (open base)	$-V_{CEO}$	max.	<i>350</i>	V
Emitter-base voltage (open collector)	$-V_{EBO}$	max.	5	V
Collector current (d.c.)	$-I_C$	max.	<i>500</i>	mA
Total power dissipation at $T_{amb} = 25^{\circ}C$	P_{tot}	max	225	mW
Storage temperature	T_{Stg}	−55 to	+150	${}^{\circ}\!C$
Junction temperature	Tj	max.	<i>150</i>	${}^{\circ}C$

CMBT 6520

$T_j = P(R_{th j-t} + R_{th s-a}) + T_{amb}$ Thermal resistance				
from junction to ambient	$R_{th\ j-a}$		556	°C/mW
CHARACTERISTICS (at $T_A = 25^{\circ}C$ unless	otherwise specified,)		
Collector-emitter breakdown voltage	_			
$-I_C = 1 \text{ mA}$	$-V_{(BR)CE}$	O min.	350	V
Collector-base breakdown voltage	(==9==	-		
$-I_C = 100 \mu A$	$-V_{(BR)CB}$	O min.	350	V
Emitter-base breakdown voltage	(21902			
$-I_E = 10 \mu A$	$-V_{(BR)EB0}$	o min.	5	V
Collector cut-off current	(BIV)EB			
$-V_{CB} = 250 \text{ V}$	-I _{CBO}	max.	50	nA
Emitter cut-off current	СБС			
$-V_{EB} = 4 V$	-I _{EBO}	max.	50	nΑ
Output capacitance at $f = 1$ MHz	LDC			
$-V_{CB} = 20 \text{ V}$	C_{c}	max.	6	pF
Input capacitance at $f = 1$ MHz				r
$-V_{EB} = 0.5 V$	C_e	max.	100	pF
Saturation voltages				
$-I_C = 10 \text{ mA}; -I_B = 1 \text{ mA}$	-V _{CEsat}	max.	0.3	V
	-V _{BEsat}	max.	0.75	V
$-I_C = 20 \text{ mA}; -I_B = 2 \text{ mA}$	-V _{CEsat}	max.	0.35	V
10 20 111 1, 1 _D 2 111 1	-V _{BEsat}	max.	0.85	\overline{V}
$-I_C = 30 \text{ mA}; -I_B = 3 \text{ mA}$	-V _{CEsat}	max.	0.5	V
	-V _{BEsat}	max.	0.9	V
$-I_C = 50 \text{ mA}; -I_B = 5 \text{ mA}$	-V _{CEsat}	max.	1.0	V
D.C. current gain				
$-I_C = 1 \text{ mA; } -V_{CE} = 10 \text{ V}$	h_{FE}	min.	20	
$-I_C = 10 \text{ mA}; -V_{CE} = 10 \text{ V}$	h_{FE}	min.	30	
$-I_C = 30 \text{ mA}; -V_{CE} = 10 \text{ V}$	h_{FE}	min.	30	
10 - 00 Hz 1, V CE - 10 V	11FE	max.	200	
		шал.	200	
$-I_C = 50 \text{ mA; } -V_{CE} = 10 \text{ V}$	h_{FE}	min.	20	
		max.	200	
$-I_C = 100 \text{ mA}; -V_{CE} = 10 \text{ V}$	h_{FE}	min.	15	
Base emitter voltage	1.11			
$I_{G} = 100 \text{ mA} \cdot V_{GB} = 10 \text{ V}$	Vanc	may	9	T/

 $V_{BE(on)}$

 f_T

max.

min.

max.

2 V

20 MHz

200 MHz

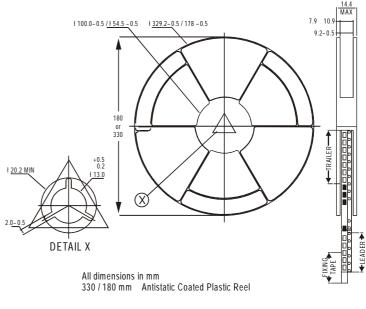
 $I_C = 100 \text{ mA}; V_{CE} = 10 \text{ V}$ Transition frequency $-V_{CE} = 20 \text{ V}; -I_C = 10 \text{ mA}; f = 20 \text{ MHz}$

SOT-23 Formed SMD Package

2.50 +/- 0.10 +/- 0.05 0.62 1.30----+/-0.05 0.62 +/-0.025 1.90 cL 3 - 0.05 - 1.30 +/- 0.05 0.62 l← 0.62 0.08 0.08 MIN MIN PARTING LINE RO.08

2.50 +/-0.10

SOT-23 Package Reel Information Reel specifications for Packing (13"/7" reels)



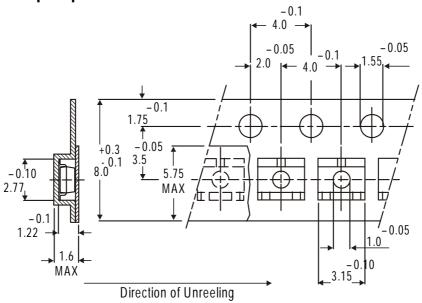
 NOTES:
 8mm Tape
 8mm Tape

 Size of Reel
 330 mm (13")
 180 mm (7")

 No. of Devices
 10,000 Pcs
 3,000 Pcs

- 1. The bandolier of 330 mm reel contains at least 10,000 devices.
- 2. The bandolier of 180 mm reel contains at least 3,000 devices.
- No more than 0.5% missing devices / reel. 50 empty compartments for 330 mm reel.
 15 empty compartments for 180 mm reel.
- Three consecutive empty places might be found provided this gap is followed by 6 consecutive devices.
- The carrier tape (leader) starts with at least 75 empty positions (equivalent to 330 mm). In order to fix the carrier tape a self adhesive tape of 20 to 50 mm is applied. At the end of the bandolier at least 40 empty positions (equivalent to 160 mm) are there.

Tape Specification for SOT-23 Surface Mount Device



0.21

Packing Detail

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net Weight/Qty	Size	Qty	Size	Qty	Gr Wt
SOT-23 T&R	3K/reel	136 gm/3K pcs	3" x 7.5" x 7.5" 9" x 9" x 9"	12.0K 51.0K	17" x 15" x 13.5" 19" x 19" x 19"	192.0K 408.0K	12 kgs 28 kgs
	10K/reel	415 gm/10K pcs	13" x 13" x 0.5"	10.0K	17" x 15" x 13.5"	300.0K	16 kgs

Customer Notes

Component Disposal Instructions

- 1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
 - 2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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